

<b>Document Title</b>	SRS_ADCDriver: Complete Change Documentation 4.3.0 - 4.3.1
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<b>Part of AUTOSAR Standard</b>	Classic Platform
<b>Part of Standard Release</b>	4.3.1

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# 1 SRS\_ADCCDriver

## 1.1 Specification Item SRS\_Adc\_12280

### Trace References:

RS\_BRF\_01872

### Content:

<b>Type:</b>	Valid
<b>Description:</b>	The ADC Driver shall allow the following result access modes for each ADC Channel Group:
<b>Rationale:</b>	Every channel value shall be accessible until the next scan of the channel group is completed.
<b>Applies to:</b>	
<b>Use Case:</b>	–
<b>Supporting Material:</b>	BMW Specification MCAL V1.0a, MAL14.2.0–.

### RfCs affecting this spec item between releases 4.3.0 and 4.3.1:

- RfC #78035: SRS requirements refer to BMW specifications

#### Problem description:

AUTOSAR documents shall not relate to any particular company.

The requirements below reference various BMW specifications and have to be corrected.

Document: SRS\_EEPROMDriver:

---

[SRS\_Eep\_00087] The EEPROM driver shall provide an asynchronous read function

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00088] The EEPROM driver shall provide an asynchronous write function

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00089] The EEPROM driver shall provide an asynchronous erase function

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00090] The EEPROM driver shall provide a synchronous cancel function

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00091] The EEPROM driver shall provide a synchronous function which returns the job processing status

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00096] EEPROM driver static shall be configured

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00092] The EEPROM driver shall only write data if at least one data value of the affected erasable block is different from the data value to be written

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00094] The EEPROM driver shall handle the EEPROM memory segmentation

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00095] The EEPROM driver shall handle only one job at the same time

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_12047] The EEPROM driver shall provide a function that has to be called for job processin

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_12050] The job processing function of the EEPROM driver shall process only as much data as the EEPROM hardware can handle

"Supporting Material: BMW Specification MCAL V1.0a"

Document: SRS\_SPALGeneral:

[SRS\_SPAL\_12056] All driver modules shall allow the static configuration of notification mechanism

"Supporting Material: BMW Specification MCAL V1.0a, [...]"

[SRS\_SPAL\_12057] All driver modules shall implement an interface for initialization

"Supporting Material: BMW Specification MCAL V1.0a, MAL1.0.0"

[SRS\_SPAL\_12063] All driver modules shall only support raw value mode

"Supporting Material: BMW Specification MCAL V1.0a, MAL1.6.0"

[SRS\_SPAL\_12064] All driver modules shall raise an error if the change of the operation mode leads to degradation of running operations

"Supporting Material: BMW Specification MCAL V1.0a, MAL1.5.2"

SRS\_SPAL\_12067 All driver modules shall set their wake-up conditions depending on the selected operation mode

"Supporting Material: BMW Specification MCAL V1.0a, MAL1.5.0"

Document: SRS\_PWMDriver:

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[SRS\_Pwm\_12293] The PWM driver shall allow the static configuration of PWM channel properties

"Supporting Material: BMW Specification MCAL V1.0a, MAL13.1.4 The idle level configuration covers the active phase requirement from Hella.

[SRS\_Pwm\_12295] The PWM driver shall provide a service for setting the duty cycle of a selected channel

"Supporting Material: BMW Specification MCAL V1.0a, MAL13.x"

[SRS\_Pwm\_12297] The PWM driver shall provide a service for setting the period of a selected channel

"Supporting Material: BMW Specification MCAL V1.0a, MAL13.8.0; Kojak movies (in case you do not know Kojak)"

Document: SRS\_BSWGeneral:

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Chapter 5 "General Requirements on Basic Software"

The ECU application experience is taken from the following concrete applications:

Sunroof and power window ECU

Diesel engine ECU

ESP ECU

BMW, DC and VW standard software packages (Standard Core, Standard Software Platform, Standard Software Core) including OSEK OS, communication modules, bootloader, basic diagnostic functions for the domains listed above

Infotainment control ECU

[SRS\_BSW\_00305] Data types naming convention

"Supporting Material: BMW Standard Core Programming Guidelines"

Document: SRS\_COM:

---

[SRS\_Com\_00177] AUTOSAR COM and LargeDataCOM shall support multiple configuration stages

"Use Case: It must be possible to configure the handled bus frames after compile- or build-time, particularly for future concepts running at BMW (reuse an ECU within another vehicle product line with different and incompatible communication layouts)."

Document: SRS\_ADCCDriver:

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[SRS\_Adc\_12280] The ADC Driver shall allow a specific result access modes for each ADC Channel Group

"Supporting Material: BMW Specification MCAL V1.0a, MAL14.2.0."

[SRS\_Adc\_12283] The ADC driver shall mask out information bits from the conversion result not belonging to the ADC value

"Supporting Material: BMW Specification MCAL V1.0a, MAL14.4.1."

[SRS\_Adc\_12819] The ADC Driver shall provide a synchronous service for reading the last valid conversion results of the selected channel group

"Supporting Material: BMW Specification MCAL V1.0a."

[SRS\_Adc\_12291] The ADC Driver shall provide a service for querying the status of an ADC Channel Group

"Supporting Material: BMW Specification MCAL V1.0a."

[SRS\_Adc\_12288] Based on the channel group configuration the ADC driver shall be able to handle the buffers of stream jobs

"Supporting Material: BMW Specification MCAL V1.0a."

[SRS\_Adc\_12292] If the ADC provides signed values, the ADC driver shall put the sign bit into the MSB of the return value

"Supporting Material: BMW Specification MCAL V1.0a, MAL14.4.2."

Document: SRS\_DIODriver:

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[SRS\_Dio\_12003] The DIO Driver shall provide a service that writes a data word to

the assigned DIO port

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.3.0"

[SRS\_Dio\_12004] The DIO Driver shall provide a service that writes a selectable number of adjoining bits to an assigned part of a DIO port

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.3.1"

[SRS\_Dio\_12005] The DIO Driver shall provide a service for write access to single DIO channels

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.4.0"

[SRS\_Dio\_12006] The DIO Driver shall provide a service for reading a data word from the assigned DIO port

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.4.0"

[SRS\_Dio\_12007] The DIO Driver shall provide a service for reading a selectable number of adjoining bits from an assigned part of a DIO port

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.4.1"

[SRS\_Dio\_12008] The DIO Driver shall provide a service for reading one bit of an assigned DIO channel

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.6.0"

**Agreed solution:**

CP\_SRS\_ADCCDriver

SRS\_Adc\_12280, SRS\_Adc\_12283, SRS\_Adc\_12819, SRS\_Adc\_12291,  
SRS\_Adc\_12288, SRS\_Adc\_12292

Change from:

Supporting Material: BMW Specification MCAL V1.0a, [...]

to:

Supporting Material: –

====

CP\_SRS\_BSWGeneral

replace "BMW Standard Core Programming Guidelines" by "–" in SRS\_BSW\_00305

====

CP\_SRS\_COM

Update [SRS\_Com\_00177] AUTOSAR COM and LargeDataCOM shall support multiple configuration stages

"Use Case: It must be possible to configure the handled bus frames after compile- or build-time, for example when reusing an ECU within another vehicle product line

with different and incompatible communication layouts."

====

CP\_SRS\_DIODriver

SRS\_Dio\_12003, SRS\_Dio\_12004, SRS\_Dio\_12005, SRS\_Dio\_12006,  
SRS\_Dio\_12007, SRS\_Dio\_12008

Change from:

Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.x.x

to:

Supporting Material: –

====

CP\_SRS\_EEPROMDriver

SRS\_Eep\_00087, SRS\_Eep\_00088, SRS\_Eep\_00089, SRS\_Eep\_00090,  
SRS\_Eep\_00091, SRS\_Eep\_00092, SRS\_Eep\_00094, SRS\_Eep\_00095,  
SRS\_Eep\_00096, SRS\_Eep\_12047, SRS\_Eep\_12050

Change from:

Supporting Material: BMW Specification MCAL V1.0a

to:

Supporting Material: –

===

CP\_SRS\_PWMDriver

SRS\_Pwm\_12293:

Change from:

Supporting Material: BMW Specification MCAL V1.0a, MAL13.1.4. The idle level configuration covers the active phase requirement from Hella.

Change to:

Supporting Material: –

SRS\_Pwm\_12295:

Change from:

Supporting Material: BMW Specification MCAL V1.0a, MAL13.x

Change to:



Supporting Material: –

SRS\_Pwm\_12297:

Change from:

Supporting Material: BMW Specification MCAL V1.0a, MAL13.8.0. Kojak movies (in case you do not know Kojak)

Change to:

Supporting Material: –

===

CP\_SRS\_SPALGeneral

SRS\_SPAL\_12056, SRS\_SPAL\_12057, SRS\_SPAL\_12063, SRS\_SPAL\_12064,  
SRS\_SPAL\_12067

Change from:

Supporting Material: BMW Specification MCAL V1.0a, ...

to:

Supporting Material: –

–Last change on issue 78035 comment 14–

**BW-C-Level:**

Application	Specification	Bus
1	1	1

## 1.2 Specification Item SRS\_Adc\_12283

**Trace References:**

RS\_BRF\_01872

**Content:**

<b>Type:</b>	Valid
<b>Description:</b>	The ADC driver shall mask out information bits from the conversion result not belonging to the ADC value.
<b>Rationale:</b>	Information bits are microcontroller specific.
<b>Applies to:</b>	
<b>Use Case:</b>	Some $\mu$ Cs are saving information bits in the result register (e.g. the channel number). These bits shall be masked out.
<b>Supporting Material:</b>	BMW Specification MCAL V1.0a, MAL14.4.1–.

**RfCs affecting this spec item between releases 4.3.0 and 4.3.1:**

- RfC #78035: SRS requirements refer to BMW specifications

**Problem description:**

AUTOSAR documents shall not relate to any particular company.

The requirements below reference various BMW specifications and have to be corrected.

Document: SRS\_EEPROMDriver:

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[SRS\_Eep\_00087] The EEPROM driver shall provide an asynchronous read function

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00088] The EEPROM driver shall provide an asynchronous write function

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00089] The EEPROM driver shall provide an asynchronous erase function

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00090] The EEPROM driver shall provide a synchronous cancel function

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00091] The EEPROM driver shall provide a synchronous function which returns the job processing status

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00096] EEPROM driver static shall be configured

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00092] The EEPROM driver shall only write data if at least one data value of the affected erasable block is different from the data value to be written

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00094] The EEPROM driver shall handle the EEPROM memory segmentation

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00095] The EEPROM driver shall handle only one job at the same time

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_12047] The EEPROM driver shall provide a function that has to be called for job processin

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_12050] The job processing function of the EEPROM driver shall process only as much data as the EEPROM hardware can handle

"Supporting Material: BMW Specification MCAL V1.0a"

Document: SRS\_SPALGeneral:

---

[SRS\_SPAL\_12056] All driver modules shall allow the static configuration of notification mechanism

"Supporting Material: BMW Specification MCAL V1.0a, [...]"

[SRS\_SPAL\_12057] All driver modules shall implement an interface for initialization

"Supporting Material: BMW Specification MCAL V1.0a, MAL1.0.0"

[SRS\_SPAL\_12063] All driver modules shall only support raw value mode

"Supporting Material: BMW Specification MCAL V1.0a, MAL1.6.0"

[SRS\_SPAL\_12064] All driver modules shall raise an error if the change of the operation mode leads to degradation of running operations

"Supporting Material: BMW Specification MCAL V1.0a, MAL1.5.2"

SRS\_SPAL\_12067 All driver modules shall set their wake-up conditions depending on the selected operation mode

"Supporting Material: BMW Specification MCAL V1.0a, MAL1.5.0"

Document: SRS\_PWMDriver:

---

[SRS\_Pwm\_12293] The PWM driver shall allow the static configuration of PWM channel properties

"Supporting Material: BMW Specification MCAL V1.0a, MAL13.1.4 The idle level configuration covers the active phase requirement from Hella.

[SRS\_Pwm\_12295] The PWM driver shall provide a service for setting the duty cycle of a selected channel

"Supporting Material: BMW Specification MCAL V1.0a, MAL13.x"

[SRS\_Pwm\_12297] The PWM driver shall provide a service for setting the period of a selected channel

"Supporting Material: BMW Specification MCAL V1.0a, MAL13.8.0; Kojak movies (in case you do not know Kojak)"

Document: SRS\_BSWGeneral:

---

Chapter 5 "General Requirements on Basic Software"

The ECU application experience is taken from the following concrete applications:

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Diesel engine ECU

ESP ECU

BMW, DC and VW standard software packages (Standard Core, Standard Software Platform, Standard Software Core) including OSEK OS, communication modules, bootloader, basic diagnostic functions for the domains listed above

Infotainment control ECU

[SRS\_BSW\_00305] Data types naming convention

"Supporting Material: BMW Standard Core Programming Guidelines"

Document: SRS\_COM:

---

[SRS\_Com\_00177] AUTOSAR COM and LargeDataCOM shall support multiple configuration stages

"Use Case: It must be possible to configure the handled bus frames after compile- or build-time, particularly for future concepts running at BMW (reuse an ECU within another vehicle product line with different and incompatible communication layouts)."

Document: SRS\_ADCCDriver:

---

[SRS\_Adc\_12280] The ADC Driver shall allow a specific result access modes for each ADC Channel Group

"Supporting Material: BMW Specification MCAL V1.0a, MAL14.2.0."

[SRS\_Adc\_12283] The ADC driver shall mask out information bits from the conversion result not belonging to the ADC value

"Supporting Material: BMW Specification MCAL V1.0a, MAL14.4.1."

[SRS\_Adc\_12819] The ADC Driver shall provide a synchronous service for reading the last valid conversion results of the selected channel group

"Supporting Material: BMW Specification MCAL V1.0a."

[SRS\_Adc\_12291] The ADC Driver shall provide a service for querying the status of an ADC Channel Group

"Supporting Material: BMW Specification MCAL V1.0a."

[SRS\_Adc\_12288] Based on the channel group configuration the ADC driver shall be able to handle the buffers of stream jobs

"Supporting Material: BMW Specification MCAL V1.0a."

[SRS\_Adc\_12292] If the ADC provides signed values, the ADC driver shall put the sign bit into the MSB of the return value

"Supporting Material: BMW Specification MCAL V1.0a, MAL14.4.2."

Document: SRS\_DIODriver:

---

[SRS\_Dio\_12003] The DIO Driver shall provide a service that writes a data word to the assigned DIO port

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.3.0"

[SRS\_Dio\_12004] The DIO Driver shall provide a service that writes a selectable number of adjoining bits to an assigned part of a DIO port

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.3.1"

[SRS\_Dio\_12005] The DIO Driver shall provide a service for write access to single DIO channels

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.4.0"

[SRS\_Dio\_12006] The DIO Driver shall provide a service for reading a data word from the assigned DIO port

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.4.0"

[SRS\_Dio\_12007] The DIO Driver shall provide a service for reading a selectable number of adjoining bits from an assigned part of a DIO port

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.4.1"

[SRS\_Dio\_12008] The DIO Driver shall provide a service for reading one bit of an assigned DIO channel

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.6.0"

**Agreed solution:**

CP\_SRS\_ADCCDriver

SRS\_Adc\_12280, SRS\_Adc\_12283, SRS\_Adc\_12819, SRS\_Adc\_12291,  
SRS\_Adc\_12288, SRS\_Adc\_12292

Change from:

Supporting Material: BMW Specification MCAL V1.0a, [...]

to:

Supporting Material: –

====

CP\_SRS\_BSWGeneral

replace "BMW Standard Core Programming Guidelines" by "–" in SRS\_BSW\_00305

===

CP\_SRS\_COM

Update [SRS\_Com\_00177] AUTOSAR COM and LargeDataCOM shall support multiple configuration stages

"Use Case: It must be possible to configure the handled bus frames after compile- or build-time, for example when reusing an ECU within another vehicle product line with different and incompatible communication layouts."

====

CP\_SRS\_DIODriver

SRS\_Dio\_12003, SRS\_Dio\_12004, SRS\_Dio\_12005, SRS\_Dio\_12006,  
SRS\_Dio\_12007, SRS\_Dio\_12008

Change from:

Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.x.x

to:

Supporting Material: –

====

CP\_SRS\_EEPROMDriver

SRS\_Eep\_00087, SRS\_Eep\_00088, SRS\_Eep\_00089, SRS\_Eep\_00090,

SRS\_Eep\_00091, SRS\_Eep\_00092, SRS\_Eep\_00094, SRS\_Eep\_00095,  
SRS\_Eep\_00096, SRS\_Eep\_12047, SRS\_Eep\_12050

Change from:

Supporting Material: BMW Specification MCAL V1.0a

to:

Supporting Material: –

===

CP\_SRS\_PWMDriver

SRS\_Pwm\_12293:

Change from:

Supporting Material: BMW Specification MCAL V1.0a, MAL13.1.4. The idle level configuration covers the active phase requirement from Hella.

Change to:

Supporting Material: –

SRS\_Pwm\_12295:

Change from:

Supporting Material: BMW Specification MCAL V1.0a, MAL13.x

Change to:

Supporting Material: –

SRS\_Pwm\_12297:

Change from:

Supporting Material: BMW Specification MCAL V1.0a, MAL13.8.0. Kojak movies (in case you do not know Kojak)

Change to:

Supporting Material: –

===

CP\_SRS\_SPALGeneral

SRS\_SPAL\_12056, SRS\_SPAL\_12057, SRS\_SPAL\_12063, SRS\_SPAL\_12064,  
SRS\_SPAL\_12067

Change from:

Supporting Material: BMW Specification MCAL V1.0a, ...

to:

Supporting Material: –

–Last change on issue 78035 comment 14–

**BW-C-Level:**

Application	Specification	Bus
1	1	1

### 1.3 Specification Item SRS\_Adc\_12288

**Trace References:**

RS\_BRF\_01872

**Content:**

<b>Type:</b>	Valid
<b>Description:</b>	Based on the channel group configuration the ADC driver shall be able to handle the buffers of stream jobs in two different way:
<b>Rationale:</b>	Allow cyclic streams of ADC conversion that run automatically.
<b>Applies to:</b>	
<b>Use Case:</b>	Electric motor control.
<b>Supporting Material:</b>	BMW Specification MCAL V1.0a. –

**RfCs affecting this spec item between releases 4.3.0 and 4.3.1:**

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[SRS\_Eep\_00089] The EEPROM driver shall provide an asynchronous erase function

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00090] The EEPROM driver shall provide a synchronous cancel function

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[SRS\_Eep\_00091] The EEPROM driver shall provide a synchronous function which returns the job processing status

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[SRS\_Eep\_00096] EEPROM driver static shall be configured

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[SRS\_SPAL\_12063] All driver modules shall only support raw value mode

"Supporting Material: BMW Specification MCAL V1.0a, MAL1.6.0"

[SRS\_SPAL\_12064] All driver modules shall raise an error if the change of the operation mode leads to degradation of running operations

"Supporting Material: BMW Specification MCAL V1.0a, MAL1.5.2"

SRS\_SPAL\_12067 All driver modules shall set their wake-up conditions depending on the selected operation mode

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Document: SRS\_PWMDriver:

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"Supporting Material: BMW Specification MCAL V1.0a."

[SRS\_Adc\_12291] The ADC Driver shall provide a service for querying the status of an ADC Channel Group

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[SRS\_Adc\_12288] Based on the channel group configuration the ADC driver shall be able to handle the buffers of stream jobs

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"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.3.1"

[SRS\_Dio\_12005] The DIO Driver shall provide a service for write access to single DIO channels

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.4.0"

[SRS\_Dio\_12006] The DIO Driver shall provide a service for reading a data word from the assigned DIO port

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.4.0"

[SRS\_Dio\_12007] The DIO Driver shall provide a service for reading a selectable number of adjoining bits from an assigned part of a DIO port

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[SRS\_Dio\_12008] The DIO Driver shall provide a service for reading one bit of an assigned DIO channel

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.6.0"

### **Agreed solution:**

CP\_SRS\_ADCCDriver

SRS\_Adc\_12280, SRS\_Adc\_12283, SRS\_Adc\_12819, SRS\_Adc\_12291,  
SRS\_Adc\_12288, SRS\_Adc\_12292

Change from:

Supporting Material: BMW Specification MCAL V1.0a, [...]

to:

Supporting Material: –

====

CP\_SRS\_BSWGeneral

replace "BMW Standard Core Programming Guidelines" by "–" in SRS\_BSW\_00305

===

**CP\_SRS\_COM**

Update [SRS\_Com\_00177] AUTOSAR COM and LargeDataCOM shall support multiple configuration stages

"Use Case: It must be possible to configure the handled bus frames after compile- or build-time, for example when reusing an ECU within another vehicle product line with different and incompatible communication layouts."

====

**CP\_SRS\_DIODriver**

SRS\_Dio\_12003, SRS\_Dio\_12004, SRS\_Dio\_12005, SRS\_Dio\_12006,  
SRS\_Dio\_12007, SRS\_Dio\_12008

Change from:

Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.x.x

to:

Supporting Material: –

====

**CP\_SRS\_EEPROMDriver**

SRS\_Eep\_00087, SRS\_Eep\_00088, SRS\_Eep\_00089, SRS\_Eep\_00090,  
SRS\_Eep\_00091, SRS\_Eep\_00092, SRS\_Eep\_00094, SRS\_Eep\_00095,  
SRS\_Eep\_00096, SRS\_Eep\_12047, SRS\_Eep\_12050

Change from:

Supporting Material: BMW Specification MCAL V1.0a

to:

Supporting Material: –

===

**CP\_SRS\_PWMDriver**

SRS\_Pwm\_12293:

Change from:

Supporting Material: BMW Specification MCAL V1.0a, MAL13.1.4. The idle level configuration covers the active phase requirement from Hella.

Change to:

Supporting Material: –

SRS\_Pwm\_12295:

Change from:

Supporting Material: BMW Specification MCAL V1.0a, MAL13.x

Change to:

Supporting Material: –

SRS\_Pwm\_12297:

Change from:

Supporting Material: BMW Specification MCAL V1.0a, MAL13.8.0. Kojak movies (in case you do not know Kojak)

Change to:

Supporting Material: –

===

CP\_SRS\_SPALGeneral

SRS\_SPAL\_12056, SRS\_SPAL\_12057, SRS\_SPAL\_12063, SRS\_SPAL\_12064,  
SRS\_SPAL\_12067

Change from:

Supporting Material: BMW Specification MCAL V1.0a, ...

to:

Supporting Material: –

–Last change on issue 78035 comment 14–

#### BW-C-Level:

Application	Specification	Bus
1	1	1

## 1.4 Specification Item SRS\_Adc\_12291

### Trace References:

RS\_BRF\_01872

### Content:

Type:	Valid
-------	-------

<b>Description:</b>	The ADC Driver shall provide a service for querying the status of an ADC Channel Group.
<b>Rationale:</b>	Before reading a value it might be necessary to query the status of the ADC.
<b>Applies to:</b>	
<b>Use Case:</b>	The status can be used for access synchronization.
<b>Supporting Material:</b>	BMW Specification MCAL V1.0a.—

### RfCs affecting this spec item between releases 4.3.0 and 4.3.1:

- RfC #78035: SRS requirements refer to BMW specifications

#### Problem description:

AUTOSAR documents shall not relate to any particular company.

The requirements below reference various BMW specifications and have to be corrected.

Document: SRS\_EEPROMDriver:

---

[SRS\_Eep\_00087] The EEPROM driver shall provide an asynchronous read function

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00088] The EEPROM driver shall provide an asynchronous write function

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00089] The EEPROM driver shall provide an asynchronous erase function

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00090] The EEPROM driver shall provide a synchronous cancel function

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00091] The EEPROM driver shall provide a synchronous function which returns the job processing status

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00096] EEPROM driver static shall be configured

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00092] The EEPROM driver shall only write data if at least one data value of the affected erasable block is different from the data value to be written  
"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00094] The EEPROM driver shall handle the EEPROM memory segmentation  
"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00095] The EEPROM driver shall handle only one job at the same time  
"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_12047] The EEPROM driver shall provide a function that has to be called for job processing  
"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_12050] The job processing function of the EEPROM driver shall process only as much data as the EEPROM hardware can handle  
"Supporting Material: BMW Specification MCAL V1.0a"

Document: SRS\_SPALGeneral:

---

[SRS\_SPAL\_12056] All driver modules shall allow the static configuration of notification mechanism  
"Supporting Material: BMW Specification MCAL V1.0a, [...]"

[SRS\_SPAL\_12057] All driver modules shall implement an interface for initialization  
"Supporting Material: BMW Specification MCAL V1.0a, MAL1.0.0"

[SRS\_SPAL\_12063] All driver modules shall only support raw value mode  
"Supporting Material: BMW Specification MCAL V1.0a, MAL1.6.0"

[SRS\_SPAL\_12064] All driver modules shall raise an error if the change of the operation mode leads to degradation of running operations  
"Supporting Material: BMW Specification MCAL V1.0a, MAL1.5.2"

SRS\_SPAL\_12067 All driver modules shall set their wake-up conditions depending on the selected operation mode  
"Supporting Material: BMW Specification MCAL V1.0a, MAL1.5.0"



Document: SRS\_PWMDriver:

---

[SRS\_Pwm\_12293] The PWM driver shall allow the static configuration of PWM channel properties

"Supporting Material: BMW Specification MCAL V1.0a, MAL13.1.4 The idle level configuration covers the active phase requirement from Hella.

[SRS\_Pwm\_12295] The PWM driver shall provide a service for setting the duty cycle of a selected channel

"Supporting Material: BMW Specification MCAL V1.0a, MAL13.x"

[SRS\_Pwm\_12297] The PWM driver shall provide a service for setting the period of a selected channel

"Supporting Material: BMW Specification MCAL V1.0a, MAL13.8.0; Kojak movies (in case you do not know Kojak)"

Document: SRS\_BSWGeneral:

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Chapter 5 "General Requirements on Basic Software"

The ECU application experience is taken from the following concrete applications:

Sunroof and power window ECU

Diesel engine ECU

ESP ECU

BMW, DC and VW standard software packages (Standard Core, Standard Software Platform, Standard Software Core) including OSEK OS, communication modules, bootloader, basic diagnostic functions for the domains listed above

Infotainment control ECU

[SRS\_BSW\_00305] Data types naming convention

"Supporting Material: BMW Standard Core Programming Guidelines"

Document: SRS\_COM:

---

[SRS\_Com\_00177] AUTOSAR COM and LargeDataCOM shall support multiple configuration stages

"Use Case: It must be possible to configure the handled bus frames after compile- or build-time, particularly for future concepts running at BMW (reuse an ECU within another vehicle product line with different

and incompatible communication layouts)."

Document: SRS\_ADCCDriver:

---

[SRS\_Adc\_12280] The ADC Driver shall allow a specific result access modes for each ADC Channel Group

"Supporting Material: BMW Specification MCAL V1.0a, MAL14.2.0."

[SRS\_Adc\_12283] The ADC driver shall mask out information bits from the conversion result not belonging to the ADC value

"Supporting Material: BMW Specification MCAL V1.0a, MAL14.4.1."

[SRS\_Adc\_12819] The ADC Driver shall provide a synchronous service for reading the last valid conversion results of the selected channel group

"Supporting Material: BMW Specification MCAL V1.0a."

[SRS\_Adc\_12291] The ADC Driver shall provide a service for querying the status of an ADC Channel Group

"Supporting Material: BMW Specification MCAL V1.0a."

[SRS\_Adc\_12288] Based on the channel group configuration the ADC driver shall be able to handle the buffers of stream jobs

"Supporting Material: BMW Specification MCAL V1.0a."

[SRS\_Adc\_12292] If the ADC provides signed values, the ADC driver shall put the sign bit into the MSB of the return value

"Supporting Material: BMW Specification MCAL V1.0a, MAL14.4.2."

Document: SRS\_DIODriver:

---

[SRS\_Dio\_12003] The DIO Driver shall provide a service that writes a data word to the assigned DIO port

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.3.0"

[SRS\_Dio\_12004] The DIO Driver shall provide a service that writes a selectable number of adjoining bits to an assigned part of a DIO port

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.3.1"

[SRS\_Dio\_12005] The DIO Driver shall provide a service for write access to single DIO channels

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.4.0"

[SRS\_Dio\_12006] The DIO Driver shall provide a service for reading a data word from the assigned DIO port

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.4.0"

[SRS\_Dio\_12007] The DIO Driver shall provide a service for reading a selectable number of adjoining bits from an assigned part of a DIO port

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.4.1"

[SRS\_Dio\_12008] The DIO Driver shall provide a service for reading one bit of an assigned DIO channel

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.6.0"

**Agreed solution:**

CP\_SRS\_ADCCDriver

SRS\_Adc\_12280, SRS\_Adc\_12283, SRS\_Adc\_12819, SRS\_Adc\_12291,  
SRS\_Adc\_12288, SRS\_Adc\_12292

Change from:

Supporting Material: BMW Specification MCAL V1.0a, [...]

to:

Supporting Material: –

====

CP\_SRS\_BSWGeneral

replace "BMW Standard Core Programming Guidelines" by "–" in SRS\_BSW\_00305

===

CP\_SRS\_COM

Update [SRS\_Com\_00177] AUTOSAR COM and LargeDataCOM shall support multiple configuration stages

"Use Case: It must be possible to configure the handled bus frames after compile- or build-time, for example when reusing an ECU within another vehicle product line with different and incompatible communication layouts."

====

CP\_SRS\_DIODriver

SRS\_Dio\_12003, SRS\_Dio\_12004, SRS\_Dio\_12005, SRS\_Dio\_12006,  
SRS\_Dio\_12007, SRS\_Dio\_12008

Change from:

Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.x.x

to:

Supporting Material: –

====

CP\_SRS\_EEPROMDriver

SRS\_Eep\_00087, SRS\_Eep\_00088, SRS\_Eep\_00089, SRS\_Eep\_00090,  
SRS\_Eep\_00091, SRS\_Eep\_00092, SRS\_Eep\_00094, SRS\_Eep\_00095,  
SRS\_Eep\_00096, SRS\_Eep\_12047, SRS\_Eep\_12050

Change from:

Supporting Material: BMW Specification MCAL V1.0a

to:

Supporting Material: –

===

CP\_SRS\_PWMDriver

SRS\_Pwm\_12293:

Change from:

Supporting Material: BMW Specification MCAL V1.0a, MAL13.1.4. The idle level configuration covers the active phase requirement from Hella.

Change to:

Supporting Material: –

SRS\_Pwm\_12295:

Change from:

Supporting Material: BMW Specification MCAL V1.0a, MAL13.x

Change to:

Supporting Material: –

SRS\_Pwm\_12297:

Change from:

Supporting Material: BMW Specification MCAL V1.0a, MAL13.8.0. Kojak movies (in case you do not know Kojak)

Change to:

Supporting Material: –

===

CP\_SRS\_SPALGeneral

SRS\_SPAL\_12056, SRS\_SPAL\_12057, SRS\_SPAL\_12063, SRS\_SPAL\_12064,  
SRS\_SPAL\_12067

Change from:

Supporting Material: BMW Specification MCAL V1.0a, ...

to:

Supporting Material: –

–Last change on issue 78035 comment 14–

**BW-C-Level:**

Application	Specification	Bus
1	1	1

## 1.5 Specification Item SRS\_Adc\_12292

**Trace References:**

RS\_BRF\_01872

**Content:**

<b>Type:</b>	Valid
<b>Description:</b>	If the ADC provides signed values, the ADC driver shall put the sign bit into the MSB of the return value.
<b>Rationale:</b>	Allow mapping of ADC return values to standard AUTOSAR integer data types.
<b>Applies to:</b>	
<b>Use Case:</b>	Some microcontrollers provide signed ADC values.
<b>Supporting Material:</b>	BMW Specification MCAL V1.0a, MAL14.4.2.–

**RfCs affecting this spec item between releases 4.3.0 and 4.3.1:**

- RfC #78035: SRS requirements refer to BMW specifications

**Problem description:**

AUTOSAR documents shall not relate to any particular company.

The requirements below reference various BMW specifications and have to be corrected.

Document: SRS\_EEPROMDriver:

---

[SRS\_Eep\_00087] The EEPROM driver shall provide an asynchronous read function

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00088] The EEPROM driver shall provide an asynchronous write function

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00089] The EEPROM driver shall provide an asynchronous erase function

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00090] The EEPROM driver shall provide a synchronous cancel function

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00091] The EEPROM driver shall provide a synchronous function which returns the job processing status

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00096] EEPROM driver static shall be configured

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00092] The EEPROM driver shall only write data if at least one data value of the affected erasable block is different from the data value to be written

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00094] The EEPROM driver shall handle the EEPROM memory segmentation

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00095] The EEPROM driver shall handle only one job at the same time

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_12047] The EEPROM driver shall provide a function that has to be called for job processing

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_12050] The job processing function of the EEPROM driver shall process only as much data as the EEPROM hardware can handle

"Supporting Material: BMW Specification MCAL V1.0a"

Document: SRS\_SPALGeneral:

---

[SRS\_SPAL\_12056] All driver modules shall allow the static configuration of notification mechanism

"Supporting Material: BMW Specification MCAL V1.0a, [...]"

[SRS\_SPAL\_12057] All driver modules shall implement an interface for initialization

"Supporting Material: BMW Specification MCAL V1.0a, MAL1.0.0"

[SRS\_SPAL\_12063] All driver modules shall only support raw value mode

"Supporting Material: BMW Specification MCAL V1.0a, MAL1.6.0"

[SRS\_SPAL\_12064] All driver modules shall raise an error if the change of the operation mode leads to degradation of running operations

"Supporting Material: BMW Specification MCAL V1.0a, MAL1.5.2"

SRS\_SPAL\_12067 All driver modules shall set their wake-up conditions depending on the selected operation mode

"Supporting Material: BMW Specification MCAL V1.0a, MAL1.5.0"

Document: SRS\_PWMDriver:

---

[SRS\_Pwm\_12293] The PWM driver shall allow the static configuration of PWM channel properties

"Supporting Material: BMW Specification MCAL V1.0a, MAL13.1.4 The idle level configuration covers the active phase requirement from Hella.

[SRS\_Pwm\_12295] The PWM driver shall provide a service for setting the duty cycle of a selected channel

"Supporting Material: BMW Specification MCAL V1.0a, MAL13.x"

[SRS\_Pwm\_12297] The PWM driver shall provide a service for setting the period of a selected channel

"Supporting Material: BMW Specification MCAL V1.0a, MAL13.8.0; Kojak movies (in case you do not know Kojak)"

Document: SRS\_BSWGeneral:

---

## Chapter 5 "General Requirements on Basic Software"

The ECU application experience is taken from the following concrete applications:

Sunroof and power window ECU

Diesel engine ECU

ESP ECU

BMW, DC and VW standard software packages (Standard Core, Standard Software Platform, Standard Software Core) including OSEK OS, communication modules, bootloader, basic diagnostic functions for the domains listed above

Infotainment control ECU

[SRS\_BSW\_00305] Data types naming convention

"Supporting Material: BMW Standard Core Programming Guidelines"

Document: SRS\_COM:

---

[SRS\_Com\_00177] AUTOSAR COM and LargeDataCOM shall support multiple configuration stages

"Use Case: It must be possible to configure the handled bus frames after compile- or build-time, particularly for future concepts running at BMW (reuse an ECU within another vehicle product line with different and incompatible communication layouts)."

Document: SRS\_ADCCDriver:

---

[SRS\_Adc\_12280] The ADC Driver shall allow a specific result access modes for each ADC Channel Group

"Supporting Material: BMW Specification MCAL V1.0a, MAL14.2.0."

[SRS\_Adc\_12283] The ADC driver shall mask out information bits from the conversion result not belonging to the ADC value

"Supporting Material: BMW Specification MCAL V1.0a, MAL14.4.1."

[SRS\_Adc\_12819] The ADC Driver shall provide a synchronous service for reading the last valid conversion results of the selected channel group

"Supporting Material: BMW Specification MCAL V1.0a."

[SRS\_Adc\_12291] The ADC Driver shall provide a service for querying the status of an ADC Channel Group



"Supporting Material: BMW Specification MCAL V1.0a."

[SRS\_Adc\_12288] Based on the channel group configuration the ADC driver shall be able to handle the buffers of stream jobs

"Supporting Material: BMW Specification MCAL V1.0a."

[SRS\_Adc\_12292] If the ADC provides signed values, the ADC driver shall put the sign bit into the MSB of the return value

"Supporting Material: BMW Specification MCAL V1.0a, MAL14.4.2."

Document: SRS\_DIODriver:

---

[SRS\_Dio\_12003] The DIO Driver shall provide a service that writes a data word to the assigned DIO port

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.3.0"

[SRS\_Dio\_12004] The DIO Driver shall provide a service that writes a selectable number of adjoining bits to an assigned part of a DIO port

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.3.1"

[SRS\_Dio\_12005] The DIO Driver shall provide a service for write access to single DIO channels

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.4.0"

[SRS\_Dio\_12006] The DIO Driver shall provide a service for reading a data word from the assigned DIO port

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.4.0"

[SRS\_Dio\_12007] The DIO Driver shall provide a service for reading a selectable number of adjoining bits from an assigned part of a DIO port

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.4.1"

[SRS\_Dio\_12008] The DIO Driver shall provide a service for reading one bit of an assigned DIO channel

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.6.0"

**Agreed solution:**

CP\_SRS\_ADCCDriver

SRS\_Adc\_12280, SRS\_Adc\_12283, SRS\_Adc\_12819, SRS\_Adc\_12291,  
SRS\_Adc\_12288, SRS\_Adc\_12292

Change from:

Supporting Material: BMW Specification MCAL V1.0a, [...]

to:

Supporting Material: –

====

CP\_SRS\_BSWGeneral

replace "BMW Standard Core Programming Guidelines" by "–" in SRS\_BSW\_00305

===

CP\_SRS\_COM

Update [SRS\_Com\_00177] AUTOSAR COM and LargeDataCOM shall support multiple configuration stages

"Use Case: It must be possible to configure the handled bus frames after compile- or build-time, for example when reusing an ECU within another vehicle product line with different and incompatible communication layouts."

====

CP\_SRS\_DIODriver

SRS\_Dio\_12003, SRS\_Dio\_12004, SRS\_Dio\_12005, SRS\_Dio\_12006,  
SRS\_Dio\_12007, SRS\_Dio\_12008

Change from:

Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.x.x

to:

Supporting Material: –

====

CP\_SRS\_EEPROMDriver

SRS\_Eep\_00087, SRS\_Eep\_00088, SRS\_Eep\_00089, SRS\_Eep\_00090,  
SRS\_Eep\_00091, SRS\_Eep\_00092, SRS\_Eep\_00094, SRS\_Eep\_00095,  
SRS\_Eep\_00096, SRS\_Eep\_12047, SRS\_Eep\_12050

Change from:

Supporting Material: BMW Specification MCAL V1.0a

to:

Supporting Material: –

===

CP\_SRS\_PWMDriver

SRS\_Pwm\_12293:

Change from:

Supporting Material: BMW Specification MCAL V1.0a, MAL13.1.4. The idle level configuration covers the active phase requirement from Hella.

Change to:

Supporting Material: –

SRS\_Pwm\_12295:

Change from:

Supporting Material: BMW Specification MCAL V1.0a, MAL13.x

Change to:

Supporting Material: –

SRS\_Pwm\_12297:

Change from:

Supporting Material: BMW Specification MCAL V1.0a, MAL13.8.0. Kojak movies (in case you do not know Kojak)

Change to:

Supporting Material: –

===

CP\_SRS\_SPALGeneral

SRS\_SPAL\_12056, SRS\_SPAL\_12057, SRS\_SPAL\_12063, SRS\_SPAL\_12064, SRS\_SPAL\_12067

Change from:

Supporting Material: BMW Specification MCAL V1.0a, ...

to:

Supporting Material: –

–Last change on issue 78035 comment 14–

**BW-C-Level:**

Application	Specification	Bus
1	1	1

## 1.6 Specification Item SRS\_Adc\_12819

### Trace References:

RS\_BRF\_01872

### Content:

<b>Type:</b>	Valid
<b>Description:</b>	The ADC Driver shall provide a synchronous service for reading the last valid conversion results of the selected channel group (passed as parameter).
<b>Rationale:</b>	Provide access to the most recent converted values of a channel group.
<b>Applies to:</b>	
<b>Use Case:</b>	This is the standard functionality for reading ADC values.
<b>Supporting Material:</b>	BMW Specification MCAL V1.0a <del>+</del> .

### RfCs affecting this spec item between releases 4.3.0 and 4.3.1:

- RfC #78035: SRS requirements refer to BMW specifications

#### Problem description:

AUTOSAR documents shall not relate to any particular company.

The requirements below reference various BMW specifications and have to be corrected.

Document: SRS\_EEPROMDriver:

---

[SRS\_Eep\_00087] The EEPROM driver shall provide an asynchronous read function

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00088] The EEPROM driver shall provide an asynchronous write function

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00089] The EEPROM driver shall provide an asynchronous erase function

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00090] The EEPROM driver shall provide a synchronous cancel function

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00091] The EEPROM driver shall provide a synchronous function which returns the job processing status

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00096] EEPROM driver static shall be configured

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00092] The EEPROM driver shall only write data if at least one data value of the affected erasable block is different from the data value to be written

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00094] The EEPROM driver shall handle the EEPROM memory segmentation

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_00095] The EEPROM driver shall handle only one job at the same time

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_12047] The EEPROM driver shall provide a function that has to be called for job processin

"Supporting Material: BMW Specification MCAL V1.0a"

[SRS\_Eep\_12050] The job processing function of the EEPROM driver shall process only as much data as the EEPROM hardware can handle

"Supporting Material: BMW Specification MCAL V1.0a"

Document: SRS\_SPALGeneral:

---

[SRS\_SPAL\_12056] All driver modules shall allow the static configuration of notification mechanism

"Supporting Material: BMW Specification MCAL V1.0a, [...]"

[SRS\_SPAL\_12057] All driver modules shall implement an interface for initialization

"Supporting Material: BMW Specification MCAL V1.0a, MAL1.0.0"

[SRS\_SPAL\_12063] All driver modules shall only support raw value mode

"Supporting Material: BMW Specification MCAL V1.0a, MAL1.6.0"

[SRS\_SPAL\_12064] All driver modules shall raise an error if the change of

the operation mode leads to degradation of running operations

"Supporting Material: BMW Specification MCAL V1.0a, MAL1.5.2"

SRS\_SPAL\_12067 All driver modules shall set their wake-up conditions depending on the selected operation mode

"Supporting Material: BMW Specification MCAL V1.0a, MAL1.5.0"

Document: SRS\_PWMDriver:

---

[SRS\_Pwm\_12293] The PWM driver shall allow the static configuration of PWM channel properties

"Supporting Material: BMW Specification MCAL V1.0a, MAL13.1.4 The idle level configuration covers the active phase requirement from Hella.

[SRS\_Pwm\_12295] The PWM driver shall provide a service for setting the duty cycle of a selected channel

"Supporting Material: BMW Specification MCAL V1.0a, MAL13.x"

[SRS\_Pwm\_12297] The PWM driver shall provide a service for setting the period of a selected channel

"Supporting Material: BMW Specification MCAL V1.0a, MAL13.8.0; Kojak movies (in case you do not know Kojak)"

Document: SRS\_BSWGeneral:

---

Chapter 5 "General Requirements on Basic Software"

The ECU application experience is taken from the following concrete applications:

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BMW, DC and VW standard software packages (Standard Core, Standard Software Platform, Standard Software Core) including OSEK OS, communication modules, bootloader, basic diagnostic functions for the domains listed above

Infotainment control ECU

[SRS\_BSW\_00305] Data types naming convention

"Supporting Material: BMW Standard Core Programming Guidelines"

Document: SRS\_COM:

---

[SRS\_Com\_00177] AUTOSAR COM and LargeDataCOM shall support multiple configuration stages

"Use Case: It must be possible to configure the handled bus frames after compile- or build-time, particularly for future concepts running at BMW (reuse an ECU within another vehicle product line with different and incompatible communication layouts)."

Document: SRS\_ADCCDriver:

---

[SRS\_Adc\_12280] The ADC Driver shall allow a specific result access modes for each ADC Channel Group

"Supporting Material: BMW Specification MCAL V1.0a, MAL14.2.0."

[SRS\_Adc\_12283] The ADC driver shall mask out information bits from the conversion result not belonging to the ADC value

"Supporting Material: BMW Specification MCAL V1.0a, MAL14.4.1."

[SRS\_Adc\_12819] The ADC Driver shall provide a synchronous service for reading the last valid conversion results of the selected channel group

"Supporting Material: BMW Specification MCAL V1.0a."

[SRS\_Adc\_12291] The ADC Driver shall provide a service for querying the status of an ADC Channel Group

"Supporting Material: BMW Specification MCAL V1.0a."

[SRS\_Adc\_12288] Based on the channel group configuration the ADC driver shall be able to handle the buffers of stream jobs

"Supporting Material: BMW Specification MCAL V1.0a."

[SRS\_Adc\_12292] If the ADC provides signed values, the ADC driver shall put the sign bit into the MSB of the return value

"Supporting Material: BMW Specification MCAL V1.0a, MAL14.4.2."

Document: SRS\_DIODriver:

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[SRS\_Dio\_12003] The DIO Driver shall provide a service that writes a data word to the assigned DIO port

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.3.0"

[SRS\_Dio\_12004] The DIO Driver shall provide a service that writes a selectable number of adjoining bits to an assigned part of a DIO port

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.3.1"

[SRS\_Dio\_12005] The DIO Driver shall provide a service for write access to single DIO channels

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.4.0"

[SRS\_Dio\_12006] The DIO Driver shall provide a service for reading a data word from the assigned DIO port

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.4.0"

[SRS\_Dio\_12007] The DIO Driver shall provide a service for reading a selectable number of adjoining bits from an assigned part of a DIO port

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.4.1"

[SRS\_Dio\_12008] The DIO Driver shall provide a service for reading one bit of an assigned DIO channel

"Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.6.0"

#### **Agreed solution:**

CP\_SRS\_ADCCDriver

SRS\_Adc\_12280, SRS\_Adc\_12283, SRS\_Adc\_12819, SRS\_Adc\_12291,  
SRS\_Adc\_12288, SRS\_Adc\_12292

Change from:

Supporting Material: BMW Specification MCAL V1.0a, [...]

to:

Supporting Material: –

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CP\_SRS\_BSWGeneral

replace "BMW Standard Core Programming Guidelines" by "–" in SRS\_BSW\_00305

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CP\_SRS\_COM

Update [SRS\_Com\_00177] AUTOSAR COM and LargeDataCOM shall support multiple configuration stages

"Use Case: It must be possible to configure the handled bus frames after compile- or build-time, for example when reusing an ECU within another vehicle product line with different and incompatible communication layouts."

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**CP\_SRS\_DIODriver**

SRS\_Dio\_12003, SRS\_Dio\_12004, SRS\_Dio\_12005, SRS\_Dio\_12006,  
SRS\_Dio\_12007, SRS\_Dio\_12008

Change from:

Supporting Material: BMW Specification MCAL V1.0a, REQ MAL10.x.x

to:

Supporting Material: –

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**CP\_SRS\_EEPROMDriver**

SRS\_Eep\_00087, SRS\_Eep\_00088, SRS\_Eep\_00089, SRS\_Eep\_00090,  
SRS\_Eep\_00091, SRS\_Eep\_00092, SRS\_Eep\_00094, SRS\_Eep\_00095,  
SRS\_Eep\_00096, SRS\_Eep\_12047, SRS\_Eep\_12050

Change from:

Supporting Material: BMW Specification MCAL V1.0a

to:

Supporting Material: –

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**CP\_SRS\_PWMDriver**

SRS\_Pwm\_12293:

Change from:

Supporting Material: BMW Specification MCAL V1.0a, MAL13.1.4. The idle level configuration covers the active phase requirement from Hella.

Change to:

Supporting Material: –

SRS\_Pwm\_12295:

Change from:

Supporting Material: BMW Specification MCAL V1.0a, MAL13.x

Change to:

Supporting Material: –

SRS\_Pwm\_12297:

Change from:

Supporting Material: BMW Specification MCAL V1.0a, MAL13.8.0. Kojak movies (in case you do not know Kojak)

Change to:

Supporting Material: –

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CP\_SRS\_SPALGeneral

SRS\_SPAL\_12056, SRS\_SPAL\_12057, SRS\_SPAL\_12063, SRS\_SPAL\_12064, SRS\_SPAL\_12067

Change from:

Supporting Material: BMW Specification MCAL V1.0a, ...

to:

Supporting Material: –

–Last change on issue 78035 comment 14–

#### **BW-C-Level:**

Application	Specification	Bus
1	1	1