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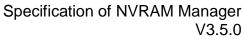
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1 Introduction and functional overview

This specification describes the functionality, API and the configuration of the AUTOSAR Basic Software module NVRAM Manager (NvM).

The NvM module shall provide services to ensure the data storage and maintenance of NV (non volatile) data according to their individual requirements in an automotive environment. The NvM module shall be able to administrate the NV data of an EEPROM and/or a FLASH EEPROM emulation device.

The NvM module shall provide the required synchronous/asynchronous services for the management and the maintenance of NV data (init/read/write/control).

The relationship between the different blocks can be visualized in the following picture:

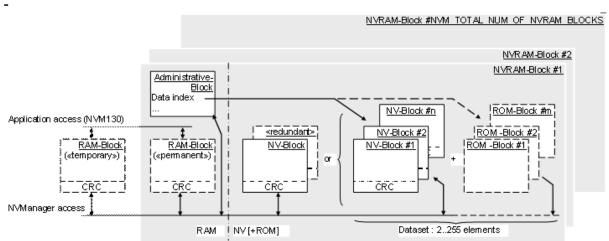


Figure 1: Memory Structure of Different Block Types



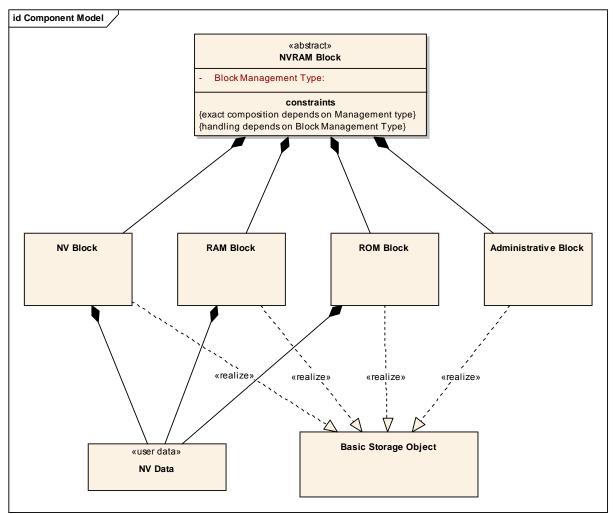


Figure 2: Logical Structure of Different Block Types



2 Acronyms and abbreviations

Acronyms and abbreviations, which have a local scope and therefore are not contained in the AUTOSAR glossary, must appear in a local glossary.

Abbreviation/ Acronym:	Description:
Basic Storage Object	A "Basic Storage Object" is the smallest entity of a "NVRAM block". Several "Basic Storage Objects" can be used to build a NVRAM Block. A "Basic Storage Object" can reside in different memory locations (RAM/ROM/NV memory).
NVRAM Block	The "NVRAM Block" is the entire structure, which is needed to administrate and to store a block of NV data.
NV data	The data to be stored in Non-Volatile memory.
Block Management Type	Type of the NVRAM Block. It depends on the (configurable) individual composition of a NVRAM Block in chunks of different mandatory/optional Basic Storage Objects and the subsequent handling of this NVRAM block.
RAM Block	The "RAM Block" is a "Basic Storage Object". It represents the part of a "NVRAM Block" which resides in the RAM. See [SRS_LIBS_08534] .[SWS_NvM_00126]
ROM Block	The "ROM Block" is a "Basic Storage Object". It represents the part of a "NVRAM Block" which resides in the ROM. The "ROM Block" is an optional part of a "NVRAM Block".[SWS NvM 00020]
NV Block	The "NV Block" is a "Basic Storage Object". It represents the part of a "NVRAM Block" which resides in the NV memory. The "NV Block" is a mandatory part of a "NVRAM Block". [SWS NvM 00125]
NV Block Header	Additional information included in the NV Block if the mechanism "Static Block ID" is enabled.
Administrative Block	The "Administrative Block" is a "Basic Storage Object". It resides in RAM. The "Administrative Block" is a mandatory part of a "NVRAM Block". [SWS_NvM_00135]
DET	Development Error Tracer – module to which development errors are reported.
DEM	Diagnostic Event Manager – module to which production relevant errors are reported
NV	Non volatile
FEE	Flash EEPROM Emulation
EA	EEPROM Abstraction
FCFS	First come first served



3 Related documentation

3.1 Input documents

- [1] List of Basic Software Modules AUTOSAR_TR_BSWModuleList.pdf
- [2] Layered Software Architecture
 AUTOSAR_EXP_LayeredSoftwareArchitecture.pdf
- [3] General Requirements on Basic Software Modules AUTOSAR_SRS_BSWGeneral.pdf
- [4] Requirements on Memory Services AUTOSAR_SRS_MemoryServices.pdf
- [5] Specification of EEPROM Abstraction AUTOSAR_SWS_EEPROMAbstraction
- [6] Specification of Flash EEPROM Emulation AUTOSAR_SWS_FlashEEPROMEmulation
- [7] Specification of Memory Abstraction Interface AUTOSAR_SWS_MemoryAbstractionInterface
- [8] Specification of Memory Mapping AUTOSAR_SWS_MemoryMapping
- [9] Virtual Functional Bus AUTOSAR_EXP_VFB.pdf
- [10] Software Component Template
 AUTOSAR_TPS_SoftwareComponentTemplate
- [11] Specification of RTE Software AUTOSAR_SWS_RTE.pdf
- [12] Specification of ECU Configuration AUTOSAR_TPS_ECUConfiguration.pdf
- [13] Basic Software Module Description Template AUTOSAR_TPS_BSWModuleDescriptionTemplate
- [14] Specification of CRC Routines AUTOSAR_SWS_CRCLibrary
- [15] General Specification of Basic Software Modules AUTOSAR_SWS_BSWGeneral.pdf



3.2 Related specification

AUTOSAR provides a General Specification on Basic Software modules [15] (SWS BSW General), which is also valid for NVRAM Manager.

Thus, the specification SWS BSW General shall be considered as additional and required specification for NVRAM Manager.



4 Constraints and assumptions

4.1 Limitations

Limitations are given mainly by the finite number of "Block Management Types" and their individual treatment of NV data. These limits can be reduced by an enhanced user defined management information, which can be stored as a structured part of the real NV data. In this case the user defined management information has to be interpreted and handled by the application at least.

4.2 Applicability to car domains

No restrictions.

4.3 Conflicts

None



5 Dependencies to other modules

This section describes the relations to other modules within the basic software.

5.1 File structure

5.1.1 Header file structure

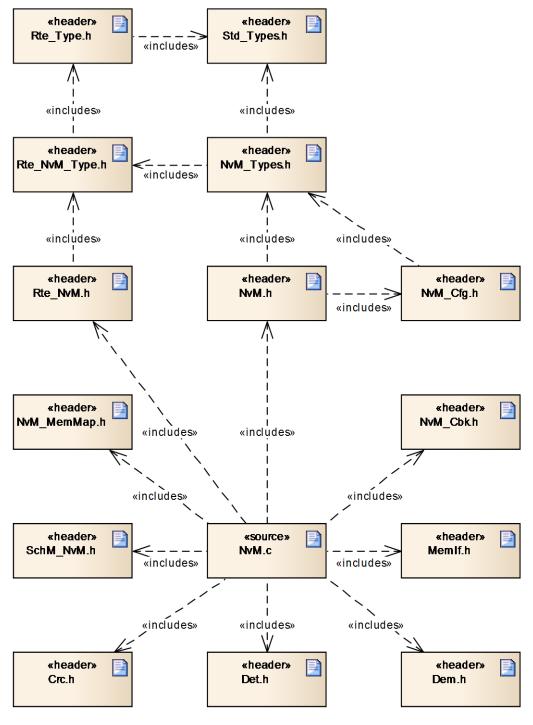


Figure 3: NvM Include structure



The include file structure shall be as follows:

[SWS_NvM_00077] 「An API interface NvM.h that provides the function prototypes to access the underlying NVRAM functions.] (SRS_BSW_00435, SRS_BSW_00436)

[SWS_NvM_00550] [A type header NvM_Types.h that provides the types for the NvM module.] ()

[SWS_NvM_00755] [The file NvM_Types.h shall include Rte_NvM_Type.h to include the types which are common used by BSW Modules and Software Components. NvM_Types.h and NvM.h shall only contain types, that are not already defined in Rte_NvM_Type.h.] (SRS_BSW_00447)

[SWS_NvM_00551] [A callback interface NvM_Cbk.h that provides the callback function prototypes to be used by the lower layers] ()

[SWS_NvM_00552] [A type header NvM_Cfg.h that provides the configuration parameters for the NvM module. | ()

[SWS NvM 00689] [NvM Cfg.h shall include NvM Types.h. | ()

[SWS NvM 00690] [NvM Types.h shall include Std Types.h. | ()

[SWS NvM 00553] [NvM.h shall include NvM Cfg.h. | ()

[SWS_NvM_00554] [NvM module shall include NvM.h, Dem.h, Memlf.h, SchM_NvM.h, NvM_MemMap.h.]()

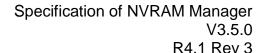
[SWS_NvM_00555] [NvM module shall include Crc.h.] ()

[SWS_NvM_00556] [NvM module shall include Det.h.] ()

[SWS NvM 00691] [Only NvM.h shall be included by the upper layer. | ()

5.2 Memory abstraction modules

The memory abstraction modules abstract the NvM module from the subordinated drivers which are hardware dependent. The memory abstraction modules provide a runtime translation of each block access initiated by the NvM module to select the corresponding driver functions which are unique for all configured EEPROM or





FLASH storage devices. The memory abstraction module is chosen via the NVRAM block device ID which is configured for each NVRAM block.

5.3 CRC module

The NvM module uses CRC generation routines (8/16/32 bit) to check and to generate CRC for NVRAM blocks as a configurable option. The CRC routines have to be provided externally [ref. to ch. 8.1.4.2].

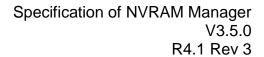
5.4 Capability of the underlying drivers

A set of underlying driver functions has to be provided for every configured NVRAM device as, for example, internal or external EEPROM or FLASH devices. The unique driver functions inside each set of driver functions are selected during runtime via a memory hardware abstraction module (see chapter 5.2). A set of driver functions has to include all the needed functions to write to, to read from or to maintain (e.g. erase) a configured NVRAM device.



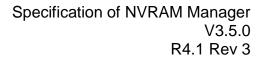
6 Requirements traceability

Requirement	Description	Satisfied by
-	-	SWS_NvM_00000
-	-	SWS_NvM_00001
-	-	SWS_NvM_00006
-	-	SWS_NvM_00014
-	-	SWS_NvM_00020
-	-	SWS_NvM_00021
-	-	SWS_NvM_00030
-	-	SWS_NvM_00038
-	-	SWS_NvM_00040
-	-	SWS_NvM_00047
-	-	SWS_NvM_00052
-	-	SWS_NvM_00054
-	-	SWS_NvM_00069
-	-	SWS_NvM_00073
-	-	SWS_NvM_00083
-	-	SWS_NvM_00085
-	-	SWS_NvM_00088
-	-	SWS_NvM_00091
-	-	SWS_NvM_00092
-	-	SWS_NvM_00111
-	-	SWS_NvM_00112
-	-	SWS_NvM_00113
-	-	SWS_NvM_00118
-	-	SWS_NvM_00121
-	-	SWS_NvM_00125
-	-	SWS_NvM_00126
-	-	SWS_NvM_00127
-	-	SWS_NvM_00128
-	-	SWS_NvM_00129
-	-	SWS_NvM_00130
-	-	SWS_NvM_00133
-	-	SWS_NvM_00134
-	-	SWS_NvM_00135
-	-	SWS_NvM_00136
-	-	SWS_NvM_00138
-	-	SWS_NvM_00139



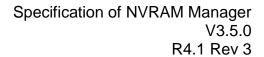


-	-	SWS_NvM_00140
-	-	SWS_NvM_00141
-	-	SWS_NvM_00143
-	-	SWS_NvM_00144
-	-	SWS_NvM_00146
-	-	SWS_NvM_00149
-	-	SWS_NvM_00150
-	-	SWS_NvM_00155
-	-	SWS_NvM_00156
-	-	SWS_NvM_00158
-	-	SWS_NvM_00160
-	-	SWS_NvM_00168
-	-	SWS_NvM_00169
-	-	SWS_NvM_00175
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-	-	SWS_NvM_00185
-	-	SWS_NvM_00192
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-	-	SWS_NvM_00206
-	-	SWS_NvM_00209
-	-	SWS_NvM_00210
-	-	SWS_NvM_00212
-	-	SWS_NvM_00216
-	-	SWS_NvM_00217
-	-	SWS_NvM_00224
-	-	SWS_NvM_00225
-	-	SWS_NvM_00226



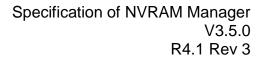


-	-	SWS_NvM_00227
-	-	SWS_NvM_00228
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-	-	SWS_NvM_00231
-	-	SWS_NvM_00232
-	-	SWS_NvM_00233
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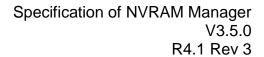


-	-	SWS_NvM_00275
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-	-	SWS_NvM_00281
-	-	SWS_NvM_00284
-	-	SWS_NvM_00287
-	-	SWS_NvM_00288
-	-	SWS_NvM_00290
-	-	SWS_NvM_00291
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-	-	SWS_NvM_00333
-	-	SWS_NvM_00334



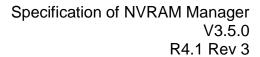


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-	-	SWS_NvM_00380



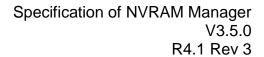


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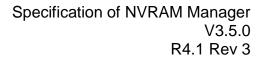


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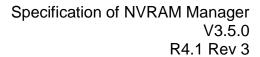


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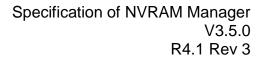


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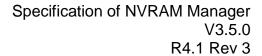


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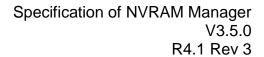


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SWS_NvM_00744			
SWS_NVM_00847	-	-	SWS_NvM_00845
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SWS_NVM_00744	-	-	SWS_NvM_00847
BSW00420 - SWS_NVM_00744 BSW00431 - SWS_NVM_00744 BSW00434 - SWS_NVM_00454 BSW176 - SWS_NVM_00460, SWS_NVM_00455, SWS_NVM_00460, SWS_NVM_00460, SWS_NVM_00460, SWS_NVM_00764, SWS_NVM_0066, SWS_NVM_0066, SWS_NVM_0066, SWS_NVM_0066, SWS_NVM_00664, SWS_NVM_00764, SWS_NVM_00664, S	-	-	SWS_NvM_00848
BSW00421 - SWS_NVM_00744 BSW00431 - SWS_NVM_00744 BSW00434 - SWS_NVM_00744 BSW176 - SWS_NVM_00454, SWS_NVM_00456, SWS_NVM_00456, SWS_NVM_00456, SWS_NVM_00460, SWS_NVM_00540, SWS_NVM_00764, SWS_NVM_0064, SWS_NVM_0064, SWS_NVM_00764, SWS_NVM_0064, SWS_NVM_0064, SWS_NVM_0064, SWS_NVM_00764, SWS_NVM_0064, S	BSW00324	-	SWS_NvM_00744
BSW00431 - SWS_NVM_00744 BSW00434 - SWS_NVM_00744 BSW176 - SWS_NVM_00454, SWS_NVM_00455, SWS_NVM_00460, SWS_NVM_00460, SWS_NVM_00540, SWS_NVM_00793 SRS_BSW_00005 Modules of the æC Abstraction Layer (MCAL) may not have hard coded horizontal interfaces SRS_BSW_00006 The source code of software modules above the æC Abstraction Layer (MCAL) shall not be processor and compiler dependent. SRS_BSW_00007 All Basic SW Modules written in C language shall conform to the MISRA C 2004 Standard. SRS_BSW_00009 All Basic SW Modules shall be documented according to a common standard. SRS_BSW_00010 The memory consumption of all Basic SW Modules shall be documented for a defined configuration for all supported platforms. SRS_BSW_00110 The Basic Software Module shall be able to initialize variables and hardware in a separate initialization function SRS_BSW_00160 Configuration files of AUTOSAR Basic SW module shall be readable for human beings SRS_BSW_00161 The AUTOSAR Basic Software shall provide a microcontroller abstraction layer which provides a standardized interface to higher software layers SRS_BSW_00162 The AUTOSAR Basic Software shall provide a hardware abstraction layer SRS_BSW_00164 The Implementation of interrupt service routines shall be done by the Operating System, complex drivers or modules	BSW00420	-	SWS_NvM_00744
BSW176 BSW2 NVM_00454, SWS_NVM_00460, SWS_NVM_00461, SWS_NVM_00540, SWS_NVM_00540, SWS_NVM_00540, SWS_NVM_00793 BSRS_BSW_00005 BRS_BSW_00005 BRS_BSW_0006 BRS_BSW_0006 BRS_BSW_0006 BRS_BSW_0006 BRS_BSW_0007 BRS_BSW_00007 BRS_BSW_00007 BRS_BSW_00007 BRS_BSW_00007 BRS_BSW_000007 BRS_BSW_000007 BRS_BSW_0000000000000000000000000000000000	BSW00421	-	SWS_NvM_00465
BSW176 SWS_NVM_00454, SWS_NVM_00451, SWS_NVM_00461, SWS_NVM_00744, SWS_NVM_00793 SRS_BSW_00005 RSS_BSW_00005 SRS_BSW_00006 The source code of software modules above the æC Abstraction Layer (MCAL) shall not be processor and compiler dependent. SRS_BSW_00007 All Basic SW Modules written in C language shall conform to the MISRA C 2004 Standard. SRS_BSW_00009 All Basic SW Modules shall be documented according to a common standard. SRS_BSW_00010 The memory consumption of all Basic SW Modules shall be documented for a defined configuration for all supported platforms. SRS_BSW_00101 The Basic Software Module shall be able to initialize variables and hardware in a separate initialization function SRS_BSW_00160 SRS_BSW_00161 The AUTOSAR Basic Software shall provide a microcontroller abstraction layer which provides a standardized interface to higher software layers or modules SRS_BSW_00164 The Implementation of interrupt service routines shall be done by the Operating System, complex drivers or modules	BSW00431	-	SWS_NvM_00744
SRS_BSW_00005 Modules of the æC Abstraction Layer (MCAL) may not have hard coded horizontal interfaces SRS_BSW_00006 The source code of software modules above the æC Abstraction Layer (MCAL) shall not be processor and compiler dependent. SRS_BSW_00007 All Basic SW Modules written in C language shall conform to the MISRA C 2004 Standard. SRS_BSW_00009 All Basic SW Modules shall be documented according to a common standard. SRS_BSW_00010 The memory consumption of all Basic SW Modules shall be documented for a defined configuration for all supported platforms. SRS_BSW_00101 The Basic Software Module shall be able to initialize variables and hardware in a separate initialization function SRS_BSW_00160 Configuration files of AUTOSAR Basic SW module shall be readable for human beings SRS_BSW_00161 The AUTOSAR Basic Software shall provide a microcontroller abstraction layer which provides a standardized interface to higher software layers SRS_BSW_00162 The AUTOSAR Basic Software shall provide a hardware abstraction layer which provides a standardized interface to higher software layers SRS_BSW_00164 The Implementation of interrupt service routines shall be done by the Operating System, complex drivers or modules	BSW00434	-	SWS_NvM_00744
(MCAL) may not have hard coded horizontal interfaces SRS_BSW_00006 The source code of software modules above the æC Abstraction Layer (MCAL) shall not be processor and compiler dependent. SRS_BSW_00007 All Basic SW Modules written in C language shall conform to the MISRA C 2004 Standard. SRS_BSW_00009 All Basic SW Modules shall be documented according to a common standard. SRS_BSW_00010 The memory consumption of all Basic SW Modules shall be documented for a defined configuration for all supported platforms. SRS_BSW_00101 The Basic Software Module shall be able to initialize variables and hardware in a separate initialization function SRS_BSW_00160 Configuration files of AUTOSAR Basic SW module shall be readable for human beings SRS_BSW_00161 The AUTOSAR Basic Software shall provide a microcontroller abstraction layer which provides a standardized interface to higher software layers SRS_BSW_00162 The AUTOSAR Basic Software shall provide a hardware abstraction layer SRS_BSW_00164 The AUTOSAR Basic Software shall provide a hardware abstraction layer SRS_BSW_00164 The AUTOSAR Basic Software shall provide a hardware abstraction layer SRS_BSW_00164 The Implementation of interrupt service routines shall be done by the Operating System, complex drivers or modules	BSW176	-	SWS_NvM_00460, SWS_NvM_00461, SWS_NvM_00540, SWS_NvM_00764,
modules above the æC Abstraction Layer (MCAL) shall not be processor and compiler dependent. SRS_BSW_00007 All Basic SW Modules written in C language shall conform to the MISRA C 2004 Standard. SRS_BSW_00009 All Basic SW Modules shall be documented according to a common standard. SRS_BSW_00010 The memory consumption of all Basic SW Modules shall be documented for a defined configuration for all supported platforms. SRS_BSW_00101 The Basic Software Module shall be able to initialize variables and hardware in a separate initialization function SRS_BSW_00160 Configuration files of AUTOSAR Basic SW module shall be readable for human beings SRS_BSW_00161 The AUTOSAR Basic Software shall provide a microcontroller abstraction layer which provides a standardized interface to higher software layers SRS_BSW_00164 The AUTOSAR Basic Software shall provide a hardware abstraction layer SRS_BSW_00164 The Implementation of interrupt SRS_BSW_00165 The AUTOSAR Basic Software shall provide a hardware abstraction layer SRS_BSW_00165 The AUTOSAR Basic Software shall provide a hardware abstraction layer SRS_BSW_001664 The Implementation of interrupt SRS_BSW_00167 System, complex drivers or modules	SRS_BSW_00005	(MCAL) may not have hard coded	SWS_NvM_00744
language shall conform to the MISRA C 2004 Standard. SRS_BSW_00009 All Basic SW Modules shall be documented according to a common standard. SRS_BSW_00010 The memory consumption of all Basic SW Modules shall be documented for a defined configuration for all supported platforms. SRS_BSW_00101 The Basic Software Module shall be able to initialize variables and hardware in a separate initialization function SRS_BSW_00160 Configuration files of AUTOSAR Basic SW module shall be readable for human beings SRS_BSW_00161 The AUTOSAR Basic Software shall provide a microcontroller abstraction layer which provides a standardized interface to higher software layers SRS_BSW_00164 The AUTOSAR Basic Software shall provide a hardware abstraction layer SRS_BSW_00164 The HUTOSAR Basic Software shall provide a hardware abstraction layer SRS_BSW_00164 The HUTOSAR Basic Software shall provide a hardware abstraction layer SRS_BSW_00164 The Implementation of interrupt service routines shall be done by the Operating System, complex drivers or modules	SRS_BSW_00006	modules above the æC Abstraction Layer (MCAL) shall not be processor	SWS_NvM_00744
documented according to a common standard. SRS_BSW_00010 The memory consumption of all Basic SW Modules shall be documented for a defined configuration for all supported platforms. SRS_BSW_00101 The Basic Software Module shall be able to initialize variables and hardware in a separate initialization function SRS_BSW_00160 Configuration files of AUTOSAR Basic SW module shall be readable for human beings SRS_BSW_00161 The AUTOSAR Basic Software shall provide a microcontroller abstraction layer which provides a standardized interface to higher software layers SRS_BSW_00162 The AUTOSAR Basic Software shall provide a hardware abstraction layer SRS_BSW_00164 The Implementation of interrupt service routines shall be done by the Operating System, complex drivers or modules	SRS_BSW_00007	language shall conform to the MISRA	SWS_NvM_00744
Basic SW Modules shall be documented for a defined configuration for all supported platforms. SRS_BSW_00101 The Basic Software Module shall be able to initialize variables and hardware in a separate initialization function SRS_BSW_00160 Configuration files of AUTOSAR Basic SW module shall be readable for human beings SRS_BSW_00161 The AUTOSAR Basic Software shall provide a microcontroller abstraction layer which provides a standardized interface to higher software layers SRS_BSW_00162 The AUTOSAR Basic Software shall provide a hardware abstraction layer SRS_BSW_00164 The Implementation of interrupt service routines shall be done by the Operating System, complex drivers or modules	SRS_BSW_00009	documented according to a common	SWS_NvM_00744
able to initialize variables and hardware in a separate initialization function SRS_BSW_00160 Configuration files of AUTOSAR Basic SW module shall be readable for human beings SRS_BSW_00161 The AUTOSAR Basic Software shall provide a microcontroller abstraction layer which provides a standardized interface to higher software layers SRS_BSW_00162 The AUTOSAR Basic Software shall provide a hardware abstraction layer SRS_BSW_00164 The Implementation of interrupt service routines shall be done by the Operating System, complex drivers or modules	SRS_BSW_00010	Basic SW Modules shall be documented for a defined configuration for all supported	SWS_NvM_00744
Basic SW module shall be readable for human beings SRS_BSW_00161 The AUTOSAR Basic Software shall provide a microcontroller abstraction layer which provides a standardized interface to higher software layers SRS_BSW_00162 The AUTOSAR Basic Software shall provide a hardware abstraction layer SRS_BSW_00164 The Implementation of interrupt service routines shall be done by the Operating System, complex drivers or modules	SRS_BSW_00101	able to initialize variables and hardware in a separate initialization	SWS_NvM_00399, SWS_NvM_00400
provide a microcontroller abstraction layer which provides a standardized interface to higher software layers SRS_BSW_00162 The AUTOSAR Basic Software shall provide a hardware abstraction layer SRS_BSW_00164 The Implementation of interrupt service routines shall be done by the Operating System, complex drivers or modules SWS_NvM_00744	SRS_BSW_00160	Basic SW module shall be readable	SWS_NvM_00744
provide a hardware abstraction layer SRS_BSW_00164 The Implementation of interrupt service routines shall be done by the Operating System, complex drivers or modules SWS_NvM_00744	SRS_BSW_00161	provide a microcontroller abstraction layer which provides a standardized	SWS_NvM_00744
service routines shall be done by the Operating System, complex drivers or modules	SRS_BSW_00162		SWS_NvM_00744
SRS_BSW_00167 All AUTOSAR Basic Software SWS_NvM_00028	SRS_BSW_00164	service routines shall be done by the Operating System, complex drivers	SWS_NvM_00744
	SRS_BSW_00167	All AUTOSAR Basic Software	SWS_NvM_00028



	Modules shall provide configuration	
	rules and constraints to enable plausibility checks	
SRS_BSW_00168	SW components shall be tested by a function defined in a common API in the Basis-SW	SWS_NvM_00744
SRS_BSW_00170	The AUTOSAR SW Components shall provide information about their dependency from faults, signal qualities, driver demands	SWS_NvM_00744
SRS_BSW_00171	Optional functionality of a Basic-SW component that is not required in the ECU shall be configurable at precompile-time	SWS_NvM_00028
SRS_BSW_00172	The scheduling strategy that is built inside the Basic Software Modules shall be compatible with the strategy used in the system	SWS_NvM_00464
SRS_BSW_00302	All AUTOSAR Basic Software Modules shall only export information needed by other modules	SWS_NvM_00744
SRS_BSW_00304	-	SWS_NvM_00744
SRS_BSW_00306	AUTOSAR Basic Software Modules shall be compiler and platform independent	SWS_NvM_00744
SRS_BSW_00307	Global variables naming convention	SWS_NvM_00744
SRS_BSW_00308	AUTOSAR Basic Software Modules shall not define global data in their header files, but in the C file	SWS_NvM_00744
SRS_BSW_00309	All AUTOSAR Basic Software Modules shall indicate all global data with read-only purposes by explicitly assigning the const keyword	SWS_NvM_00744
SRS_BSW_00312	Shared code shall be reentrant	SWS_NvM_00744
SRS_BSW_00314	All internal driver modules shall separate the interrupt frame definition from the service routine	SWS_NvM_00744
SRS_BSW_00321	The version numbers of AUTOSAR Basic Software Modules shall be enumerated according specific rules	SWS_NvM_00744
SRS_BSW_00323	All AUTOSAR Basic Software Modules shall check passed API parameters for validity	SWS_NvM_00027
SRS_BSW_00325	The runtime of interrupt service routines and functions that are running in interrupt context shall be kept short	SWS_NvM_00744
SRS_BSW_00326	-	SWS_NvM_00744
SRS_BSW_00327	Error values naming convention	SWS_NvM_00023, SWS_NvM_00027
SRS_BSW_00328	All AUTOSAR Basic Software	SWS_NvM_00744



	Modules shall avoid the duplication of code	
SRS_BSW_00330	It shall be allowed to use macros instead of functions where source code is used and runtime is critical	SWS_NvM_00744
SRS_BSW_00331	All Basic Software Modules shall strictly separate error and status information	SWS_NvM_00023, SWS_NvM_00027
SRS_BSW_00334	All Basic Software Modules shall provide an XML file that contains the meta data	SWS_NvM_00744
SRS_BSW_00335	Status values naming convention	SWS_NvM_00744
SRS_BSW_00336	Basic SW module shall be able to shutdown	SWS_NvM_00744
SRS_BSW_00337	Classification of development errors	SWS_NvM_00023
SRS_BSW_00341	Module documentation shall contains all needed informations	SWS_NvM_00744
SRS_BSW_00342	It shall be possible to create an AUTOSAR ECU out of modules provided as source code and modules provided as object code, even mixed	SWS_NvM_00744
SRS_BSW_00343	The unit of time for specification and configuration of Basic SW modules shall be preferably in physical time unit	
SRS_BSW_00344	BSW Modules shall support link-time configuration	SWS_NvM_00744
SRS_BSW_00347	A Naming seperation of different instances of BSW drivers shall be in place	SWS_NvM_00744
SRS_BSW_00348	All AUTOSAR standard types and constants shall be placed and organized in a standard type header file	SWS_NvM_00744
SRS_BSW_00353	All integer type definitions of target and compiler specific scope shall be placed and organized in a single type header	SWS_NvM_00744
SRS_BSW_00355	-	SWS_NvM_00744
SRS_BSW_00361	All mappings of not standardized keywords of compiler specific scope shall be placed and organized in a compiler specific type and keyword header	SWS_NvM_00744
SRS_BSW_00371	The passing of function pointers as API parameter is forbidden for all AUTOSAR Basic Software Modules	
SRS_BSW_00373	The main processing function of each AUTOSAR Basic Software Module shall be named according the defined	SWS_NvM_00464



	convention	
SRS_BSW_00375	Basic Software Modules shall report wake-up reasons	SWS_NvM_00744
SRS_BSW_00376	-	SWS_NvM_00464
SRS_BSW_00378	AUTOSAR shall provide a boolean type	SWS_NvM_00744
SRS_BSW_00380	Configuration parameters being stored in memory shall be placed into separate c-files	SWS_NvM_00744
SRS_BSW_00381	The pre-compile time parameters shall be placed into a separate configuration header file	SWS_NvM_00028
SRS_BSW_00383	The Basic Software Module specifications shall specify which other configuration files from other modules they use at least in the description	
SRS_BSW_00384	The Basic Software Module specifications shall specify at least in the description which other modules they require	SWS_NvM_00465, SWS_NvM_00466
SRS_BSW_00385	List possible error notifications	SWS_NvM_00023, SWS_NvM_00027
SRS_BSW_00386	The BSW shall specify the configuration for detecting an error	SWS_NvM_00023, SWS_NvM_00027
SRS_BSW_00387	The Basic Software Module specifications shall specify how the callback function is to be implemented	SWS_NvM_00330, SWS_NvM_00331
SRS_BSW_00388	Containers shall be used to group configuration parameters that are defined for the same object	SWS_NvM_00028
SRS_BSW_00389	Containers shall have names	SWS_NvM_00028
SRS_BSW_00390	Parameter content shall be unique within the module	SWS_NvM_00028
SRS_BSW_00391	-	SWS_NvM_00028
SRS_BSW_00392	Parameters shall have a type	SWS_NvM_00028
SRS_BSW_00393	Parameters shall have a range	SWS_NvM_00028
SRS_BSW_00394	The Basic Software Module specifications shall specify the scope of the configuration parameters	SWS_NvM_00028
SRS_BSW_00395	The Basic Software Module specifications shall list all configuration parameter dependencies	SWS_NvM_00028
SRS_BSW_00396	The Basic Software Module specifications shall specify one classe (of the three) to be supported	SWS_NvM_00028
SRS_BSW_00397	The configuration parameters in pre- compile time are fixed before	SWS_NvM_00028



	T	T
	compilation starts	
SRS_BSW_00398	The link-time configuration is achieved on object code basis in the stage after compiling and before linking	
SRS_BSW_00399	Parameter-sets shall be located in a separate segment and shall be loaded after the code	SWS_NvM_00744
SRS_BSW_00400	Parameter shall be selected from multiple sets of parameters after code has been loaded and started	SWS_NvM_00744
SRS_BSW_00404	BSW Modules shall support post- build configuration	SWS_NvM_00744
SRS_BSW_00405	BSW Modules shall support multiple configuration sets	SWS_NvM_00744
SRS_BSW_00406	A static status variable denoting if a BSW module is initialized shall be initialized with value 0 before any APIs of the BSW module is called	
SRS_BSW_00412	References to c-configuration parameters shall be placed into a separate h-file	SWS_NvM_00744
SRS_BSW_00415	Interfaces which are provided exclusively for one module shall be separated into a dedicated header file	SWS_NvM_00744
SRS_BSW_00416	The sequence of modules to be initialized shall be configurable	SWS_NvM_00744
SRS_BSW_00417	Software which is not part of the SW-C shall report error events only after the DEM is fully operational.	SWS_NvM_00744
SRS_BSW_00422	Pre-de-bouncing of error status information is done within the DEM	SWS_NvM_00744
SRS_BSW_00423	BSW modules with AUTOSAR interfaces shall be describable with the means of the SW-C Template	SWS_NvM_00744
SRS_BSW_00425	The BSW module description template shall provide means to model the defined trigger conditions of schedulable objects	
SRS_BSW_00426	BSW Modules shall ensure data consistency of data which is shared between BSW modules	SWS_NvM_00744
SRS_BSW_00427	ISR functions shall be defined and documented in the BSW module description template	SWS_NvM_00744
SRS_BSW_00429	BSW modules shall be only allowed to use OS objects and/or related OS services	SWS_NvM_00332
SRS_BSW_00432	Modules should have separate main processing functions for read/receive	SWS_NvM_00744



	and write/transmit data path	
SRS_BSW_00435	-	SWS_NvM_00077
SRS_BSW_00436	-	SWS_NvM_00077
SRS_BSW_00447	Standardizing Include file structure of BSW Modules Implementing Autosar Service	SWS_NvM_00755
SRS_LIBS_08528	E2E library shall provide E2E profiles, where each E2E profile completely defines a particular safety protocol	SWS_NvM_00137, SWS_NvM_00557
SRS_LIBS_08529	Each of the defined E2E profiles shall use an appropriate subset of specific mechanisms	
SRS_LIBS_08531	E2E library shall call the CRC routines of CRC library	SWS_NvM_00137, SWS_NvM_00559
SRS_LIBS_08533	CRC used in each E2E profile shall be different than the CRC used by the underlying communication protocols [Approved]	
SRS_LIBS_08534	E2E library shall provide separate errors flag and error counters for each type of detected communication failure	
SRS_LIBS_08535	E2E library should provide the last received data element	SWS_NvM_00018, SWS_NvM_00253, SWS_NvM_00461
SRS_Mem_00011	The NVRAM manager shall be independent from its underlying memory hardware.	SWS_NvM_00157
SRS_Mem_00013	The NVRAM manager shall provide a mechanism to handle multiple, concurrent read / write requests	
SRS_Mem_00016	•	SWS_NvM_00010, SWS_NvM_00051, SWS_NvM_00122, SWS_NvM_00195, SWS_NvM_00196, SWS_NvM_00454, SWS_NvM_00764, SWS_NvM_00765, SWS_NvM_00766
SRS_Mem_00017	The NVRAM manager shall provide functionality to store data associated with an NVRAM block in the non-volatile memory	
SRS_Mem_00018	The NVRAM manager shall provide functionality to restore an NVRAM block's associated data from ROM defaults	SWS_NvM_00122, SWS_NvM_00266,
SRS_Mem_00020	The NVRAM manager shall provide functionality to read out the status of read/write operations	SWS_NvM_00015, SWS_NvM_00451
SRS_Mem_00027	The NVRAM manager shall provide an implicit way of accessing blocks in the NVRAM and in the shared	SWS_NvM_00442



	memory (RAM).	
SRS_Mem_00030	* ` '	SWS_NvM_00164
SRS_Mem_00034	Write accesses of the NVRAM manager to persistent memory shall be executed quasi-parallel to normal operation of the ECU	SWS_NvM_00162
SRS_Mem_00041	Each application shall be enabled to declare the memory requirements at configuration time	SWS_NvM_00051, SWS_NvM_00122
SRS_Mem_00125	For each block a notification shall be configurable	SWS_NvM_00463
SRS_Mem_00127	The NVRAM manager shall allow enabling/disabling a write protection for each NVRAM block individually	SWS_NvM_00016, SWS_NvM_00450
SRS_Mem_00129	The NVRAM manager shall repair data in blocks of management type 'NVRAM redundant'	SWS_NvM_00165, SWS_NvM_00582
SRS_Mem_00130	The NVRAM manager shall provide information about used memory resources	SWS_NvM_00744
SRS_Mem_00135	The NVRAM manager shall have an unique configuration identifier	SWS_NvM_00034
SRS_Mem_08000	The NVRAM manager shall be able to access multiple non-volatile memory devices	SWS_NvM_00051, SWS_NvM_00123, SWS_NvM_00442
SRS_Mem_08007	The NVRAM manager shall provide a service for the selection of valid dataset NV blocks	SWS_NvM_00448
SRS_Mem_08009	The NVRAM Manager shall allow a static configuration of a default write protection (on/off) for each NVRAM block	
SRS_Mem_08010	The NVRAM manager shall copy the ROM default data to the data area of the corresponding RAM block if it can not read data from NV into RAM	SWS_NvM_00171, SWS_NvM_00172
SRS_Mem_08011	The NVRAM manager shall provide a service to invalidate a block of data in the non-volatile memory	SWS_NvM_00421, SWS_NvM_00459
SRS_Mem_08014	The NVRAM manager shall allow anon-continuous RAM block allocation in the global RAM area	SWS_NvM_00051, SWS_NvM_00122, SWS_NvM_00442
SRS_Mem_08015	-	SWS_NvM_00397
SRS_Mem_08540	The NVRAM manager shall provide a function for aborting the shutdown process	SWS_NvM_00019, SWS_NvM_00458
SRS_Mem_08541	The NVRAM manager shall guarantee that an accepted write	SWS_NvM_00208, SWS_NvM_00384, SWS_NvM_00472, SWS_NvM_00798



	request will be presented	
	request will be processed	
SRS_Mem_08542	The NVRAM manager shall provide a prioritization for job processing order	SWS_NvM_00032, SWS_NvM_00378, SWS_NvM_00564
SRS_Mem_08544	The NVRAM manager shall provide a service to erase the NV block(s) associated with an NVRAM block	SWS_NvM_00415, SWS_NvM_00457
SRS_Mem_08545	The NVRAM Manager shall provide a service for marking the permanent RAM data block of an NVRAM block valid	SWS_NvM_00241, SWS_NvM_00405, SWS_NvM_00453
SRS_Mem_08546	It shall be possible to protect permanent RAM data blocks against data loss due to reset	SWS_NvM_00240, SWS_NvM_00548
SRS_Mem_08547	The NVRAM Manager shall be able to distinguish between explicitly invalidated and inconsistent data	SWS_NvM_00132, SWS_NvM_00164, SWS_NvM_00165, SWS_NvM_00571
SRS_Mem_08548	The NVRAM Manager shall request default data from the application	SWS_NvM_00700
SRS_Mem_08549	The NVRAM manager shall provide functionality to automatically initialize RAM data blocks after a software update	SWS_NvM_00171
SRS_Mem_08550	The NVRAM Manager shall provide a service for marking permanent RAM data blocks as modified/unmodified	SWS_NvM_00344, SWS_NvM_00345, SWS_NvM_00696
SRS_Mem_08554	The NVRAM manager shall retry read and write operations on NVRAM blocks if they have not succeeded up to a configurable number of times	SWS_NvM_00213, SWS_NvM_00526, SWS_NvM_00527, SWS_NvM_00529, SWS_NvM_00581, SWS_NvM_00804
SRS_Mem_08555	-	SWS_NvM_00523, SWS_NvM_00524, SWS_NvM_00593
SRS_Mem_08556	The NVRAM manager shall provide a mechanism for verification of the written block data by again reading and comparing it	
SRS_Mem_08558	The NVRAM manager shall provide a mechanism to remove all unprocessed requests associated with a NVRAM block	SWS_NvM_00458
SRS_Mem_08559	The NVRAM manager shall provide means to make shared access to a block possible	SWS_NvM_00535, SWS_NvM_00536
SRS_Mem_08560	Each NVRAM block shall be configurable for shared access	SWS_NvM_00535, SWS_NvM_00536



7 Functional specification

7.1 Basic architecture guidelines

7.1.1 Layer structure

The figure below shows the communication interaction of module NvM.

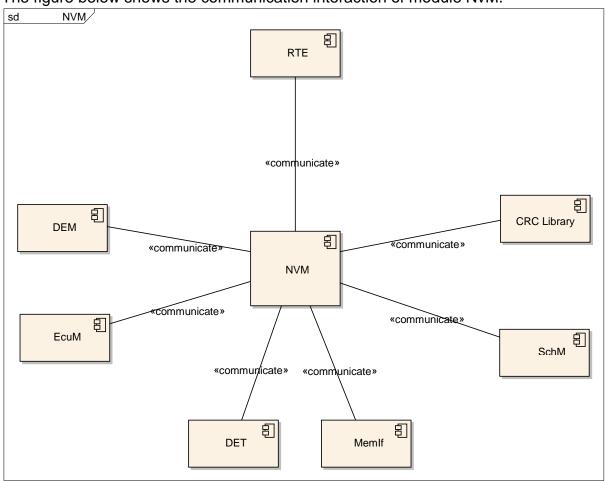


Figure 4: NVRAM Manager interactions overview

7.1.2 Addressing scheme for the memory hardware abstraction

[SWS_NvM_00051] [The Memory Abstraction Interface, the underlying Flash EEPROM Emulation and EEPROM Abstraction Layer provide the NvM module with a virtual linear 32bit address space which is composed of a 16bit block number and a 16bit block address offset.] (SRS_Mem_00041, SRS_Mem_08000, SRS_Mem_08014, SRS_Mem_00016, SRS_Mem_00017, SRS_Mem_00018)

Hint: According to [SWS_NvM_00051], the NvM module allows for a (theoretical) maximum of 65536 logical blocks, each logical block having a (theoretical) maximum size of 64 Kbytes.



[SWS_NvM_00122] [The NvM module shall further subdivide the 16bit Fee/Ea block number into the following parts:

- NV block base number (NVM_NV_BLOCK_BASE_NUMBER) with a bit width of (16 -NVM_DATASET_SELECTION_BITS)
- Data index with a bit width of (NVM_DATASET_SELECTION_BITS)
- J (SRS_Mem_00041, SRS_Mem_08014, SRS_Mem_00016, SRS_Mem_00017, SRS_Mem_00018)

[SWS_NvM_00343] [Handling/addressing of redundant NVRAM blocks shall be done towards the memory hardware abstraction in the same way like for dataset NVRAM blocks, i.e. the redundant NV blocks shall be managed by usage of the configuration parameter NvMDatasetSelectionBits. | ()

[SWS_NvM_00123] | The NV block base number (NVM_NV_BLOCK_BASE_NUMBER) shall be located in the most significant bits of the Fee/Ea block number. | (SRS_Mem_08000)

[SWS_NvM_00442] [The configuration tool shall configure the block identifiers.] (SRS_Mem_08000, SRS_Mem_00027, SRS_Mem_08014)

[SWS_NvM_00443] [The NvM module shall not modify the configured block identifiers. | ()

7.1.2.1 Examples

To clarify the previously described addressing scheme which is used for NVRAM manager ↔ memory hardware abstraction interaction, the following examples shall help to understand the correlations between the configuration parameters NvMNvBlockBaseNumber, NvMDatasetSelectionBits on NVRAM manager side and EA_BLOCK_NUMBER / FEE_BLOCK_NUMBER on memory hardware abstraction side [ECUC_NvM_00061].

For the given examples A and B a simple formula is used:

FEE/EA_BLOCK_NUMBER = (NvMNvBlockBaseNumber << NvMDatasetSelectionBits)
+ DataIndex.</pre>

Example A:

The configuration parameter NvMDatasetSelectionBits is configured to be 2. This leads to the result that 14 bits are available as range for the configuration parameter NvMNvBlockBaseNumber.

- Range of NvMNvBlockBaseNumber: 0x1..0x3FFE
- Range of data index: 0x0..0x3(=2^NvMDatasetSelectionBits-1)
- Range of FEE BLOCK NUMBER/EA BLOCK NUMBER: 0x4..0xFFFB

With this configuration the FEE/EA_BLOCK_NUMBER computes using the formula mentioned before should look like in the examples below:

For a native NVRAM block with NvMNvBlockBaseNumber = 2:

NV block is accessed with FEE/EA_BLOCK_NUMBER = 8



For a redundant NVRAM block with NvMNvBlockBaseNumber = 3:

- 1st NV block with data index 0 is accessed with FEE/EA_BLOCK_NUMBER = 12
- 2nd NV block with data index 1 is accessed with FEE/EA_BLOCK_NUMBER = 13

For a dataset NVRAM block with NvMNvBlockBaseNumber = 4, NvMNvBlockNum = 3:

- NV block #0 with data index 0 is accessed with FEE/EA_BLOCK_NUMBER = 16
- NV block #1 with data index 1 is accessed with FEE/EA_BLOCK_NUMBER = 17
- NV block #2 with data index 2 is accessed with FEE/EA BLOCK NUMBER = 18

Example B:

The configuration parameter NvMDatasetSelectionBits is configured to be 4. This leads to the result that 12 bits are available as range for the configuration parameter NvMNvBlockBaseNumber.

- Range of NvMNvBlockBaseNumber: 0x1..0xFFE
- Range of data index: 0x0..0xF(=2^NvMDatasetSelectionBits-1)
- Range of FEE/EA Block Number: 0x10..0xFFEF

7.1.3 Basic storage objects

7.1.3.1 NV block

[SWS_NvM_00125] [The NV block is a basic storage object and represents a memory area consisting of NV user data and (optionally) a CRC value and (optionally) a NV block header.

NV Block NV block Header

NV block data

NV block CRC
(optional)

Figure 5: NV Block layout

Note: This figure does not show the physical memory layout of an NV block. Only the logical clustering is shown.] ()



7.1.3.2 RAM block

[SWS_NvM_00126] The RAM block is a basic storage object and represents an area in RAM consisting of user data and (optionally) a CRC value and (optionally) a NV block header.] ()

[SWS_NvM_00127] [Restrictions on CRC usage on RAM blocks. CRC is only available if the corresponding NV block(s) also have a CRC. CRC has to be of the same type as that of the corresponding NV block(s). [ECUC_NvM_00061].] ()

[SWS_NvM_00129] [The user data area of a RAM block can reside in a different RAM address location (global data section) than the state of the RAM block.] ()

[SWS_NvM_00130] [The data area of a RAM block shall be accessible from NVRAM Manager and from the application side (data passing from/to the corresponding NV block).

RAM Block

RAM block data (permanent/ temporary) RAM block CRC (optional) RAM block CRC (optional) RAM block CRC Field

Figure 6: RAM Block layout

Note: This figure does not show the physical memory layout of a RAM block. Only the logical clustering is shown.

As the NvM module doesn't support alignment, this could be managed by configuration, i.e. the block length could be enlarged by adding padding to meet alignment requirements. | ()

[SWS_NvM_00373] [The RAM block data shall contain the permanently or temporarily assigned user data.] ()

[SWS_NvM_00370] In case of permanently assigned user data, the address of the RAM block data is known during configuration time. | ()



[SWS_NvM_00372] [In case of temporarily assigned user data, the address of the RAM block data is not known during configuration time and will be passed to the NvM module during runtime. | ()

[SWS_NvM_00088] [It shall be possible to allocate each RAM block without address constraints in the global RAM area. The whole number of configured RAM blocks needs not be located in a continuous address space.] ()

7.1.3.3 ROM block

[SWS_NvM_00020] The ROM block is a basic storage object, resides in the ROM (FLASH) and is used to provide default data in case of an empty or damaged NV block.

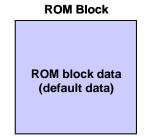


Figure 7: ROM block layout

]()

7.1.3.4 Administrative block

[SWS_NvM_00134] [The Administrative block shall be located in RAM and shall contain a block index which is used in association with Dataset NV blocks. Additionally, attribute/error/status information of the corresponding NVRAM block shall be contained.] ()

[SWS_NvM_00128] [The NvM module shall use state information of the permanent RAM block or of the RAM mirror in the NvM module in case of explicit syncronization (invalid/valid) to determine the validity of the permanent RAM block user data. | ()

[SWS_NvM_00132] [The RAM block state "invalid" indicates that the data area of the respective RAM block is invalid. The RAM block state "valid" indicates that the data area of the respective RAM block is valid.] (SRS_Mem_08547)

[SWS_NvM_00133] [The value of "invalid" shall be represented by all other values except "valid".] ()



[SWS_NvM_00135] The Administrative block shall be invisible for the application and is used exclusively by the NvM module for security and administrative purposes of the RAM block and the NVRAM block itself.] ()

[SWS_NvM_00054] [The NvM module shall use an attribute field to manage the NV block write protection in order to protect/unprotect a NV block data field.] ()

[SWS_NvM_00136] [The NvM module shall use an error/status field to manage the error/status value of the last request [SWS_NvM_00083].] ()

7.1.3.5 NV Block Header

[SWS_NvM_00522] [The NV Block header shall be included first in the NV Block, if the mechanism Static Block ID is enabled.

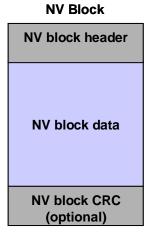


Figure 8: NV block layout with Static Block ID enabled

]()



7.1.4 Block management types

7.1.4.1 Block management types overview

[SWS_NvM_00137] [The following types of NVRAM storage shall be supported by the NvM module implementation:

- NVM BLOCK NATIVE
- NVM_BLOCK_REDUNDANT
- NVM_BLOCK_DATASET | (SRS_LIBS_08534, SRS_LIBS_08528, SRS_LIBS_08529, SRS_LIBS_08531)

[SWS_NvM_00557] [NVM_BLOCK_NATIVE type of NVRAM storage shall consist of the following basic storage objects:

- NV Blocks: 1RAM Blocks: 1ROM Blocks: 0..1
- Administrative Blocks:1 | (SRS_LIBS_08528)

[SWS_NvM_00558] [NVM_BLOCK_REDUNDANT type of NVRAM storage shall consist of the following basic storage objects:

- NV Blocks: 2RAM Blocks: 1ROM Blocks: 0..1
- Administrative Blocks:1 | (SRS LIBS 08529)

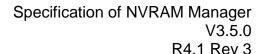
[SWS_NvM_00559] [NVM_BLOCK_DATASET type of NVRAM storage shall consist of the following basic storage objects:

- NV Blocks: 1..(m<256)*
- RAM Blocks: 1ROM Blocks: 0..n
- Administrative Blocks:1
- * The number of possible datasets depends on the configuration parameter NvMDatasetSelectionBits. J (SRS_LIBS_08531)

7.1.4.2 NVRAM block structure

[SWS_NvM_00138] [The NVRAM block shall consist of the mandatory basic storage objects NV block, RAM block and Administrative block.] ()

[SWS_NvM_00139] [The basic storage object ROM block is optional.] ()





[SWS_NvM_00140] [The composition of any NVRAM block is fixed during configuration by the corresponding NVRAM block descriptor. | ()

[SWS_NvM_00141] [All address offsets are given relatively to the start addresses of RAM or ROM in the NVRAM block descriptor. The start address is assumed to be zero.

Hint: A device specific base address or offset will be added by the respective device driver if needed.] ()

For details of the NVRAM block descriptor see chapter 7.1.4.3.

7.1.4.3 NVRAM block descriptor table

[SWS_NvM_00069] [A single NVRAM block to deal with will be selected via the NvM module API by providing a subsequently assigned Block ID.] ()

[SWS_NvM_00143] [All structures related to the NVRAM block descriptor table and their addresses in ROM (FLASH) have to be generated during configuration of the NvM module.] ()

7.1.4.4 Native NVRAM block

The Native NVRAM block is the simplest block management type. It allows storage to/retrieval from NV memory with a minimum of overhead.

[SWS_NvM_00000] [The Native NVRAM block consists of a single NV block, RAM block and Administrative block.] ()

7.1.4.5 Redundant NVRAM block

In addition to the Native NVRAM block, the Redundant NVRAM block provides enhanced fault tolerance, reliability and availability. It increases resistance against data corruption.

[SWS_NvM_00001] [The Redundant NVRAM block consists of two NV blocks, a RAM block and an Administrative block.

The following figure reflects the internal structure of a redundant NV block:



NV block header (optional) NV block data

NV Block

NV Block

NV block CRC (optional)

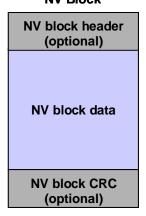


Figure 9: Redundant NVRAM Block layout

Note: This figure does not show the physical NV memory layout of a redundant NVRAM block. Only the logical clustering is shown.] ()

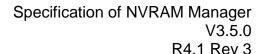
[SWS_NvM_00531] In case one NV Block associated with a Redundant NVRAM block is deemed invalid (e.g. during read), an attempt shall be made to recover the NV Block using data from the incorrupt NV Block. ()

[SWS_NvM_00546] In case the recovery fails then this shall be reported to the DEM using the code NVM_E_LOSS_OF_REDUNDANCY.

Note: "Recovery" denotes the re-establishment of redundancy. This usually means writing the recovered data back to the NV Block. \rfloor ()

7.1.4.6 Dataset NVRAM block

The Dataset NVRAM block is an array of equally sized data blocks (NV/ROM). The application can at one time access exactly one of these elements.





[SWS_NvM_00006] [The Dataset NVRAM block consists of multiple NV user data, (optionally) CRC areas, (optional) NV block headers, a RAM block and an Administrative block.] ()

[SWS_NvM_00144] The index position of the dataset is noticed via a separated field in the corresponding Administrative block.] ()

[SWS_NvM_00374] The NvM module shall be able to read all assigned NV blocks.

[SWS_NvM_00375] The NvM module shall only be able to write to all assigned NV blocks if (and only if) write protection is disabled.] ()

[SWS_NvM_00146] [If the basic storage object ROM block is selected as optional part, the index range which normally selects a dataset is extended to the ROM to make it possible to select a ROM block instead of a NV block. The index covers all NV/ROM blocks which may build up the NVRAM Dataset block.] ()

[SWS_NvM_00376] [The NvM module shall be able to only read optional ROM blocks (default datasets).] ()

[SWS_NvM_00377] [The NvM module shall treat a write to a ROM block like a write to a protected NV block.] ()

[SWS_NvM_00444] [The total number of configured datasets (NV+ROM blocks) must be in the range of 1..255. | ()

[SWS_NvM_00445] In case of optional ROM blocks, data areas with an index from 0 up to NvMNvBlockNum - 1 represent the NV blocks with their CRC in the NV memory. Data areas with an index from NvMNvBlockNum up to NvMNvBlockNum + NvMRomBlockNum - 1 represent the ROM blocks.



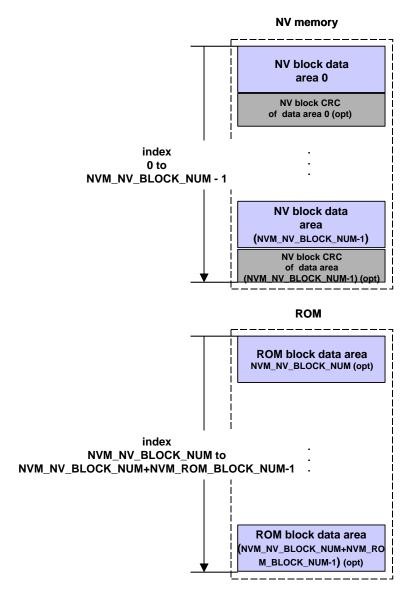


Figure 10: Dataset NVRAM block layout

Note: This figure does not show the physical NV memory layout of a Dataset NVRAM block. Only the logical clustering is shown.] ()

7.1.4.7 NVRAM Manager API configuration classes

[SWS_NvM_00149] To have the possibility to adapt the NvM module to limited hardware resources, three different API configuration classes shall be defined:

- API configuration class 3: All specified API calls are available. A maximum of functionality is supported.
- API configuration class 2: An intermediate set of API calls is available.
- API configuration class 1: Especially for matching systems with very limited hardware resources this API configuration class offers only a minimum set of API calls which are required in any case.] ()



[SWS_NvM_00560] [API configuration class 3 shall consist of the following API: Type 1: NvM_SetDataIndex(...) NvM_GetDataIndex(...) NvM_SetBlockProtection(...) NvM_GetErrorStatus(...) NvM SetRamBlockStatus(...) NvM_SetBlockLockStatus Type 2: NvM ReadBlock(...) NvM WriteBlock(...) NvM_RestoreBlockDefaults(...) NvM EraseNvBlock(...) NvM_InvalidateNvBlock(...) NvM CancelJobs(...) – NvM_ReadPRAMBlock(...) NvM WritePRAMBlock(...) NvM_RestorePRAMBlockDefaults(...) Type 3: NvM_ReadAll(...) NvM WriteAll(...) NvM_CancelWriteAll(...) Type 4: – NvM_Init(...) | () **ISWS NvM 005611** [API configuration class 2 shall consist of the following API: Type 1: NvM_SetDataIndex(...) NvM GetDataIndex(...) NvM GetErrorStatus(...) NvM SetRamBlockStatus(...) NvM SetBlockLockStatus Type 2: NvM_ReadBlock(...) NvM_WriteBlock(...) NvM RestoreBlockDefaults(...) NvM CancelJobs(...) NvM_ReadPRAMBlock(...) NvM_WritePRAMBlock(...) NvM_RestorePRAMBlockDefaults(...) Type 3: NvM ReadAll(...) NvM_WriteAll(...) NvM_CancelWriteAll(...)

Type 4:

– NvM_Init(...) ()



[SWS_NvM_00562] [API configuration class 1 shall consist of the following API: Type 1:

- NvM_GetErrorStatus(...)
- NvM_SetRamBlockStatus(...)
- NvM SetBlockLockStatus

Type 2:

_ _

Type 3:

- NvM_ReadAll(...)
- NvM_WriteAll(...)
- NvM_CancelWriteAll(...)

Type 4:

NvM_Init(...)

Note: For API configuration class 1 no queues are needed, no immediate data can be written. Furthermore the API call NvM_SetRamBlockStatus is only available if configured by NvMSetRamBlockStatusApi.] ()

[SWS_NvM_00365] [Within API configuration class 1, the block management type NVM_BLOCK_DATASET is not supported.] ()

For information regarding the definition of Type 1...4 refer to chapter 8.1.5.

[SWS_NvM_00150] [The NvM module shall only contain that code that is needed to handle the configured block types. | ()

7.1.5 Scan order / priority scheme

[SWS_NvM_00032] [The NvM module shall support a priority based job processing.] (SRS_Mem_08542)

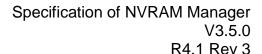
[SWS_NvM_00564] [By configuration parameter NvMJobPrioritization [SWS_NvM_00028] priority based job processing shall be enabled/disabled.] (SRS_Mem_08542)

[SWS_NvM_00378] [In case of priority based job processing order, the NvM module shall use two queues, one for immediate write jobs (crash data) another for all other jobs (including immediate read/erase jobs). | (SRS_Mem_08542)

[SWS_NvM_00379] If priority based job processing is disabled via configuration, the NvM module shall not support immediate write jobs. In this case, the NvM module processes all jobs in FCFS order.] ()

[SWS_NvM_00380] [The job queue length for multi block requests originating from the NvM_ReadAll and NvM_WriteAll shall be one (only one job is queued).] ()

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[SWS_NvM_00381] [The NvM module shall not interrupt jobs originating from the NvM_ReadAll request by other requests. | ()

Note: The only exception to the rule given in [SWS_NvM_00381, SWS_NvM_00567] is a write job with immediate priority which shall preempt the running read / write job [SWS_NvM_00182]. The preempted job shall subsequently be resumed / restarted by the NvM module.

[SWS_NvM_00567] [The NvM module shall not interrupt jobs originating from the NvM_WriteAll request by other requests.] ()

[SWS_NvM_00568] [The NvM module shall rather queue read jobs that are requested during an ongoing NvM_ReadAll request and executed them subsequently.]()

[SWS_NvM_00569] [The NvM module shall rather queue write jobs that are requested during an ongoing NvM_WriteAll request and executed them subsequently.] ()

[SWS_NvM_00725] [The NvM module shall rather queue write jobs that are requested during an ongoing NvM_ReadAll request and executed them subsequently.]()

[SWS_NvM_00726] [The NvM module shall rather queue read jobs that are requested during an ongoing NvM_WriteAll request and executed them subsequently.]()

Note: The NvM_WriteAll request can be aborted by calling NvM_CancelWriteAll. In this case, the current block is processed completely but no further blocks are written [SWS_NvM_00238].

Hint: It shall be allowed to dequeue requests, if they became obsolete by completion of the regarding NVRAM block.

[SWS_NvM_00570] [The preempted job shall subsequently be resumed / restarted by the NvM module. This behavior shall apply for single block requests as well as for multi block requests.] ()



7.2 General behavior

7.2.1 Functional requirements

[SWS_NvM_00383] [For each asynchronous request, a notification of the caller after completion of the job shall be a configurable option.] ()

[SWS_NvM_00384] [The NvM module shall provide a callback interface <u>SWS_NvM_00113</u>.

Hint: The NvM module's environment shall access the non-volatile memory via the NvM module only. It shall not be allowed for any module (except for the NvM module) to access the non-volatile memory directly.] (SRS_Mem_08541)

[SWS_NvM_00038] [The NvM module only provides an implicit way of accessing blocks in the NVRAM and in the shared memory (RAM). This means, the NvM module copies one or more blocks from NVRAM to the RAM and the other way round. | ()

[SWS_NvM_00692] [The application accesses the RAM data directly, with respect to given restrictions (e.g. synchronization).] ()

[SWS_NvM_00385] [The NvM module shall queue all asynchronous "single block" read/write/control requests if the block with its specific ID is not already queued or currently in progress (multitasking restrictions).] ()

[SWS_NvM_00386] [The NvM module shall accept multiple asynchronous "single block" requests as long as no queue overflow occurs. | ()

[SWS_NvM_00155] [The highest priority request shall be fetched from the queues by the NvM module and processed in a serialized order.] ()

[SWS_NvM_00040] [The NvM module shall implement implicit mechanisms for consistency / integrity checks of data saved in NV memory [SWS_NvM_00165]. | ()

[SWS_NvM_00156] [Depending on implementation of the memory stack, callback routines provided and/or invoked by the NvM module may be called in interrupt context.

Hint: The NvM module providing routines called in interrupt context has therefore to make sure that their runtime is reasonably short. | ()

[SWS_NvM_00085] [If there is no default ROM data available at configuration time or no callback defined by NvMInitBlockCallback then the application shall be responsible for providing the default initialization data.



In this case, the application has to use NvM_GetErrorStatus() to be able to distinguish [ECUC_NvM_00061] between first initialization and corrupted data [SWS_NvM_00083].] ()

[SWS_NvM_00387] [During processing of NvM_ReadAll, the NvM module shall be able to detect corrupted RAM data by performing a checksum calculation. [ECUC_NvM_00476].] ()

[SWS_NvM_00226] [During processing of NvM_ReadAll, the NvM module shall be able to detect invalid RAM data by testing the validity of a data within the administrative block [ECUC_NvM_00476].] ()

[SWS_NvM_00388] [During startup phase and normal operation of NvM_ReadAll and if the NvM module has detected an unrecoverable error within the NV block, the NvM module shall copy default data (if configured) to the corresponding RAM block.] ()

[SWS_NvM_00332] [To make use of the OS services, the NvM module shall only use the BSW scheduler instead of directly making use of OS objects and/or related OS services.] (SRS_BSW_00429)

7.2.2 Design notes

7.2.2.1 NVRAM manager startup

[SWS_NvM_00693] [NvM_Init shall be invoked by the ECU state manager exclusively.] ()

[SWS_NvM_00091] Due to strong constraints concerning the ECU startup time, the NvM_Init request shall not contain the initialization of the configured NVRAM blocks.

[SWS_NvM_00157] The NvM_Init request shall not be responsible to trigger the initialization of underlying drivers and memory hardware abstraction. This shall also be handled by the ECU state manager. | (SRS_Mem_00011)

[SWS_NvM_00158] [The initialization of the RAM data blocks shall be done by another request, namely NvM_ReadAll.] ()

NvM_ReadAll shall be called exclusively by the ECU state manager if EcuM Fixed is used or by integration code if EcuM Flex is used.



[SWS_NvM_00694] [Software components which use the NvM module shall be responsible for checking global error/status information resulting from the NvM module startup. The ECU state manager shall use polling by using NvM_GetErrorStatus [SWS_NvM_00015] (reserved block ID 0) or callback notification (configurable option NvM_MultiBlockCallback [SWS_NvM_00028]) to derive global error/status information resulting from startup. If polling is used, the end of the NVRAM startup procedure shall be detected by the global error/status NVM_REQ_OK or NVM_REQ_NOT_OK (during startup NVM_REQ_PENDING) [SWS_NvM_00083]. If callbacks are chosen for notification, software components shall be notified automatically if an assigned NVRAM block has been processed [SWS_NvM_00281].

Note 1: If callbacks are configured for each NVRAM block which is processed within NvM_ReadAll, they can be used by the RTE to start e.g. SW-Cs at an early point of time.

Note 2: To ensure that the DEM is fully operational at an early point of time, i.e. its NV data is restored to RAM, DEM related NVRAM blocks should be configured to have a low ID to be processed first within NvM_ReadAll. \(\) ()

[SWS_NvM_00160] [The NvM module shall not store the currently used Dataset index automatically in a persistent way.

Software components shall check the specific error/status of all blocks they are responsible for by using NvM_GetErrorStatus [SWS_NvM_00015] with specific block IDs to determine the validity of the corresponding RAM blocks.] ()

[SWS_NvM_00695] [For all blocks of the block management type "NVRAM Dataset" [SWS_NvM_00006] the software component shall be responsible to set the proper index position by NvM_SetDataIndex [SWS_NvM_00014]. E.g. the current index position can be stored/maintained by the software component in a unique NVRAM block. To get the current index position of a "Dataset Block", the software component shall use the NvM_GetDataIndex [SWS_NvM_00021] API call. | ()

7.2.2.2 NVRAM manager shutdown

[SWS_NvM_00092] [The basic shutdown procedure shall be done by the request NvM_WriteAll [SWS_NvM_00018].

Hint: NvM WriteAll shall be invoked by the ECU state manager. | ()

7.2.2.3 (Quasi) parallel write access to the NvM module

[SWS_NvM_00162] The NvM module shall receive the requests via an asynchronous interface using a queuing mechanism. The NvM module shall process all requests serially depending on their priority. J (SRS_Mem_00013, SRS_Mem_00034)



7.2.2.4 NVRAM block consistency check

[SWS_NvM_00164] [The NvM module shall provide implicit techniques to check the data consistency of NVRAM blocks [ECUC_NvM_00476], [SWS_NvM_00040].] (SRS_Mem_08547, SRS_Mem_00030)

[SWS_NvM_00571] The data consistency check of a NVRAM block shall be done by CRC recalculations of its corresponding NV block(s). (SRS_Mem_08547)

[SWS_NvM_00165] [The implicit way of a data consistency check shall be provided by configurable options of the internal functions. The implicit consistency check shall be configurable for each NVRAM block and depends on the configurable parameters NvMBlockUseCrc and NvMCalcRamBlockCrc [ECUC NvM 00061].] (SRS_Mem_08547, SRS_Mem_00129)

[SWS_NvM_00724] [NvMBlockUseCrc should be enabled for NVRAM blocks where NvMWriteBlockOnce = TRUE. NvMBlockWriteProt should be disabled for NVRAM blocks where NvMWriteBlockOnce = TRUE, to enable the user to write data to the NVRAM block in case of CRC check is failed.] ()

[SWS_NvM_00544] [Depending on the configurable parameters NvMBlockUseCrc and NvMCalcRamBlockCrc, NvM module shall allocate memory for the largest CRC used.

Hint: NvM users must not know anything about CRC memory (e.g. size, location) for their data in a RAM block. | ()

7.2.2.5 Error recovery

[SWS_NvM_00047] [The NvM module shall provide techniques for error recovery. The error recovery depends on the NVRAM block management type [SWS_NvM_00001].] ()

[SWS_NvM_00389] [The NvM module shall provide error recovery on read for every kind of NVRAM block management type by loading of default values.] ()

[SWS_NvM_00390] The NvM module shall provide error recovery on read for NVRAM blocks of block management type NVM_BLOCK_REDUNDANT by loading the RAM block with default values. ()



[SWS_NvM_00168] The NvM module shall provide error recovery on write by performing write retries regardless of the NVRAM block management type. ()

[SWS_NvM_00169] [The NvM module shall provide read error recovery on startup for all NVRAM blocks with configured RAM block CRC in case of RAM block revalidation failure.] ()

7.2.2.6 Recovery of a RAM block with ROM data

[SWS_NvM_00171] [The NvM module shall provide implicit and explicit recovery techniques to restore ROM data to its corresponding RAM block in case of unrecoverable data inconsistency of a NV block [SWS_NvM_00387, SWS_NvM_00226, SWS_NvM_00388]. | (SRS_Mem_08549, SRS_Mem_08010)

7.2.2.7 Implicit recovery of a RAM block with ROM default data

[SWS_NvM_00172] [The data content of the corresponding NV block shall remain unmodified during the implicit recovery.] (SRS_Mem_08010)

[SWS_NvM_00572] [The implicit recovery shall not be provided during startup (part of NvM_ReadAll) and NvM_ReadBlock or NvM_ReadPRAMBlock for each NVRAM block when no ROM block is configured. | ()

[SWS_NvM_00573] [The implicit recovery shall not be provided during startup (part of NvM_ReadAll) and NvM_ReadBlock or NvM_ReadPRAMBlock for each NVRAM block for the following conditions:

- The ROM block is configured.
- The permanent RAM block or the content of the RAM mirror in the NvM module (in case of explicit synchronization) state is valid and CRC (data) is consistent. | ()

[SWS_NvM_00574] [The implicit recovery shall not be provided during startup (part of NvM_ReadAll) and NvM_ReadBlock or NvM_ReadPRAMBlock for each NVRAM block for the following conditions:

- The ROM block is configured.
- The permanent RAM block or the content of the RAM mirror in the NvM module (in case of explicit synchronization) state is invalid and CRC (data) is inconsistent.
- Read attempt from NV success. | ()



[SWS_NvM_00575] The implicit recovery shall be provided during startup (part of NvM_ReadAll) and NvM_ReadBlock or NvM_ReadPRAMBlock for each NVRAM block for the following conditions:

- The ROM block is configured.
- The permanent RAM block state or the content of the RAM mirror in the NvM module (in case of explicit synchronization) is invalid and CRC (data) is inconsistent.
- Read attempt from NV fails. | ()

7.2.2.8 Explicit recovery of a RAM block with ROM default data

[SWS_NvM_00391] For explicit recovery with ROM block data the NvM module shall provide functions NvM_RestoreBlockDefaults and NvM_RestorePRAMBlockDefaults [SWS_NvM_00012] to restore ROM data to its corresponding RAM block. | ()

[SWS_NvM_00392] [The function NvM_RestoreBlockDefaults and NvM_RestorePRAMBlockDefaults shall remain unmodified the data content of the corresponding NV block.

Hint: The function NvM_RestoreBlockDefaults or NvM_RestorePRAMBlockDefaults shall be used by the application to restore ROM data to the corresponding RAM block every time it is needed.] ()

7.2.2.9 Detection of an incomplete write operation to a NV block

[SWS_NvM_00174] [The detection of an incomplete write operation to a NV block is out of scope of the NvM module. This is handled and detected by the memory hardware abstraction. The NvM module expects to get information from the memory hardware abstraction if a referenced NV block is invalid or inconsistent and cannot be read when requested.

SW-Cs may use NvM_InvalidateNvBlock to prevent lower layers from delivering old data. | (SRS Mem 08547)

7.2.2.10 Termination of a single block request

[SWS_NvM_00175] [All asynchronous requests provided by the NvM module (except for NvM_CancelWriteAll) shall indicate their result in the designated error/status field of the corresponding Administrative block [SWS_NvM_00000]. | ()

[SWS_NvM_00176] [The optional configuration parameter NvMSingleBlockCallback configures the notification via callback on the termination of an asynchronous block request (except for NvM CancelWriteAll) [ECUC NvM 00061].



Note: In communication with application SW-C, the NvMSingleBlockCallback shall be mapped to the Rte call <o> API.] ()

7.2.2.11 Termination of a multi block request

[SWS_NvM_00393] [The NvM module shall use a separate variable to store the result of an asynchronous multi block request (NvM_ReadAll, NvM_WriteAll including NvM CancelWriteAll).]()

[SWS_NvM_00394] [The function NvM_GetErrorStatus [SWS_NvM_00015] shall return the most recent error/status information of an asynchronous multi block request (including NvM_CancelWriteAll) [SWS_NvM_00083] in conjunction with a reserved block ID value of 0.] ()

[SWS_NvM_00395] [The result of a multi block request shall represent only a common error/status information.] ()

[SWS_NvM_00396] [The multi block requests provided by the NvM module shall indicate their detailed error/status information in the designated error/status field of each affected Administrative block. | ()

[SWS_NvM_00179] [The optional configuration parameter NvMMultiBlockCallback configures the notification via callback on the termination of an asynchronous multi block request [SWS_NvM_00028].] ()

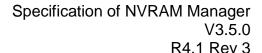
7.2.2.12 General handling of asynchronous requests/ job processing

[SWS_NvM_00180] [Every time when CRC calculation is processed within a request, the NvM module shall calculate the CRC in multiple steps if the referenced NVRAM block length exceeds the number of bytes configured by the parameter NvMCrcNumOfBytes. | ()

[SWS_NvM_00351] For CRC calculation, the NvM module shall use initial values which are published by the CRC module.] ()

[SWS_NvM_00181] [Multiple concurrent single block requests shall be queueable.]

[SWS_NvM_00182] [The NvM module shall interrupt asynchronous request/job processing in favor of jobs with immediate priority (crash data). | ()





[SWS_NvM_00184] [If the invocation of an asynchronous function on the NvM module leads to a job queue overflow, the function shall return with E NOT OK.] ()

[SWS_NvM_00185] [On successful enqueuing a request, the NvM module shall set the request result of the corresponding NVRAM block to NVM REQ PENDING.] ()

[SWS_NvM_00270] [If the NvM module has successfully processed a job, it shall return NVM_REQ_OK as job result.] ()

7.2.2.13 NVRAM block write protection

The NvM module shall offer different kinds of write protection which shall be configurable. Every kind of write protection is only related to the NV part of NVRAM block, i.e. the RAM block data can be modified but not be written to NV memory.

[SWS_NvM_00325] [Enabling/Disabling of the write protection is allowed using NvM_SetBlockProtection function when the NvMWriteBlockOnce is FALSE regardless of the value (True/False) configured for NvMBlockWriteProt. J (SRS_Mem_08009)

[SWS_NvM_00577] [Enabling/Disabling of the write protection is not allowed using NvM_SetBlockProtection function when the NvMWriteBlockOnce is TRUE regardless of the value (True/False) configured for NvMBlockWriteProt.] (SRS_Mem_08009)

[SWS_NvM_00326] For all NVRAM blocks configured with NvMBlockWriteProt = TRUE, the NvM module shall enable a default write protection. J (SRS_Mem_08009)

[SWS_NvM_00578] [The NvM module's environment can explicitly disable the write protection using the NvM_SetBlockProtection function.] ()

[SWS_NvM_00397] [For NVRAM blocks configured with NvMWriteBlockOnce == TRUE [NVM072], the NvM module shall only write once to the associated NV memory, i.e in case of a blank NV device. | (SRS_Mem_08015)

[SWS_NvM_00398] [For NVRAM blocks configured with NvMWriteBlockOnce == TRUE, the NvM module shall not allow disabling the write protection explicitly using the NvM_SetBlockProtection function.[SWS_NvM_00450] | ()

7.2.2.14 Validation and modification of RAM block data

This chapter shall give summarized information regarding the internal handling of NVRAM Manager status bits. Depending on different API calls, the influence on the



status of RAM blocks shall be described in addition to the specification items located in chapter 8.1.3. The following figures depict the state transitions of RAM blocks.

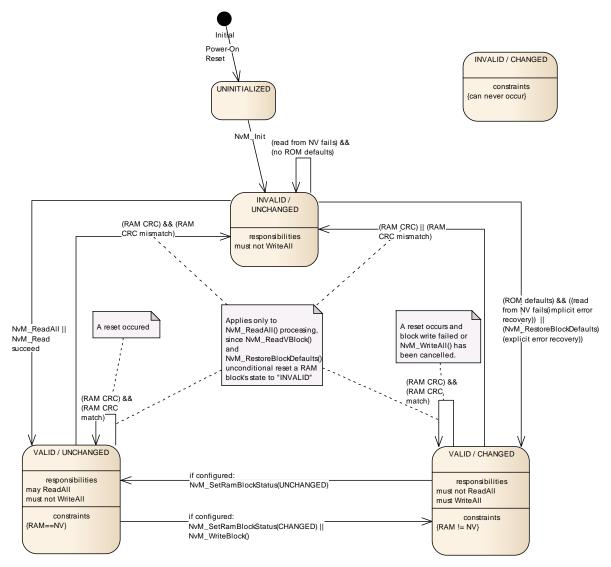
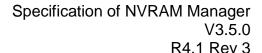


Figure 11: RAM Block States

After the Initialization the RAM Block is in state INVALID/UNCHANGED until it is updated via NvM_ReadAll, which causes a transition to state VALID/UNCHANGED. In this state WriteAll is not allowed. This state is left, if the NvM_SetRamBlockStatus is invoked. If there occurs a CRC error the RAM Block changes to state INVALID again, which than can be left via the implicit or explicit error recovery mechanisms. After error recovery the block is in state VALID/CHANGED as the content of the RAM differs from the NVRAM content.

[SWS_NvM_00344] [If the API for modifying the RAM block status has been configured out (via NvMSetRamBlockStatusApi or NvMBlockUseSetRamBlockStatus) the NvM module shall treat a RAM block or the RAM mirror in the NvM module (in case of explicit synchronization) as valid and





modified when writing to it, i.e. during NvM_WriteAll, the NvM module shall write each permanent RAM block to NV memory.] (SRS_Mem_08550)

[SWS_NvM_00345] If the API for modifying the RAM block status has been configured out (via NvMSetRamBlockStatusApi or NvMBlockUseSetRamBlockStatus) the NvM module shall treat a RAM block as invalid when reading it, i.e. during NvM_ReadAll, the NvM module shall copy each NVRAM block to RAM if configured accordingly. J (SRS_Mem_08550)

[SWS_NvM_00696] [In case of an unsuccessful block read attempt, it is the responsibility of the application to provide valid data before the next write attempt.] (SRS_Mem_08550)

[SWS_NvM_00472] [In case a RAM block is successfully copied to NV memory the RAM block state shall be set to "valid/unmodified" afterwards.

] (SRS_Mem_08541)

7.2.2.15 Communication and implicit synchronization between application and NVRAM manager

To minimize locking/unlocking overhead or the use of other synchronization methods, the communication between applications and the NvM module must follow a strict sequence of steps which is described below. This ensures a reliable communication between applications and the NvM module and avoids data corruption in RAM blocks and a proper synchronization is guaranteed.

This access model assumes that two parties are involved in communication with a RAM block: The application and the NvM module.

[SWS_NvM_00697] [If several applications are using the same RAM block it is not the job of the NvM module to ensure the data integrity of the RAM block. In this case, the applications have to synchronize their accesses to the RAM block and have to guarantee that no unsuitable accesses to the RAM block take place during NVRAM operations (details see below).

Especially if several applications are sharing a NVRAM block by using (different) temporary RAM blocks, synchronization between applications becomes more complex and this is not handled by the NvM module, too. In case of using callbacks as notification method, it could happen that e.g. an application gets a notification although the request has not been initiated by this application.

All applications have to adhere to the following rules. | ()

7.2.2.15.1 Write requests (NvM WriteBlock or NvM WritePRAMBlock)

[SWS_NvM_00698] [Applications have to adhere to the following rules during write request for implicit synchronization between application and NVRAM manager:



- 1. The application fills a RAM block with the data that has to be written by the NvM module
- 2. The application issues the NvM_WriteBlock or NvM_WritePRAMBlock request which transfers control to the NvM module.
- 3. From now on the application must not modify the RAM block until success or failure of the request is signaled or derived via polling. In the meantime the contents of the RAM block may be read.
- 4. An application can use polling to get the status of the request or can be informed via a callback function asynchronously.
- 5. After completion of the NvM module operation, the RAM block is reusable for modifications. J (SRS_Mem_00013)

7.2.2.15.2 Read requests (NvM_ReadBlock or NvM_ReadPRAMBlock)

[SWS_NvM_00699] [Applications have to adhere to the following rules during read request for implicit synchronization between application and NVRAM manager:

- 1. The application provides a RAM block that has to be filled with NVRAM data from the NvM module's side.
- 2. The application issues the NvM_ReadBlock request which transfers control to the NvM module.
- 3. From now on the application must not read or write to the RAM block until success or failure of the request is signaled or derived via polling.
- 4. An application can use polling to get the status of the request or can be informed via a callback function.
- 5. After completion of the NvM module operation, the RAM block is available with new data for use by the application. | (SRS_Mem_00013)

7.2.2.15.3 Restore default requests (NvM_RestoreBlockDefaults and NvM_RestorePRAMBlockDefaults)

[SWS_NvM_00700] [Applications have to adhere to the following rules during restore default requests for implicit synchronization between application and NVRAM manager:

- 1. The application provides a RAM block, which has to be filled with ROM data from the NvM modules side.
- The application issues the NvM_RestoreBlockDefaults or NvM_RestorePRAMBlockDefaults request which transfers control to the NvM module
- 3. From now on the application must not read or write to the RAM block until success or failure of the request is signaled or derived via polling.
- 4. An application can use polling to get the status of the request or can be informed via a callback function.
- 5. After completion of the NvM module operation, the RAM block is available with the ROM data for use by the application. J (SRS_Mem_08548)



7.2.2.15.4 Multi block read requests (NvM ReadAll)

This request may be triggered only by the ECU state manager if EcuM Fixed is used or by integration code if EcuM Flex is used at system startup.

This request fills all configured permanent RAM blocks with necessary data for startup.

If the request fails or the request is handled only partially successful, the NVRAM-Manager signals this condition to the DEM and returns an error to the ECU state manager. The DEM and the ECU state manager have to decide about further measures that have to be taken. These steps are beyond the scope of the NvM module and are handled in the specifications of DEM and ECU state manager.

[SWS_NvM_00701] [Applications have to adhere to the following rules during multi block read requests for implicit synchronization between application and NVRAM manager:

The ECU state manager issues the NvM_ReadAll.

- 1. The ECU state manager can use polling to get the status of the request or can be informed via a callback function.
- 2. During NvM_ReadAll, a single block callback (if configured) will be invoked after having completely processed a NVRAM block. These callbacks enable the RTE to start each SW-C individually.] ()

7.2.2.15.5 Multi block write requests (NvM_WriteAll)

This request must only be triggered by the ECU state manager at shutdown of the system. This request writes the contents of all modified permanent RAM blocks to NV memory. By calling this request only during ECU shutdown, the ECU state manager can ensure that no SW component is able to modify data in the RAM blocks until the end of the operation. These measures are beyond the scope of the NvM module and are handled in the specifications of the ECU state manager.

[SWS_NvM_00702] [Applications have to adhere to the following rules during multi block write requests for implicit synchronization between application and NVRAM manager:

- 1. The ECU state manager issues the NvM_WriteAll request which transfers control to the NvM module.
- 2. The ECU state manager can use polling to get the status of the request or can be informed via a callback function.] ()

7.2.2.15.6 Cancel Operation (NvM_CancelWriteAll)

This request cancels a pending NvM_WriteAll request. This is an asynchronous request and can be called to terminate a pending NvM_WriteAll request.

[SWS_NvM_00703] [NvM_CancelWriteAll request shall only be used by the ECU state manager.] ()



7.2.2.15.7 Modification of administrative blocks

For administrative purposes an administrative block is part of each configured NVRAM block (ref. to ch. 7.1.3.4).

[SWS NvM 00704] [If there is a pending single-block operation for a NVRAM block, the application is not allowed to call any operation that modifies the administrative block, like NvM SetDataIndex, NvM SetBlockProtection, NvM SetRamBlockStatus, until the pending job has finished. | ()

7.2.2.16 Normal and extended runtime preparation of NVRAM blocks

This subchapter is supposed to provide a short summary of normal and extended runtime preparation of NVRAM blocks. The detailed behavior regarding the handling of NVRAM blocks during start-up is specified in chapter 8.1.3.3.1.

Depending on the two configuration parameters NvMDynamicConfiguration and NvMResistantToChangedSw the NVRAM Manager shall behave in different ways during start-up, i.e. while processing the request NvM_ReadAll().

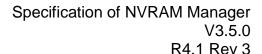
If NvMDynamicConfiguration is set to FALSE, the NVRAM Manager shall ignore the stored configuration ID (see SWS_NvM_00034) and continue with the normal runtime preparation of NVRAM blocks. In this case the RAM block shall be checked for its validity. If the RAM block content is detected to be invalid the NV block shall be checked for its validity. A NV block which is detected to be valid shall be copied to its assigned RAM block. If an invalid NV Block is detected default data shall be loaded. If NvMDynamicConfiguration is set to TRUE and a configuration ID mismatch is detected, the extended runtime preparation shall be performed for those NVRAM blocks which are configured with NvMResistantToChangedSw(FALSE). In this case default data shall be loaded independent of the validity of an assigned RAM or NV block.

7.2.2.17 Communication and explicit synchronization between application and NVRAM manager

In contrast to the implicit synchronization between the application and the NvM module (see section 7.2.2.15) an optional (i.e. configurable) explicit synchronization mechanism is available. It is realized by a RAM mirror in the NvM module. The data is transferred by the application in both directions via callback routines, called by the NvM module.

Here is a short analysis of this mechanism:

- The advantage is that applications can control their data in a better way. They are responsible for copying consistent data to and from the NvM module's RAM mirror, so they know the point in time. The RAM block is never in an inconsistent state due to concurrent accesses.
- The drawbacks are the additional RAM which needs to have the same size as the largest NVRAM block that uses this mechanism and the necessity of an additional copy between two RAM locations for every operation.





This mechanism especially enables the sharing of NVRAM blocks by different applications, if there is a module that synchronizes these applications and is the owner of the NVRAM block from the NvM module's perspective.

[SWS_NvM_00511] For every NVRAM block there shall be the possibility to configure the usage of an explicit synchronization mechanism by the parameter NvMBlockUseSyncMechanism.] ()

[SWS_NvM_00512] [The NvM module must not allocate a RAM mirror if no block is configured to use the explicit synchronization mechanism.] ()

[SWS_NvM_00513] [The NvM module shall allocate only one RAM mirror if at least one block is configured to use the explicit synchronization mechanism. This RAM mirror must not exceed the size of the longest NVRAM block configured to use the explicit synchronization mechanism. | ()

[SWS_NvM_00514] The NvM module shall use the internal mirror as buffer for all operations that read and write the RAM block of those NVRAM blocks with NvMBlockUseSyncMechanism == TRUE. The buffer must not be used for the other NVRAM blocks.] ()

[SWS_NvM_00515] [The NvM module shall call the routine NvMWriteRamBlockToNvM in order to copy the data from the RAM block to the mirror for all NVRAM blocks with NvMBlockUseSyncMechanism == TRUE. This routine must not be used for the other NVRAM blocks.] ()

[SWS_NvM_00516] The NvM module shall call the routine NvMReadRamBlockFromNvM in order to copy the data from the mirror to the RAM block for all NVRAM blocks with NvMBlockUseSyncMechanism == TRUE. This routine must not be used for the other NVRAM blocks. | ()

[SWS_NvM_00517] [During a single block request if the routines NvMReadRamBlockFromNvM return E_NOT_OK, then the NvM module shall retry the routine call NvMRepeatMirrorOperations times. Thereafter the single block read job shall set the block specific job result to NVM_REQ_NOT_OK and shall report NVM E REQ_FAILED to the DEM. | ()

[SWS_NvM_00839] In the case the NvMReadRamBlockFromNvM routine returns E_NOT_OK, the NvM module shall retry the routine call in the next call of the NvM MainFunction. ()

[SWS_NvM_00579] [During a single block request if the routines NvMWriteRamBlockToNvM return E_NOT_OK, then the NvM module shall retry the routine call NvMRepeatMirrorOperations times. Thereafter the single block write job



shall set the block specific job result to NVM_REQ_NOT_OK and shall report NVM_E_REQ_FAILED to the DEM. | ()

[SWS_NvM_00840] In the case the NvMWriteRamBlockToNvM routine returns E_NOT_OK, the NvM module shall retry the routine call in the next call of the NvM_MainFunction. ()

[SWS_NvM_00837] 「During a multi block request(NvM_WriteAll) if the routines NvMWriteRamBlockToNvM return E_NOT_OK, then the NvM module shall retry the routine call NvMRepeatMirrorOperations times. Thereafter the job of the function NvM_WriteAll shall set the block specific job result to NVM_REQ_NOT_OK and shall report NVM_E_REQ_FAILED to the DEM.」()

[SWS_NvM_00838] During a multi block request(NvM_ReadAll) if the routines NvMReadRamBlockFromNvM return E_NOT_OK, then the NvM module shall retry the routine call NvMRepeatMirrorOperations times. Thereafter the job of the function NvM_ReadAll shall set the block specific job result to NVM_REQ_NOT_OK and shall report NVM_E_REQ_FAILED to the DEM. ()

The following two sections clarify the differences when using the explicit synchronization mechanism, compare to 7.2.2.15.1 and 7.2.2.15.2.

7.2.2.17.1 Write requests (NvM_WriteBlock or NvM_WritePRAMBlock)

[SWS_NvM_00705] [Applications have to adhere to the following rules during write request for explicit synchronization between application and NVRAM manager:

- 1. The application fills a RAM block with the data that has to be written by the NvM module.
- 2. The application issues the NvM_WriteBlock or NvM_WritePRAMBlock request.
- 3. The application might modify the RAM block until the routine NvMWriteRamBlockToNvM is called by the NvM module.
- 4. If the routine NvMWriteRamBlockToNvM is called by the NvM module, then the application has to provide a consistent copy of the RAM block to the requested destination the NvM by The application can use the return value E_NOT_OK in order to signal that data not consistent. NvMmodule The will accept NvMRepeatMirrorOperations times and then postpones the request and continues with its next request.
- 5. Continuation only if data was copied to the NvM module:
- 6. From now on the application can read and write the RAM block again.
- 7. An application can use polling to get the status of the request or can be informed via a callback routine asynchronously.

Note: The application may combine several write requests to different positions in one RAM block, if NvM_WriteBlock or NvM_WritePRAMBlock was requested, but not



yet processed by the NvM module. The request was not processed, if the callback routine NvMWriteRamBlockToNvM was not called.] ()

7.2.2.17.2 Read requests (NvM ReadBlock or NvM ReadPRAMBlock)

[SWS_NvM_00706] [Applications have to adhere to the following rules during read request for explicit synchronization between application and NVRAM manager:

- 1. The application provides a RAM block that has to be filled with NVRAM data from the NvM module's side.
- 2. The application issues the NvM_ReadBlock or NvM_ReadPRAMBlock request.
- 3. The application might modify the RAM block until the routine NvMReadRamBlockFromNvM is called by the NvM module.
- 4.If the routine NvMReadRamBlockFromNvM is called by the NvM module, then the application copy the data from the destination given by the NvM module to the RAM block. The application can use the return value E_NOT_OK in order to signal that data was not copied. The NvM module will accept this NvMRepeatMirrorOperations times and then postpones the request and continues with its next request.
- 5. Continuation only if data was copied from the NvM module:
- 6. Now the application finds the NV block values in the RAM block.
- 7. The application can use polling to get the status of the request or can be informed via a callback routine.

Note: The application may combine several read requests to different positions in one NV block, if NvM_ReadBlock or NvM_ReadPRAMBlock was requested, but not yet processed by the NvM module. The request was not processed, if the callback routine NvMReadRamBlockFromNvM was not called.

Note: NvM_RestoreBlockDefaults and NvM_RestorePRAMBlockDefaults works similarly to NvM_ReadBlock.] ()

7.2.2.17.3 Multi block read requests (NvM_ReadAll)

This request may be triggered only by the ECU state manager at system startup. This request fills all configured permanent RAM blocks with necessary data for startup.

If the request fails or the request is handled only partially successful, the NVRAM-Manager signals this condition to the DEM and returns an error to the ECU state manager. The DEM and the ECU state manager have to decide about further measures that have to be taken. These steps are beyond the scope of the NvM module and are handled in the specifications of DEM and ECU state manager.

Normal operation:

- 1. The ECU state manager issues the NvM ReadAll.
- 2. The ECU state manager can use polling to get the status of the request or can be informed via a callback function.



- 3. During NvM_ReadAll job, if a synchronization callback (NvM_ReadRamBlockFromNvm) is configured for a block it will be called by the NvM module. In this callback the application shall copy the data from the destination given by the NvM module to the RAM block. The application can use the return value E_NOT_OK in order to signal that data was not copied. The NvM module will accept this NvMRepeatMirrorOperations times and then report the read operation as failed.
- 4. Now the application finds the NV block values in the RAM block if the read operation was successful.
- 5. During NvM_ReadAll, a single block callback (if configured) will be invoked after having completely processed a NVRAM block. These callbacks enable the RTE to start each SW-C individually.

7.2.2.17.4 Multi block write requests (NvM WriteAll)

This request must only be triggered by the ECU state manager at shutdown of the system. This request writes the contents of all modified permanent RAM blocks to NV memory. By calling this request only during ECU shutdown, the ECU state manager can ensure that no SW component is able to modify data in the RAM blocks until the end of the operation. These measures are beyond the scope of the NvM module and are handled in the specifications of the ECU state manager.

Normal operation:

- 1. The ECU state manager issues the NvM_WriteAll request which transfers control to the NvM module.
- synchronization 2. During callback NvM WriteAll job, if а (NvM_WriteRamBlockToNvM) is configured for a block it will be called by the NvM module. In this callback the application has to provide a consistent copy of the RAM block to the destination requested by the NvM module. The application can use the return value E NOT OK in order to signal that not consistent. The NvMmodule accept will NvMRepeatMirrorOperations times and then report the write operation as failed.
- 3. Now the application can read and write the RAM block again.
- 4. The ECU state manager can use polling to get the status of the request or can be informed via a callback function.



7.2.2.18 Static Block ID Check

Note: NVRAM Manager stores the NV Block Header including the Static Block ID in the NV Block each time the block is written to NV memory. When a block is read, its Static Block ID is compared to the requested block ID. This permits to detect hardware failures which cause a wrong block to be read.

[SWS_NvM_00523] [The NVRAM Manager shall store the Static Block ID field of the Block Header each time the block is written to NV memory.] (SRS_Mem_08555)

[SWS_NvM_00524] 「The NVRAM Manager shall check the Block Header each time the block is read from NV memory. (SRS_Mem_08555)

[SWS_NvM_00580] [If the Static Block ID check fails then the read error recovery is initiated.

Hint: A check shall be made during configuration to ensure that all Static Block IDs are unique.] ()

7.2.2.19 Read Retry

[SWS_NvM_00526] [If the NVRAM manager detects a failure during a read operation from NV memory, a CRC error then one or more additional read attempts shall be made, as configured by NVM_MAX_NUM_OF_READ_RETRIES, before continuing to read the redundant NV Block. | (SRS_Mem_08554)

[SWS_NvM_00581] [If the NVRAM manager detects a failure during a read operation from NV memory, a CRC error then one or more additional read attempts shall be made, as configured by NVM_MAX_NUM_OF_READ_RETRIES, before continuing to read the ROM Block. | (SRS_Mem_08554)

[SWS_NvM_00582] [If the NVRAM manager detects a failure during a read operation from NV memory, a Static Block ID check then one or more additional read attempts shall be made, as configured by NVM_MAX_NUM_OF_READ_RETRIES, before continuing to read the redundant NV Block.] (SRS_Mem_00129)

[SWS_NvM_00583] [If the NVRAM manager detects a failure during a read operation from NV memory, a Static Block ID check then one or more additional read attempts shall be made, as configured by NVM_MAX_NUM_OF_READ_RETRIES, before continuing to read the ROM Block.] ()



7.2.2.20 Write Verification

When a RAM Block is written to NV memory the NV block shall be immediately read back and compared with the original content in RAM Block if the behaviour is enabled by NVM_WRITE_VERIFICATION.

[SWS_NvM_00527] [Comparison between original content in RAM Block and the block read back shall be performed in steps so that the number of bytes read and compared is not greater than as specified by the configuration parameter NVM_WRITE_VERIFICATION_DATA_SIZE.] (SRS_Mem_08554, SRS_Mem_08556)

[SWS_NvM_00528] If the original content in RAM Block is not the same as read back then the production code error NVM_E_VERIFY_FAILED shall be reported to DEM. (SRS_Mem_08556)

[SWS_NvM_00529] If the original content in RAM Block is not the same as read back then write retries shall be performed as specified in this document. (SRS_Mem_08554, SRS_Mem_08556)

[SWS_NvM_00530] [If the read back operation fails then no read retries shall be performed.] ()

7.2.2.21 NvM and BswM interaction

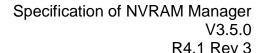
[SWS_NvM_00745] [If BswMMultiBlockJobStatusInformation is true the NVM shall inform the BSWM about the current state of a multi block job via BswM_NvM_CurrentJobMode and the configured multi job callback should not be called.] ()

[SWS_NvM_00746] [If BswMBlockStatusInformation is true, the NVM shall inform the BSWM about the current state of the block via BswM_NvM_CurrentBlockMode.] ()

7.2.2.22 NvM behaviour in case of Block locked

The NvM_SetBlockLockStatus API service shall only be usable by BSW Components, it is not published as Service in the SWC-Description. Thus it will not be accessible via RTE.

[SWS_NvM_00751] [If the API was called with parameter Locked as TRUE, the NVM shall guarantee that The NV contents associated to the NVRAM block identified





by BlockId, will not be modified by any request. The Block shall be skipped during NvM_WriteAll, other requests, that are NvM_WriteBlock, NvM_WritePRAMBlock, NvM_InvalidateNvBlock, NvM_EraseNvBlock, shall be rejected. | ()

[SWS_NvM_00752] [If the API was called with parameter Locked as TRUE, the NVM shall guarantee that at next start-up, during processing of NvM_ReadBlock or NvM_ReadPRAMBlock, this NVRAM block shall be loaded from NV memory. | ()

[SWS_NvM_00753] [If the Locked parameter got the value FALSE, the NVM shall guarantee normal processing of this NVRAM block as specified by AUTOSAR.] ()

[SWS_NvM_00754] [The setting made using this service shall not be changeable by NvM_SetRamBlockStatus, nor by NvM_SetBlockProtection.] ()

7.2.2.22.1 Use Case

Save new Data for an NVRAM block via diagnostic services into NV memory. These data shall be made available to the SW-C(s) with next ECU start-up, i.e. they shall neither be overwritten by a request originating from an SW-C, nor be overwritten with permanent RAM block's data during shut-down (NvM WriteAll).

7.2.2.22.2 Usage (by DCM):

- 1. DCM requests NvM_SetBlockLockStatus (<BlockId>, FALSE), in order to re-enable writing to this block. (It might be locked by executing this procedure before).
- 2. DCM requests NvM WriteBlock(<blockId>, <DataBuffer>)
- 3. DCM polls for completion of write request (using NvM GetErrorStatus())
- 4. On success (NVM_REQ_OK), the DCM issues
 NvM SetBlockLockStatus (<BlockId>, TRUE).

7.3 Development Errors

[SWS_NvM_00023] [The Development errors

- NVM_E_PARAM_BLOCK_ID (0x0A)
- NVM_E_PARAM_BLOCK_TYPE (0x0B)
- NVM_E_PARAM_BLOCK_DATA_IDX (0x0C)
- NVM_E_PARAM_ADDRESS (0x0D)
- NVM_E_PARAM_DATA (0x0E)
- NVM E PARAM POINTER (0x0F)

shall be detectable by the NvM module when API requests are called with wrong parameters, depending on whether the build version mode is development mode. J (SRS_BSW_00385, SRS_BSW_00386, SRS_BSW_00406, SRS_BSW_00337, SRS_BSW_00327, SRS_BSW_00331)



[SWS_NvM_00586] [The Development error NVM_E_NOT_INITIALIZED (0x14) shall be detectable by the NvM module when NVRAM manager is still not initialized, depending on whether the build version mode is development mode.] ()

[SWS_NvM_00587] [The Development error NVM_E_BLOCK_PENDING (0x15) shall be detectable by the NvM module when API read/write/control request failed because a block with the same ID is already listed or currently in progress, depending on whether the build version mode is development mode.] ()

[SWS_NvM_00590] [The development error NVM_E_BLOCK_CONFIG (0x18) shall be detectable by the NvM module when the service is not possible with this block configuration, depending on whether the build version mode is development mode.] ()

[SWS_NvM_00747] [The development error NVM_E_BLOCK_LOCKED (0x19) shall be detectable by the NvM module when API write request failed for this block because RAM block is locked, depending on whether the build version mode is development mode. | ()

7.4 Production Errors

7.4.1 NVM E HARDWARE

ISWS NvM 008351[

[2442_144141_00033]			
Error Name:	NVM_E_HARI	DWARE	
Short Description:	Reading from	or writing to non volatile memory failed	
	If read job (multi job or single job read) fails either because the MemIf reports MEMIF_JOB_FAILED, MEMIF_BLOCK_INCONSISTENT or a CRC mismatch occurs or if a write/invalidate/erase job fails because the MemIf reports MEMIF_JOB_FAILED, NvM shall report NVM_E_HARDWARE to the DEM.		
Recommended DTC:	-		
	Pass	MemIf reports MEMIF_JOB_FAILED, MEMIF_BLOCK_INCONSISTENT or a CRC mismatch occurs during read / write / invalidate / erase operation. Read / write / invalidate / erase is successfull. (MemIf does not report MEMIF_JOB_FAILED , MEMIF_BLOCK_INCONSISTENT and no CRC mismatch occurs)	
	The condition under which the FAIL and/or PASS detection is active: Every time a read / write / invalidate / erase is requested for the block NvM shall report if the condition of the block changed.		
Time Required:	Not applicabal	e. (there is no timeout monitoring in the NvM)	
Monitor Frequency	continous		
MIL illumniation:	-		

1()



7.5 Extended Production Errors

Type or error	Related error code	Value
		[hex]
The processing of the read service detects an	NVM_E_INTEGRITY_FAILED	Assigned
inconsistency		by DEM
The processing of the service fails	NVM_E_REQ_FAILED	Assigned
		by DEM
The Static Block ID check during read failed	NVM_E_WRONG_BLOCK_ID	Assigned
		by DEM
The write verification failed	NVM_E_VERIFY_FAILED	Assigned
		by DEM
There is a loss of redundancy for a block of	NVM_E_LOSS_OF_REDUNDANCY	Assigned
redundant type		by DEM
The NVRAM Manager's job queue overflow	NVM_E_QUEUE_OVERFLOW	Assigned
occurs		by DEM
There is a write attempt to a NVRAM block with	NVM_E_WRITE_PROTECTED	Assigned
write protection		by DEM

[SWS_NvM_00591] [The extended production error NVM_E_INTEGRITY_FAILED (value assigned by DEM, see container NvmDemEventParameterRefs) shall be detectable by the NvM module when API request integrity failed, depending on whether the build version mode is in production mode. | ()

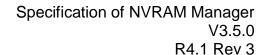
[SWS_NvM_00592] [The extended production error NVM_E_REQ_FAILED (value assigned by DEM, see container NvmDemEventParameterRefs) shall be detectable by the NvM module when API request failed, depending on whether the build version mode is in production mode.] ()

[SWS_NvM_00593] [The extended production error NVM_E_WRONG_BLOCK_ID (value assigned by DEM, see container NvmDemEventParameterRefs) shall be detectable by the NvM module when Static Block ID check failed, depending on whether the build version mode is in production mode. | (SRS Mem 08555)

[SWS_NvM_00594] [The extended production error NVM_E_VERIFY_FAILED (value assigned by DEM, see container NvmDemEventParameterRefs) shall be detectable by the NvM module when write Verification failed, depending on whether the build version mode is in production mode.] ()

[SWS_NvM_00595] [The extended production error NVM_E_LOSS_OF_REDUNDANCY (value assigned by DEM, see container NvmDemEventParameterRefs) shall be detectable by the NvM module when loss of redundancy, depending on whether the build version mode is in production mode.] ()

[SWS_NvM_00722] [The extended production error NVM_E_QUEUE_OVERFLOW (value assigned by DEM, see container NvmDemEventParameterRefs) shall be detectable by the NvM module when an NVRAM Managers job queue overflow occurs.] ()





[SWS_NvM_00723] [The extended production error NVM_E_WRITE_PROTECTED (value assigned by DEM) shall be detectable by the NvM module when a write attempt to a NVRAM block with write protection occurs. | ()

7.6 Error detection

[SWS_NvM_00027] [If development error detection is enabled for NvM module, the function NvM_SetDataIndex shall report the DET error NVM_E_NOT_INITIALIZED when NVM is not yet initialized. J (SRS_BSW_00323, SRS_BSW_00385, SRS_BSW_00386, SRS_BSW_00406, SRS_BSW_00327, SRS_BSW_00331)

[SWS_NvM_00598] [If development error detection is enabled for NvM module, the function NvM_SetDataIndex shall report the DET error NVM_E_BLOCK_PENDING when NVRAM block identifier is already queued or currently in progress. | ()

[SWS_NvM_00599] [If development error detection is enabled for NvM module, the function NvM_SetDataIndex shall report the DET error NVM_E_PARAM_BLOCK_DATA_IDX when DataIndex parameter exceeds the total number of configured datasets [SWS_NvM_00444, [SWS_NvM_00445.] ()

[SWS_NvM_00600] [If development error detection is enabled for NvM module, the function NvM_SetDataIndex shall report the DET error NVM_E_PARAM_BLOCK_TYPE when the request is not possible in conjunction with the configured block management type. | ()

[SWS_NvM_00601] [If development error detection is enabled for NvM module, the function NvM_SetDataIndex shall report the DET error NVM_E_PARAM_BLOCK_ID when the passed BlockID is out of range. | ()

[SWS_NvM_00602] [If development error detection is enabled for NvM module, the function NvM_GetDataIndex shall report the DET error NVM_E_NOT_INITIALIZED when NVM not yet initialized. | ()

[SWS_NvM_00603] [If development error detection is enabled for NvM module, the function NvM_GetDataIndex shall report the DET error NVM_E_PARAM_BLOCK_TYPE when the request is not possible in conjunction with the configured block management type.] ()

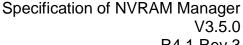
[SWS_NvM_00604] [If development error detection is enabled for NvM module, the function NvM_GetDataIndex shall report the DET error NVM_E_PARAM_BLOCK_ID when the passed BlockID is out of range.] ()

[SWS_NvM_00605] [If development error detection is enabled for NvM module, the function NvM_GetDataIndex shall report the DET error NVM_E_PARAM_DATA when a NULL pointer is passed via the parameter DataIndexPtr. | ()

[SWS_NvM_00606] [If development error detection is enabled for NvM module, the function NvM_SetBlockProtection shall report the DET error NVM_E_NOT_INITIALIZED when NVM is not yet initialized. | ()



- **[SWS_NvM_00607]** [If development error detection is enabled for NvM module, the function NvM_SetBlockProtection shall report the DET error NVM_E_BLOCK_PENDING when NVRAM block identifier is already queued or currently in progress.] ()
- [SWS NvM 00608] [If development error detection is enabled for NvM module, the NvM SetBlockProtection shall report the DET function error NVM E BLOCK CONFIG when **NVRAM** block is configured with the NvMWriteBlockOnce = TRUE. | ()
- **[SWS_NvM_00609]** [If development error detection is enabled for NvM module, the function NvM_SetBlockProtection shall report the DET error NVM_E_PARAM_BLOCK_ID when the passed BlockID is out of range. | ()
- **[SWS_NvM_00759]** [If development error detection is enabled for NvM module, the function NvM_SetBlockProtection shall report the DET error NVM_E_BLOCK_LOCKED when the block is locked. | ()
- **[SWS_NvM_00610]** [If development error detection is enabled for NvM module, the function NvM_GetErrorStatus shall report the DET error NVM_E_NOT_INITIALIZED when NVM is not yet initialized.] ()
- **[SWS_NvM_00611]** [If development error detection is enabled for NvM module, the function NvM_GetErrorStatus shall report the DET error NVM_E_PARAM_BLOCK_ID when the passed BlockID is out of range.] ()
- **[SWS_NvM_00612]** [If development error detection is enabled for NvM module, the function NvM_GetErrorStatus shall report the DET error NVM_E_PARAM_DATA when a NULL pointer is passed via the parameter RequestResultPtr.] ()
- **[SWS_NvM_00613]** [If development error detection is enabled for NvM module, the function NvM_GetVersionInfo shall report the DET error NVM_E_PARAM_POINTER when a NULL pointer is passed via the parameter versioninfo.] ()
- **[SWS_NvM_00614]** [If development error detection is enabled for NvM module, the function NvM_ReadBlock shall report the DET error NVM_E_NOT_INITIALIZED when NVM is not yet initialized. | ()
- **[SWS_NvM_00615]** [If development error detection is enabled for NvM module, the function NvM_ReadBlock shall report the DET error NVM_E_BLOCK_PENDING when NVRAM block identifier is already queued or currently in progress.] ()
- **[SWS_NvM_00616]** [If development error detection is enabled for NvM module, the function NvM_ReadBlock shall report the DET error NVM_E_PARAM_ADDRESS when no permanent RAM block and no explicit syncronization is configured and a NULL pointer is passed via the parameter NvM_DstPtr.] ()



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[SWS_NvM_00618] [If development error detection is enabled for NvM module, the function NvM ReadBlock shall report the DET error NVM E PARAM BLOCK ID when the passed BlockID is out of range. | ()

AUTOSAR

[SWS NvM 00823] [If development error detection is enabled for NvM module, the function NvM ReadPRAMBlock shall report the DET error NVM E NOT INITIALIZED when NVM is not yet initialized. | ()

[SWS NvM 00824] [If development error detection is enabled for NvM module, the function NvM ReadPRAMBlock shall report the NVM E BLOCK PENDING when NVRAM block identifier is already gueued or currently in progress. | ()

[SWS NvM 00825] [If development error detection is enabled for NvM module, the NvM ReadPRAMBlock shall report the DET error NVM E PARAM ADDRESS when no permanent RAM block and no explicit syncronization is configured and a NULL pointer is passed via the parameter NvM DstPtr. | ()

[SWS NvM 00826] [If development error detection is enabled for NvM module, the NvM ReadPRAMBlock shall report the DET error NVM_E_PARAM_BLOCK_ID when the passed BlockID is out of range. | ()

[SWS NvM 00619] [If development error detection is enabled for NvM module, the function NvM_WriteBlock shall report the DET error NVM_E_NOT_INITIALIZED when NVM not yet initialized. | ()

[SWS NvM 00620] [If development error detection is enabled for NvM module, the function NvM_WriteBlock shall report the DET error NVM_E_BLOCK_PENDING when NVRAM block identifier is already queued or currently in progress. | ()

[SWS NvM 00622] [If development error detection is enabled for NvM module, the function NvM_WriteBlock shall report the DET error NVM_E_PARAM_ADDRESS when no permanent RAM block and no explicit syncronization is configured and a NULL pointer is passed via the parameter NvM SrcPtr. I ()

[SWS NvM 00624] [If development error detection is enabled for NvM module, the function NvM_WriteBlock shall report the DET error NVM_E_PARAM_BLOCK_ID when the passed BlockID is out of range. I ()

[SWS_NvM_00748] [If development error detection is enabled for NvM module, the NvM WritePRAMBlock function shall report the DET error NVM_E_BLOCK_LOCKED when the block is locked. | ()

[SWS NvM 00827] [If development error detection is enabled for NvM module, the NvM WritePRAMBlock function shall report the DET error NVM E NOT INITIALIZED when NVM not yet initialized. | ()

[SWS NvM 00828] [If development error detection is enabled for NvM module, the NvM WritePRAMBlock function shall report the



NVM_E_BLOCK_PENDING when NVRAM block identifier is already queued or currently in progress. | ()

[SWS_NvM_00829] [If development error detection is enabled for NvM module, the function NvM_WritePRAMBlock shall report the DET error NVM_E_PARAM_BLOCK_ID when the passed BlockID is out of range.] ()

[SWS_NvM_00830] [If development error detection is enabled for NvM module, the function NvM_WritePRAMBlock shall report the DET error NVM_E_BLOCK_LOCKED when the block is locked. | ()

[SWS_NvM_00625] [If development error detection is enabled for NvM module, the function NvM_RestoreBlockDefaults shall report the DET error NVM_E_NOT_INITIALIZED when NVM is not yet initialized. | ()

[SWS_NvM_00626] [If development error detection is enabled for NvM module, the function NvM_RestoreBlockDefaults shall report the DET error NVM_E_BLOCK_PENDING when NVRAM block identifier is already queued or currently in progress. | ()

[SWS_NvM_00628] [If development error detection is enabled for NvM module, the function NvM_RestoreBlockDefaults shall report the DET error NVM_E_BLOCK_CONFIG when Default data is not available/configured for the referenced NVRAM block. | ()

[SWS_NvM_00629] [If development error detection is enabled for NvM module, the function NvM_RestoreBlockDefaults shall report the DET error NVM_E_PARAM_ADDRESS when no permanent RAM block and no explicit syncronization is configured and a NULL pointer is passed via the parameter NvM DstPtr. | ()

[SWS_NvM_00630] [If development error detection is enabled for NvM module, the function NvM_RestoreBlockDefaults shall report the DET error NVM_E_PARAM_BLOCK_ID when the passed BlockID is out of range.] ()

[SWS_NvM_00831] [If development error detection is enabled for NvM module, the function NvM_RestorePRAMBlockDefaults shall report the DET error NVM_E_NOT_INITIALIZED when NVM is not yet initialized.] ()

[SWS_NvM_00832] [If development error detection is enabled for NvM module, the function NvM_RestorePRAMBlockDefaults shall report the DET error NVM_E_BLOCK_PENDING when NVRAM block identifier is already queued or currently in progress.] ()

[SWS_NvM_00833] [If development error detection is enabled for NvM module, the function NvM_RestorePRAMBlockDefaults shall report the DET error NVM_E_BLOCK_CONFIG when Default data is not available/configured for the referenced NVRAM block.] ()



- **[SWS_NvM_00834]** [If development error detection is enabled for NvM module, the function NvM_RestorePRAMBlockDefaults shall report the DET error NVM_E_PARAM_BLOCK_ID when the passed BlockID is out of range. | ()
- **[SWS_NvM_00631]** [If development error detection is enabled for NvM module, the function NvM_EraseNvBlock shall report the DET error NVM_E_NOT_INITIALIZED when the NVM is not yet initialized. | ()
- **[SWS_NvM_00632]** [If development error detection is enabled for NvM module, the function NvM_EraseNvBlock shall report the DET error NVM_E_BLOCK_PENDING when the NVRAM block identifier is already queued or currently in progress. | ()
- **[SWS_NvM_00635]** [If development error detection is enabled for NvM module, the function NvM_EraseNvBlock shall report the DET error NVM_E_PARAM_BLOCK_ID when the passed BlockID is out of range. | ()
- **[SWS_NvM_00636]** [If development error detection is enabled for NvM module, the function NvM_EraseNvBlock shall report the DET error NVM_E_BLOCK_CONFIG when the NVRAM block has not immediate priority. | ()
- [SWS_NvM_00757] [If development error detection is enabled for NvM module, the function NvM_EraseNvBlock shall report the DET error NVM_E_BLOCK_LOCKED when the block is locked. | ()
- **[SWS_NvM_00637]** [If development error detection is enabled for NvM module, the function NvM_CancelWriteAll shall report the DET error NVM_E_NOT_INITIALIZED when NVM is not yet initialized.] ()
- **[SWS_NvM_00638]** [If development error detection is enabled for NvM module, the function NvM_InvalidateNvBlock shall report the DET error NVM_E_NOT_INITIALIZED when NVM is not yet initialized. | ()
- **[SWS_NvM_00639]** [If development error detection is enabled for NvM module, the function NvM_InvalidateNvBlock shall report the DET error NVM_E_BLOCK_PENDING when NVRAM block identifier is already queued or currently in progress. | ()
- **[SWS_NvM_00642]** [If development error detection is enabled for NvM module, the function NvM_InvalidateNvBlock shall report the DET error NVM_E_PARAM_BLOCK_ID when the passed BlockID is out of range. | ()
- **[SWS_NvM_00756]** [If development error detection is enabled for NvM module, the function NvM_InvalidateNvBlock shall report the DET error NVM_E_BLOCK_LOCKED when the block is locked.] ()
- **[SWS_NvM_00643]** [If development error detection is enabled for NvM module, the function NvM_SetRamBlockStatus shall report the DET error NVM_E_NOT_INITIALIZED when NVM not yet initialized.] ()



[SWS_NvM_00644] [If development error detection is enabled for NvM module, the function NvM_SetRamBlockStatus shall report the DET error NVM_E_BLOCK_PENDING when NVRAM block identifier is already queued or currently in progress.] ()

[SWS_NvM_00645] [If development error detection is enabled for NvM module, the function NvM_SetRamBlockStatus shall report the DET error NVM_E_PARAM_BLOCK_ID when the passed BlockID is out of range.] ()

[SWS_NvM_00758] [If development error detection is enabled for NvM module, the function NvM_SetRamBlockStatus shall report the DET error NVM_E_BLOCK_LOCKED when the block is locked.| ()

[SWS_NvM_00646] [If development error detection is enabled for NvM module, the function NvM_ReadAll shall report the DET error NVM_E_NOT_INITIALIZED when NVM is not yet initialized.] ()

[SWS_NvM_00647] [If development error detection is enabled for NvM module, the function NvM_WriteAll shall report the DET error NVM_E_NOT_INITIALIZED when NVM is not yet initialized.] ()

[SWS_NvM_00648] [If development error detection is enabled for NvM module, the function NvM_CancelJobs shall report the DET error NVM_E_NOT_INITIALIZED when NVM is not yet initialized.] ()

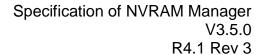
[SWS_NvM_00649] [If development error detection is enabled for NvM module, the function NvM_CancelJobs shall report the DET error NVM_E_PARAM_BLOCK_ID when the passed BlockID is out of range. | ()

[SWS_NvM_00728] [If development error detection is enabled for NvM module, the function NvM_SetBlockLockStatus shall report the DET error NVM_E_NOT_INITIALIZED when NVM is not yet initialized. | ()

[SWS_NvM_00729] [If development error detection is enabled for NvM module, the function NvM_SetBlockLockStatus shall report the DET error NVM_E_BLOCK_PENDING when NVRAM block identifier is already queued or currently in progress.] ()

[SWS NvM 00730] [If development error detection is enabled for NvM module, the NvM SetBlockLockStatus function shall report the DET error NVM_E_BLOCK_CONFIG when the NVRAM block is configured with NvMWriteBlockOnce = TRUE. | ()

[SWS_NvM_00731] [If development error detection is enabled for NvM module, the function NvM_SetBlockLockStatus shall report the DET error NVM_E_PARAM_BLOCK_ID when the passed BlockID is out of range.] ()





7.7 Debugging

 $\cite{bullet} \cite{bullet} \cite{bullet}$



8 API specification

8.1 API

8.1.1 Imported types

In this chapter all types included from the following files are listed:

[SWS NvM 00446] [

5110_1111111_00++0]			
Module	Imported Type		
Dem	Dem_EventIdType		
	Dem_EventStatusType		
MemIf	Memlf_JobResultType		
	MemIf_ModeType		
	Memlf_StatusType		
Std_Types	Std_ReturnType		
	Std_VersionInfoType		

]()

8.1.2 Type definitions

8.1.2.1 NvM_RequestResultType

[SWS_NvM_00083] [The type NvM_RequestResultType is an asynchronous request result, which will be returned by the API service NvM_GetErrorStatus [SWS_NvM_00015].] ()

[SWS_NvM_00470]

Name:	NvM_RequestResultType	NvM_RequestResultType		
Туре:	uint8			
Range:	NVM_REQ_OK	0x00 0x00: The last asynchronous read/write/control request has been finished successfully. This shall be the default value after reset. This status shall have the value 0.		
	NVM_REQ_NOT_OK	0x01 0x01: The last asynchronous read/write/control request has been finished unsuccessfully.		
	NVM_REQ_PENDING	0x02 0x02: An asynchronous read/write/control request is currently pending.		
	NVM_REQ_INTEGRITY_FAILED	0x03 0x03: The result of the last asynchronous request		
		NvM_ReadBlock or NvM_ReadAll		
		is a data integrity failure.		



			Note:	
			In case	of
			NvM_ReadBlock	
			the content of the RAM changed but has become in application is responsible and validate the RAM block	nvalid. The to renew
	NVM_REQ_BLOCK_SKIPPED	0x04	0x04: The referenced by skipped during exect NvM_ReadAll or NvM_Wr Dataset NVRAM (NvM_ReadAll) or NVRAW without a permanently RAM block.	ution of iteAll, e.g. blocks M blocks
	NVM_REQ_NV_INVALIDATED	0x05	0x05: The referenced N\ invalidated.	/ block is
	NVM_REQ_CANCELED	0x06	0x06: The multi block NvM_WriteAll was canceled NvM_CancelWriteAll. Or	
			Any single block job (NvM_ReadBlock, NvM_\ NvM_EraseNvBlock,	
			NvM_InvalidateNvBlock NvM_RestoreBlockDefaults canceled by calling NvM_C	ancelJobs.
	NVM_REQ_REDUNDANCY_FAILED	0x07	0x07: The required redundar referenced NV block is lost.	
	NVM_REQ_RESTORED_FROM_ROM	0x08		block has
Description:	This is an asynchronous request NvM_GetErrorStatus. The availability additionally signaled via a callback fund	of an		

]()

8.1.2.2 NvM_BlockIdType

ISWS NvM 004711

[<u>3882_I88I8I_UU</u>	47 I]
Name:	NvM_BlockIdType
Type:	uint16
	02^(16- NvMDatasetSelectionBits)- 1
Description:	Identification of a NVRAM block via a unique block identifier. Reserved NVRAM block IDs: 0 -> to derive multi block request results via NvM_GetErrorStatus 1 -> redundant NVRAM block which holds the configuration ID

]()

[SWS_NvM_00475] [The NVRAM block IDs shall be in a sequential order, i.e. the NVRAM manager does not need to be capable of handling non-sequential NVRAM block IDs.] ()



Example: If 50 NvM block have to be configured then their IDs are expected to be configured from 2 until 51 because block ID 0 and 1 are reserved for NvM internal use. So the sequential order will start with the ID 0 and increase one by one until 51, however only the blocks with IDs from 2 to 51 can and will be configured.

8.1.3 Function definitions

8.1.3.1 Synchronous requests

8.1.3.1.1 NvM_Init

[SWS]	NvM	004471	Γ
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	In Table 1 to
Service name:	NvM_Init
Syntax:	void NvM_Init(
	void
Service ID[hex]:	0x00
Sync/Async:	Synchronous
Reentrancy:	Non Reentrant
Parameters (in):	None
Parameters	None
(inout):	
Parameters (out):	None
Return value:	None
Description:	Service for resetting all internal variables.

]()

[SWS_NvM_00399] [The function NvM_Init shall reset all internal variables, e.g. the queues, request flags, state machines, to their initial values. It shall signal "INIT DONE" internally, e.g. to enable job processing and queue management.] (SRS_BSW_00101, SRS_BSW_00406)

[SWS_NvM_00400] [The function NvM_Init shall not modify the permanent RAM block contents or call explicit synchronization callback, as this shall be done on NvM_ReadAll.| (SRS_BSW_00101, SRS_BSW_00406)

[SWS_NvM_00192] [The function NvM_Init shall set the dataset index of all NVRAM blocks of type NVM_BLOCK_DATASET to zero.] ()

[SWS_NvM_00193] The function NvM_Init shall not initialize other modules (it is assumed that the underlying layers are already initialized). | ()

The function NvM_Init is affected by the common [SWS_NvM_00028] and published configuration parameter.



Hint: The time consuming NVRAM block initialization and setup according to the block descriptor [ECUC NVM 00061] shall be done by the NvM_ReadAll request.

8.1.3.1.2 NvM_SetDataIndex

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ISWS	NVIVI	004481	Т

[<u>3443_I44I4I_004</u>		
Service name:	NvM_SetDataIn	dex
Syntax:	Std_ReturnType	- '
		NvM_BlockIdType BlockId,
		uint8 DataIndex
)	
Service ID[hex]:	0x01	
Sync/Async:	Synchronous	
Reentrancy:	Reentrant	
	BlockId	The block identifier uniquely identifies one NVRAM block
Parameters (in):		descriptor. A NVRAM block descriptor contains all needed
rarameters (m).		information about a single NVRAM block.
	DataIndex	Index position (association) of a NV/ROM block.
Parameters	None	
(inout):		
Parameters (out):	None	
Return value:	Std_ReturnType	E_OK: The index position was set successfully.
Return value.		E_NOT_OK: An error occurred.
Description:	Service for setting the DataIndex of a dataset NVRAM block.	

| (SRS Mem 08007)

[SWS_NvM_00014] [The function NvM_SetDataIndex shall set the index to access a certain dataset of a NVRAM block (with/without ROM blocks). | ()

[SWS_NvM_00263] [The function NvM_SetDataIndex shall leave the content of the corresponding RAM block unmodified.] ()

[SWS_NvM_00264] [For blocks with block management different from NVM_BLOCK_DATASET, NvM_SetDataIndex shall return without any effect in production mode. | ()

Regarding error detection, the requirement [is applicable to the function NvM SetDataIndex.

[SWS_NvM_00707] [The NvM module's environment shall have initialized the NvM module before it calls the function NvM_SetDataIndex. | ()

Hint: NVRAM common block configuration parameters [<u>SWS NvM 00028</u>], block management types [<u>ECUC NvM 00061</u>] and one configured NVRAM block descriptor needed [NVM062].



8.1.3.1.3 NvM_GetDataIndex

[SWS NvM 00449] [

[
Service name:	NvM_GetDataInd	dex	
Syntax:	Std_ReturnType	NvM_GetDataIndex(NvM_BlockIdType BlockId, uint8* DataIndexPtr	
Service ID[hex]:	0x02		
Sync/Async:	Synchronous		
Reentrancy:	Reentrant		
Parameters (in):		The block identifier uniquely identifies one NVRAM block descriptor. A NVRAM block descriptor contains all needed information about a single NVRAM block.	
Parameters (inout):	None		
Parameters (out):	DataIndexPtr	Pointer to where to store the current dataset index (0255)	
Return value:		E_OK: The index position has been retrieved successfully. E_NOT_OK: An error occurred.	
Description:	Service for getting	ng the currently set DataIndex of a dataset NVRAM block	

]()

[SWS_NvM_00021] [The function NvM_GetDataIndex shall get the current index (association) of a dataset NVRAM block (with/without ROM blocks). | ()

[SWS_NvM_00265] [For blocks with block management different from NVM_BLOCK_DATASET, NvM_GetDataIndex shall set the index pointed by DataIndexPtr to zero.] ()

[SWS_NvM_00708] [The NvM module's environment shall have initialized the NvM module before it calls the function NvM_GetDataIndex.

Hint: NVRAM common block configuration parameters [SWS_NvM_00028], block management types [ECUC_NvM_00061] and one configured NVRAM block descriptor needed [NVM062]. | ()



8.1.3.1.4 NvM_SetBlockProtection

[SWS NvM 00450] [

[<u>3442_ 44 4 </u>	י נסכ	
Service name:	NvM_SetBlockProt	tection
Syntax:	Std_ReturnType)	NvM_SetBlockProtection(NvM_BlockIdType BlockId, boolean ProtectionEnabled
Service ID[hex]:	0x03	
Sync/Async:	Synchronous	
Reentrancy:	Reentrant	
Parameters (in):	ProtectionEnabled	The block identifier uniquely identifies one NVRAM block descriptor. A NVRAM block descriptor contains all needed information about a single NVRAM block. TRUE: Write protection shall be enabled FALSE: Write protection shall
Parameters (inout):	None	
Parameters (out):	None	
Return value:		E_OK: The block was enabled/disabled as requested E_NOT_OK: An error occured.
Description:	Service for setting/	resetting the write protection for a NV block.

] (SRS_Mem_00127)

[SWS_NvM_00016] [The function NvM_SetBlockProtection shall set/reset the write protection for the corresponding NV block by setting the write protection attribute in the administrative part of the corresponding NVRAM block. | (SRS_Mem_00127)

[SWS_NvM_00709] [The NvM module's environment shall have initialized the NvM module before it calls the function NvM_SetBlockProtection.

Hint: NVRAM common block configuration parameters [SWS_NvM_00028], block management types [ECUC_NvM_00061] and one configured NVRAM block descriptor needed [NVM062].] ()



8.1.3.1.5 NvM GetErrorStatus

[SWS_NvM_00451][

Service name:	NvM_GetErrorStatu	IS .	
Syntax:	Std_ReturnType	NvM_BlockIdType NvM_RequestResultType*	NvM_GetErrorStatus(BlockId, RequestResultPtr
Service ID[hex]:	0x04		
Sync/Async:	Synchronous		
Reentrancy:	Reentrant		
Parameters (in):	de	he block identifier uniquely identifies escriptor. A NVRAM block descriptor of the street about a single NVRAM block.	
Parameters (inout):	None		
Parameters (out):	RequestResultPtrP N	ointer to where to store the r lvM_RequestResultType .	equest result. See
Return value:	SI	_OK: The block dependent error/status uccessfullyNOT_OK: An error occured.	information was read
Description:	Service to read the	block dependent error/status information.	•

(SRS_Mem_00020)

[SWS_NvM_00015] [The function NvM_GetErrorStatus shall read the block dependent error/status information in the administrative part of a NVRAM block. The status/error information of a NVRAM block shall be set by a former or current asynchronous request.] (SRS_Mem_00020)

[SWS_NvM_00710] [The NvM module's environment shall have initialized the NvM module before it calls the function NvM_GetErrorStatus. | ()

NVRAM common block configuration parameters [<u>SWS NvM 00028</u>], block management types [<u>ECUC NvM 00061</u>] and one configured NVRAM block descriptor are needed in the configuration with respect to the function NvM_GetErrorStatus [NVM062].



8.1.3.1.6 NvM_GetVersionInfo

[SWS NvM 00452] [

···				
Service name:	NvM_GetVe	rsionInfo		
Syntax:	void	NvM_GetVersionInfo(Std_VersionInfoType* versioninfo		
)	200_101010111110111110		
Service ID[hex]:	0x0f			
Sync/Async:	Synchronous	Synchronous		
Reentrancy:	Reentrant	Reentrant		
Parameters (in):	None			
Parameters (inout):	None			
Parameters (out):	versioninfo	Pointer to where to store the version information of this module.		
Return value:	None			
Description:	Service to get the version information of the NvM module.			

]()

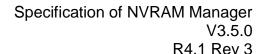
8.1.3.1.7 NvM_SetRamBlockStatus

[SWS NvM 00453] [

[
Service name:	NvM_SetRamBl	ockStatus
Syntax:	Std_ReturnType)	NvM_SetRamBlockStatus(NvM_BlockIdType BlockId, boolean BlockChanged
Service ID[hex]:	0x05	
Sync/Async:	Synchronous	
Reentrancy:	Reentrant	
Parameters (in):	BlockId BlockChanged	The block identifier uniquely identifies one NVRAM block descriptor. A NVRAM block descriptor contains all needed information about a single NVRAM block. TRUE: Validate the RAM block and mark block as changed. FALSE: Invalidate the RAM block and mark block as unchanged.
Parameters (inout):	None	
Parameters (out):	None	
Return value:	Std_ReturnType	E_OK: The status of the RAM-Block was changed as requested. E_NOT_OK: An error occurred.
Description:	Service for settir	ng the RAM block status of an NVRAM block.

] (SRS_Mem_08545)

[SWS_NvM_00240] [The function NvM_SetRamBlockStatus shall only work on NVRAM blocks with a permanently configured RAM block that have NvMBlockUseSetRamBlockStatus enabled and shall have no effect to other NVRAM blocks...] (SRS_Mem_08546)





[SWS_NvM_00241] [The function NvM_SetRamBlockStatus shall assume that a changed permanent RAM block or the content of the RAM mirror in the NvM module (in case of explicit synchronization) is valid (basic assumption).] (SRS_Mem_08545)

[SWS_NvM_00405] [When the "BlockChanged" parameter passed to the function NvM_SetRamBlockStatus is FALSE the corresponding RAM block is either invalid or unchanged (or both).] (SRS_Mem_08545)

[SWS_NvM_00406] [When the "BlockChanged" parameter passed to the function NvM_SetRamBlockStatus is TRUE, the corresponding permanent RAM block or the content of the RAM mirror in the NvM module (in case of explicit synchronization) is valid and changed.] ()

[SWS_NvM_00121] The function NvM_SetRamBlockStatus shall request the recalculation of CRC in the background, i.e. the CRC recalculation shall be processed by the NvM_MainFunction, if the given "BlockChanged" parameter is TRUE and CRC calculation in RAM is configured (i.e. NvMCalcRamBlockCrc == TRUE).] ()

Hint:

In some cases, a permanent RAM block cannot be validated neither by a reload of its NV data, nor by a load of its ROM data during the execution of a NvM_ReadAll command (startup). The application is responsible to fill in proper data to the RAM block and to validate the block via the function $NvM_SetRamBlockStatus$ before this RAM block can be written to its corresponding NV block by $NvM_WriteAll$. It is expected that the function $NvM_SetRamBlockStatus$ will be called frequently for NVRAM blocks which are configured to be protected in RAM via CRC. Otherwise this function only needs to be called once to mark a block as "changed" and to be processed during $NvM_WriteAll$.

[SWS_NvM_00711] [The NvM module's environment shall have initialized the NvM module before it calls the function NvM_SetRamBlockStatus.] ()

[SWS NvM 00408] [The NvM module function shall provide the NvM SetRamBlockStatus only if it is configured via NvMSetRamBlockStatusApi [SWS_NvM_00028]. | ()

NVRAM common configuration parameters [<u>SWS_NvM_00028</u>], block management types [<u>ECUC_NvM_00061</u>] and one configured NVRAM block descriptor are needed in the configuration with respect to the function NvM_SetRamBlockStatus [NVM062].



8.1.3.1.8 NvM_SetBlockLockStatus

[SWS NvM 00548] [

Comitos nomes	NI-M CatDla	ald a alcOtatura					
Service name:	NvM_SetBloo	CKLOCKStatus					
Syntax:	void				NvM	I_SetBlockL	∟ockStatus(
			NvM E	lockIdTy	oe		Blockld,
			bool			В	lockLocked
)						
Service ID[hex]:	0x13						
Sync/Async:	Synchronous	;					
Reentrancy:	Reentrant						
Parameters (in):			ock desci		entifies one NV tains all neede		
	BlockLocked	TRUE: FALSE: Mark	Mark the RAM	the block as.	RAM.block unlocked	as	locked
Parameters (inout):	None						
Parameters (out):	None						
Return value:	None						
Description:		etting the locion of a NVRA		of a perr	nanent RAM b	lock or of	the explicit

] (SRS_Mem_08546)

[SWS_NvM_00732] [The function NvM_BlockLockStatus shall only work on NVRAM blocks with a permanently configured RAM block or on NVRAM blocks configured to support explicit synchronization and shall have no effect to other NVRAM blocks. Hint: This function is to be used mainly by DCM, but it can also be used by complex device drivers. The function is not included in the ServicePort interface. | ()



8.1.3.1.9 NvM_CancelJobs

[SWS NvM 00535] [

Service name:	NvM_CancelJobs	S
Syntax:	Std_ReturnType)	NvM_CancelJobs(NvM_BlockIdType BlockId
Service ID[hex]:	0x10	
Sync/Async:	Synchronous	
Reentrancy:	Reentrant	
Parameters (in):		The block identifier uniquely identifies one NVRAM block descriptor. A NVRAM block descriptor contains all needed information about a single NVRAM block.
Parameters (inout):	None	
Parameters (out):	None	
Return value:		E_OK: The job was successfully removed from queue. E_NOT_OK: The job could not be found in the queue.
Description:	Service to cance	all jobs pending for a NV block.

] (SRS_Mem_08560, SRS_Mem_08559)

[SWS_NvM_00536] [The function NvM_CancelJobs shall cancel all jobs pending in the queue for the specified NV Block. If requested the result type for the canceled blocks is NVM_REQ_CANCELED. | (SRS_Mem_08560, SRS_Mem_08559)

[SWS_NvM_00537] [A currently processed job shall continue even after the call of NvM_CancelJobs.] ()

[SWS_NvM_00225] [The job of the function NvM_CancelJobs shall set block specific job result for specified NVRAM block to NVM_REQ_CANCELED in advance if the request is accepted.

Hint: The intent is just to empty the queue during the cleanup phase in case of termination or restart of a partition, to avoid later end of job notification.] ()



8.1.3.2 Asynchronous single block requests

8.1.3.2.1 NvM_ReadBlock

[SWS_NvM_00454] [

[<u>3442_ 44 4 _004</u> ;	<u>] </u>	
Service name:	NvM_ReadBlock	
Syntax:	Std_ReturnType	NvM_ReadBlock(NvM_BlockIdType BlockId, void* NvM_DstPtr
Service ID[hex]:	0x06	
Sync/Async:	Asynchronous	
Reentrancy:	Reentrant	
Parameters (in):		The block identifier uniquely identifies one NVRAM block descriptor. A NVRAM block descriptor contains all needed information about a single NVRAM block.
Parameters (inout):	None	
Parameters (out):	NvM_DstPtr	Pointer to the RAM data block.
Return value:	Std_ReturnType	E_OK: request has been accepted E_NOT_OK: request has not been accepted
Description:	Service to copy t	he data of the NV block to its corresponding RAM block.

(SRS_LIBS_08533, BSW176, SRS_Mem_00016)

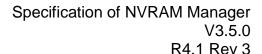
[SWS_NvM_00010] [The job of the function NvM_ReadBlock shall copy the data of the NV block to the corresponding RAM block.] (SRS_Mem_00016)

[SWS_NvM_00195] [The function NvM_ReadBlock shall take over the given parameters, queue the read request in the job queue and return.] (SRS_Mem_00016)

[SWS_NvM_00196] [If the funtion is provided with a valid RAM block address, it is used. If a NULL pointer is provided and if a permanent block or an NvMBlockUseSyncMechanism is specified the permanent block of the APIs shall be used. Otherwise a DET-Parameter error (see Section 7.6) shall be emitted. J (SRS Mem 00016)

[SWS_NvM_00278] The job of the function NvM_ReadBlock shall provide the possibility to copy NV data to a temporary RAM block although the NVRAM block is configured with a permanent RAM block or explicit synchronization callbacks. In this case, the parameter NvM_DstPtr must be unequal to the NULL pointer. Otherwise a DET-Parameter error (see Section 7.6) shall be emitted. | ()

[SWS_NvM_00198] The function NvM_ReadBlock shall invalidate a permanent RAM block immediately when the block is successfully enqueued or the job processing starts, i.e. copying data from NV memory or ROM to RAM. If the block has a synchronization callback (NvM_ReadRamBlockFromNvm) configured the invalidation will be done just before NvMReadRamBlockFromNvM is called. | ()





[SWS_NvM_00199] The job of the function NvM_ReadBlock shall initiate a read attempt on the second NV block if the passed BlockId references a NVRAM block of type NVM_BLOCK_REDUNDANT and the read attempts on the first NV block fail.] (

[SWS_NvM_00340] [In case of NVRAM block management type NVM_BLOCK_DATASET, the job of the function NvM_ReadBlock shall copy only that NV block to the corresponding RAM block which is selected via the data index in the administrative block. | ()

[SWS_NvM_00355] [The job of the function NvM_ReadBlock shall not copy the NV block to the corresponding RAM block if the NVRAM block management type is NVM_BLOCK_DATASET and the NV block selected by the dataset index is invalidate. | ()

[SWS_NvM_00651] [The job of the function NvM_ReadBlock shall not copy the NV block to the corresponding RAM block if the NVRAM block management type is NVM_BLOCK_DATASET and the NV block selected by the dataset index is inconsistent.] ()

[SWS_NvM_00354] [The job of the function NvM_ReadBlock shall copy the ROM block to RAM and set the job result to NVM_REQ_OK if the NVRAM block management type is NVM_BLOCK_DATASET and the dataset index points at a ROM block.] ()

[SWS_NvM_00200] The job of the function NvM_ReadBlock shall set the RAM block to valid and assume it to be unchanged after a successful copy process of the NV block to RAM. | ()

[SWS_NvM_00366] [The job of the function NvM_ReadBlock shall set the RAM block to valid and assume it to be changed if the default values are copied to the RAM successfully.] ()

[SWS_NvM_00206] The job of the function NvM_ReadBlock shall set the job result to NVM_REQ_OK if the NV block was copied successfully from NV memory to RAM.

[SWS_NvM_00341] [The job of the function NvM_ReadBlock shall set the request result to NVM_REQ_NV_INVALIDATED if the MemIf reports MEMIF BLOCK INVALID. | ()

[SWS_NvM_00652] [The job of the function NvM_ReadBlock shall report no error to the DEM if the MemIf reports MEMIF_BLOCK_INVALID.] ()

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[SWS_NvM_00358] The job of the function NvM_ReadBlock shall set the request result to NVM_REQ_INTEGRITY_FAILED if the MemIf reports

MEMIF_BLOCK_INCONSISTENT. |()

[SWS_NvM_00653] [The job of the function NvM_ReadBlock shall report NVM_E_INTEGRITY_FAILED to the DEM if the MemIf reports MEMIF_BLOCK_INCONSISTENT. | ()

Note: After the production of an ECU / a car, on the production line all blocks shall have been written with valid data (may be default data) and all diagnostic events (errors) shall have been deleted. If the process does not allow to write all NV blocks during production than the NvM will report diagnostic events (errors) because of blocks that were never written and reported as MEMIF_BLOCK_INCONSISTENT by MemIf.

[SWS_NvM_00359] [The job of the function NvM_ReadBlock shall set the request result to NVM_REQ_NOT_OK if the MemIf reports MEMIF_JOB_FAILED.] ()

[SWS_NvM_00654] [The job of the function NvM_ReadBlock shall report NVM_E_REQ_FAILED to the DEM if the MemIf reports MEMIF_JOB_FAILED.] ()

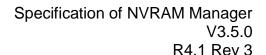
[SWS_NvM_00279] The job of the function NvM_ReadBlock shall set the job result to NVM_REQ_OK if the block management type of the given NVRAM block is NVM_BLOCK_REDUNDANT and one of the NV blocks was copied successfully from NV memory to RAM. ()

[SWS_NvM_00655] [The job of the function NvM_ReadBlock shall report no error to the DEM if the block management type of the given NVRAM block is NVM_BLOCK_REDUNDANT and one of the NV blocks was copied successfully from NV memory to RAM.] ()

[SWS_NvM_00316] [The job of the function NvM_ReadBlock shall mark every NVRAM block that has been configured with NVM_WRITE_BLOCK_ONCE (TRUE) that is not detected by underlying SW as being invalidated, shall be marked as write protected.] ()

[SWS_NvM_00317] [The job of the function NvM_ReadBlock shall invalidate a NVRAM block of management type redundant if both NV blocks have been invalidated.] ()

[SWS_NvM_00201] [The job of the function NvM_ReadBlock shall request a CRC recalculation over the RAM block data after the copy process [SWS_NvM_00180] if





the NV block is configured with CRC, i.e. if NvMCalRamBlockCrC == TRUE for the NV block.] ()

[SWS_NvM_00202] [The job of the function NvM_ReadBlock shall load the default values according to processing of NvM_RestoreBlockDefaults if the recalculated CRC is not equal to the CRC stored in NV memory.] ()

[SWS_NvM_00658] [NvM_ReadBlock: If there are no default values available, the RAM blocks shall remain invalid. | ()

[SWS_NvM_00657] [The job of the function NvM_ReadBlock shall load the default values according to processing of NvM_RestoreBlockDefaults if the read request passed to the underlying layer fails. | ()

[SWS_NvM_00203] The job of the function NvM_ReadBlock shall report NVM E INTEGRITY FAILED to the DEM if a CRC mismatch occurs. | ()

[SWS_NvM_00204] [The job of the function NvM_ReadBlock shall set the job result NVM_REQ_INTEGRITY_FAILED if a CRC mismatch occurs. | ()

[SWS_NvM_00712] [The NvM module's environment shall have initialized the NvM module before it calls the function NvM_ReadBlock. | ()

Hint: NVRAM common block configuration parameters [SWS NvM 00028], block management types [ECUC NvM 00061] and one configured NVRAM block descriptor are needed for configuration with respected to the function NvM_ReadBlock [NVM062].



8.1.3.2.2 NvM_WriteBlock

[SWS NvM 004551 [

[
Service name:	NvM_WriteBlock					
Syntax:	Std_ReturnType				NvM_	WriteBlock(
			NvM_BlockId7	Туре		BlockId,
		const		void*	1	NvM_SrcPtr
)					
Service ID[hex]:	0x07					
Sync/Async:	Asynchronous					
Reentrancy:	Reentrant					
Parameters (in):	BlockId	descriptor.	A NVRAM b	iquely identifies block descriptor NVRAM block.		
	NvM_SrcPtr	Pointer to the	ne RAM data b	lock.		
Parameters (inout):	None					
Parameters (out):	None					
Return value:	Std_ReturnType			has not been accepte	been ed	accepted
Description:	Service to copy	the data of t	he RAM block t	to its correspond	ling NV bloc	k.

(BSW176, SRS_Mem_00017)

[SWS_NvM_00410] [The job of the function NvM_WriteBlock shall copy the data of the RAM block to its corresponding NV block. J (SRS_Mem_00017)

[SWS_NvM_00411] [The function NvM_WriteBlock shall test the write protection attribute of the NV block in the administrative part of the corresponding RAM block. In case of failure an NVM_E_WRITE_PROTECTED / (during production) error shall be reported.] (SRS_Mem_00017)

[SWS_NvM_00217] [The function NvM_WriteBlock shall return with E_NOT_OK, if a write protected NVRAM block is referenced by the passed BlockId parameter.] ()

[SWS_NvM_00749] [The function NvM_WriteBlock shall return with E_NOT_OK, if a locked NVRAM block is referenced by the passed BlockId parameter. and a DET error (see Section 7.6) shall be emitted. | ()

[SWS_NvM_00208] [The function NvM_WriteBlock shall take over the given parameters, queue the write request in the job queue and return.] (SRS_Mem_08541)

[SWS_NvM_00209] [The function NvM_WriteBlock shall check the NVRAM block protection when the request is enqueued but not again before the request is executed. | ()

[SWS_NvM_00300] [The function NvM_WriteBlock shall cancel a pending job immediately in a destructive way if the passed BlockId references a NVRAM block configured to have immediate priority. The immediate job shall be the next active job to be processed. | ()



[SWS_NvM_00210] [If the function is provided with a valid RAM block address, it is used. If a NULL pointer is provided and if a permanent block or an NvMBlockUseSyncMechanism is specified the permanent block of the APIs shall be used. Otherwise a DET-Parameter error (see Section 7.6) shall be emitted...] ()

[SWS_NvM_00280] [The job of the function NvM_WriteBlock shall provide the possibility to copy a temporary RAM block to a NV block although the NVRAM block is configured with a permanent RAM block or explicit synchronization callbacks. In this case, the parameter NvM_SrcPtr must be unequal to a NULL pointer. Otherwise a DET-Parameter error (see Section 7.6) shall be emitted ()

[SWS_NvM_00212] [The job of the function NvM_WriteBlock shall request a CRC recalculation before the RAM block will be copied to NV memory if the NV block is configured with CRC [SWS_NvM_00180]. | ()

[SWS_NvM_00338] [The job of the function NvM_WriteBlock shall copy the RAM block to the corresponding NV block which is selected via the data index in the administrative block if the NVRAM block management type of the given NVRAM block is NVM_BLOCK_DATASET. | ()

[SWS_NvM_00303] [The job of the function NvM_WriteBlock shall assume a referenced permanent RAM block or the RAM mirror in the NvM module in case of explicit synchronization to be valid when the request is passed to the NvM module. If the permanent RAM block is still in an invalid state, the function NvM_WriteBlock shall validate it automatically before copying the RAM block contents to NV memory or after calling explicit synchronization callback (NvM_WriteRamBlockToNvm).] ()

[SWS_NvM_00213] 「The job of the function NvM_WriteBlock shall check the number of write retries using a write retry counter to avoid infinite loops. Each negative result reported by the memory interface shall be followed by an increment of the retry counter. In case of a retry counter overrun, the job of the function NvM_WriteBlock shall set the job result to NVM_REQ_NOT_OK. 」 (SRS_Mem_08554)

[SWS_NvM_00659] [The job of the function NvM_WriteBlock shall check the number of write retries using a write retry counter to avoid infinite loops. Each negative result reported by the memory interface shall be followed by an increment of the retry counter. In case of a retry counter overrun, the job of the function NvM_WriteBlock shall report NVM_E_REQ_FAILED to the DEM.] ()

[SWS_NvM_00216] [The configuration parameter NVM_MAX_NUM_OF_WRITE_RETRIES [SWS_NvM_00028] shall prescribe the maximum number of write retries for the job of the function NvM_WriteBlock when RAM block data cannot be written successfully to the corresponding NV block.] ()

[SWS_NvM_00760] [The job of the function NvM_WriteBlock shall copy the data content of the RAM block to both corresponding NV blocks if the NVRAM block



management type of the processed NVRAM block is NVM_BLOCK_REDUNDANT.] (

[SWS_NvM_00761] [If the processed NVRAM block is of type NVM_BLOCK_REDUNDANT the job of the function NvM_WriteBlock shall start to copy the data of the RAM block to NV block which has not been read during the jobs started by NvM_ReadBlock, NvM_ReadPRAMBlock or NvM_ReadAll then continue to copy the other NV block.] ()

[SWS_NvM_00284] [The job of the function NvM_WriteBlock shall set NVM_REQ_OK as job result if the passed BlockId references a NVRAM block of type NVM_BLOCK_REDUNDANT and at least one of the NV blocks have been written successfully.]()

[SWS_NvM_00328] [The job of the function NvM_WriteBlock shall set the write protection flag in the administrative block immediately if the NVRAM block is configured with NvMWriteBlockOnce == TRUE and the data has been written successfully to the NV block. | ()

[SWS_NvM_00713] [The NvM module's environment shall have initialized the NvM module before it calls the function NvM_WriteBlock. | ()

Hint:

To avoid the situation that in case of redundant NVRAM blocks two different NV blocks are containing different but valid data at the same time, each client of the function NvM_WriteBlock may call NvM_InvalidateNvBlock in advance.

NVRAM common block configuration parameters [<u>SWS_NvM_00028</u>], block management types [<u>ECUC_NvM_00061</u>] and one configured NVRAM block descriptor are needed in the configuration with respect to the function NvM_WriteBlock [NVM062].

[SWS_NvM_00547] [The job of the function NvM_WriteBlock with Block ID 1 shall write the compiled NVRAM configuration ID to the stored NVRAM configuration ID (block 1). | ()

Hint: If a pristine ECU is flashed for the first time, such a call invoked by will ensure that after a power-off without a proper shutdown, everything is as expected at the next start-up. Otherwise, the new configuration ID would not be stored in NV RAM and all ROM defaultd would be used.

A macro scan be used to indicate this usage.



8.1.3.2.3 NvM RestoreBlockDefaults

[SWS NvM 00456] [

0110_I11II_00+					
Service name:	NvM_RestoreBlo	ockDefaults			
Syntax:	Std_ReturnType	Std_ReturnType NvM_RestoreBlockDefault			
		NvM_BlockIdType BlockId,			
		void* NvM DestPtr			
)	<u>-</u>			
Service ID[hex]:	0x08				
Sync/Async:	Asynchronous				
Reentrancy:	Non Reentrant				
	BlockId	The block identifier uniquely identifies one NVRAM block			
Parameters (in):		descriptor. A NVRAM block descriptor contains all needed			
		information about a single NVRAM block.			
Parameters	None				
(inout):					
Parameters (out):	NvM_DestPtr	Pointer to the RAM data block.			
Dotum volue	Std_ReturnType	E_OK: request has been accepted			
Return value:		E_NOT_OK: request has not been accepted			
Description:	Service to restor	e the default data to its corresponding RAM block.			

(SRS Mem 00018)

[SWS_NvM_00012] [The job of the function NvM_RestoreBlockDefaults shall restore the default data to its corresponding RAM block.] (SRS_Mem_00018)

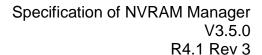
[SWS_NvM_00224] [The function NvM_RestoreBlockDefaults shall take over the given parameters, queue the request in the job queue and return.] ()

[SWS_NvM_00267] [The job of the function NvM_RestoreBlockDefaults shall load the default data from a ROM block if a ROM block is configured.] (SRS_Mem_00018)

[SWS_NvM_00266] The NvM module's environment shall call the function NvM_RestoreBlockDefaults to obtain the default data if no ROM block is configured for a NVRAM block and an application callback routine is configured via the parameter NvMInitBlockCallback. J (SRS_Mem_00018)

[SWS_NvM_00353] [The function NvM_RestoreBlockDefaults shall return with E_NOT_OK if the block management type of the given NVRAM block is NVM_BLOCK_DATASET, at least one ROM block is configured and the data index points at a NV block.] ()

[SWS_NvM_00435] [If the function is provided with a valid RAM block address, it is used. If a NULL pointer is provided and if a permanent block or an NvMBlockUseSyncMechanism is specified the permanent block of the APIs shall be used. Otherwise a DET-Parameter error (see Section 7.6) shall be emitted. | ()





[SWS_NvM_00436] [The NvM module's environment shall pass a pointer unequal to NULL via the parameter NvM_DstPtr to the function $NvM_RestoreBlockDefaults$ in order to copy ROM data to a temporary RAM block although the NVRAM block is configured with a permanent RAM block or explicit synchronization callbacks. Otherwise a DET-Parameter error (see Section 7.6) shall be emitted | ()

[SWS_NvM_00227] [The job of the function NvM_RestoreBlockDefaults shall invalidate a RAM block before copying default data to the RAM if a permanent RAM block is requested or before explicit synchronization callback (NvMReadRamBlockFromNvM) is called.] ()

[SWS_NvM_00228] [The job of the function NvM_RestoreBlockDefaults shall validate and assume a RAM block to be changed if the requested RAM block is permanent or after explicit synchronization callback (NvMReadRamBlockFromNvM) that is called returns E_OK and the copy process of the default data to RAM was successful.] ()

[SWS_NvM_00229] [The job of the function NvM_RestoreBlockDefaults shall request a recalculation of CRC from a RAM block after the copy process/validation if a CRC is configured for this RAM block. | ()

[SWS_NvM_00714] [The NvM module's environment shall have initialized the NvM module before it calls the function NvM_RestoreBlockDefaults. | ()

Hint: For the block management type NVM_BLOCK_DATASET, the application has to ensure that a valid dataset index is selected (pointing to ROM data). NVRAM common block configuration parameters [SWS_NVM_00028], block management types [ECUC_NVM_00061] and one configured NVRAM block descriptor are needed in the configuration with respect to the function NvM_RestoreBlockDefaults [NVM062].



8.1.3.2.4 NvM EraseNvBlock

[SWS N	/M_00457] [
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Service name:	NvM_EraseNvBl	ock				
Syntax:	Std_ReturnType)		NvM_Blocklo	dType	NvM_Era	aseNvBlock(BlockId
Service ID[hex]:	0x09					
Sync/Async:	Asynchronous					
Reentrancy:	Reentrant					
Parameters (in):		descriptor.	A NVRAM	niquely identifies block descriptor e NVRAM block.		
Parameters (inout):	None					
Parameters (out):	None					
Return value:	Std_ReturnType		request : request has	has not been accepte	been ed	accepted
Description:	Service to erase	a NV block.				

] (SRS_Mem_08544)

[SWS_NvM_00415] [The job of the function NvM_EraseNvBlock shall erase a NV block.] (SRS_Mem_08544)

[SWS_NvM_00231] [The function NvM_EraseNvBlock shall take over the given parameters, queue the request and return.] ()

[SWS_NvM_00418] The function NvM_EraseNvBlock shall queue the request to erase in case of disabled write protection.] ()

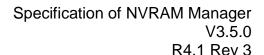
[SWS_NvM_00416] The job of the function NvM_EraseNvBlock shall leave the content of the RAM block unmodified.] ()

[SWS_NvM_00417] The function NvM_EraseNvBlock shall test the write protection attribute of the NV block in the corresponding Administrative block. In case of failure an NVM_E_WRITE_PROTECTED / (during production) error shall be reported.] ()

[SWS_NvM_00262] [The function NvM_EraseNvBlock shall return with E_NOT_OK if a write protected NV block is referenced. | ()

[SWS_NvM_00661] [The function NvM_EraseNvBlock shall return with E_NOT_OK if a ROM block of a dataset NVRAM block is referenced. | ()

[SWS_NvM_00230] The function NvM_EraseNvBlock shall check the write protection attribute of a NVRAM block only before the job is put to the job queue. In





case of failure an NVM_E_WRITE_PROTECTED / (during production) error shall be reported.] ()

[SWS_NvM_00662] [NvM_EraseNvBlock: The NvM module shall not re-check the write protection before fetching the job from the job queue. | ()

[SWS_NvM_00269] [If the referenced NVRAM block is of type NVM_BLOCK_REDUNDANT, the function NvM_EraseNvBlock shall only succeed when both NV blocks have been erased.] ()

[SWS_NvM_00271] The job of the function NvM_EraseNvBlock shall set the job result to NVM_REQ_NOT_OK if the processing of the service fails. ()

[SWS_NvM_00663] [The job of the function NvM_EraseNvBlock shall report NVM_E_REQ_FAILED to the DEM if the processing of the service fails.] ()

[SWS_NvM_00357] [The function NvM_EraseNvBlock shall return with E_NOT_OK, when development error detection is enabled and the referenced NVRAM block is configured with standard priority.] ()

[SWS_NvM_00715] [The NvM module's environment shall have initialized the NvM module before it calls the function NvM_EraseNvBlock.] () NVRAM common block configuration parameters [SWS_NvM_00028], block management types [ECUC_NvM_00061] and one configured NVRAM block descriptor are needed in the configuration with respect to the function NvM_EraseNvBlock [NVM062].



8.1.3.2.5 NvM_CancelWriteAll

[SWS_NvM_004	58] [
Service name:	NvM_CancelWriteAll	
Syntax:	void)	NvM_CancelWriteAll(void
Service ID[hex]:	0x0a	
Sync/Async:	Asynchronous	
Reentrancy:	Non Reentrant	
Parameters (in):	None	
Parameters (inout):	None	
Parameters (out):	None	
Return value:	None	
Description:	Service to cancel a running NvM_WriteAll request.	

(SRS Mem 08558, SRS Mem 08540)

[SWS_NvM_00019] [The function NvM_CancelWriteAll shall cancel a running NvM_WriteAll request. It shall terminate the NvM_WriteAll request in a way that the data consistency during processing of a single NVRAM block is not compromised] (SRS Mem 08540)

[SWS_NvM_00232] [The function NvM_CancelWriteAll shall signal the request to the NvM module and return. | ()

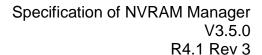
[SWS_NvM_00233] [The function NvM_CancelWriteAll shall be without any effect if no NvM_WriteAll request is pending.] ()

[SWS_NvM_00234] [The function NvM_CancelWriteAll shall treat multiple requests to cancel a running NvM_WriteAll request as one request, i.e. subsequent requests will be ignored.] ()

[SWS_NvM_00235] [The request result of the function NvM_CancelWriteAll shall be implicitly given by the result of the NvM_WriteAll request to be canceled.] ()

[SWS_NvM_00255] [The function NvM_CancelWriteAll shall ignore an already pending NvM_CancelWriteAll request. | ()

[SWS_NvM_00236] The function NvM_CancelWriteAll shall only modify the error/status attribute field of the pending blocks to NVM_REQ_CANCELED and for the currently written block after the processing of a single NVRAM block is finished to NVM_REQ_OK or NVM_REQ_NOT_OK depending on the success of the write operation.] ()





[SWS_NvM_00716] [The NvM module's environment shall have initialized the NvM module before it calls the function function NvM_CancelWriteAll.] ()

[SWS_NvM_00420] [The function NvM_CancelWriteAll shall signal the NvM module and shall not be queued, i.e. there can be only one pending request of this type.] ()



8.1.3.2.6 NvM InvalidateNvBlock

[SWS_NvM_00459] [

Service name:	NvM InvalidateN	lvBlock				
Syntax:	Std_ReturnType		NvM_Blocklo	dТуре	NvM_Invalid	lateNvBlock(BlockId
Service ID[hex]:	0x0b					
Sync/Async:	Asynchronous					
Reentrancy:	Reentrant					
Parameters (in):		descriptor.	A NVRAM	niquely identif block descrip NVRAM bloc	tor contains	
Parameters (inout):	None		-			
Parameters (out):	None					
Return value:	Std_ReturnType			has not been acce	been epted	accepted
Description:	Service to invalid	late a NV blo	ock.			

] (SRS_Mem_08011)

[SWS_NvM_00421] [The job of the function NvM_InvalidateNvBlock shall invalidate a NV block.] (SRS_Mem_08011)

[SWS_NvM_00422] [The job of the function NvM_InvalidateNvBlock shall leave the RAM block unmodified.] ()

[SWS_NvM_00423] [The function NvM_InvalidateNvBlock shall check the write protection attribute of the NV block in the administrative part of the corresponding RAM block. In case of failure an NVM_E_WRITE_PROTECTED / (during production) error shall be reported. | ()

[SWS_NvM_00424] [The function NvM_InvalidateNvBlock shall queue the request if the write protection of the corresponding NV block is disabled.] ()

[SWS_NvM_00239] [The function NvM_InvalidateNvBlock shall take over the given parameters, queue the request and return. | ()

[SWS_NvM_00272] [The function NvM_InvalidateNvBlock shall return with E_NOT_OK if a write protected NV block is referenced by the BlockId parameter.] ()

[SWS_NvM_00664] [The function NvM_InvalidateNvBlock shall return with E_NOT_OK if a ROM block of a dataset NVRAM block is referenced by the BlockId parameter. | ()



[SWS_NvM_00273] The function NvM_InvalidateNvBlock shall check the write protection attribute of a NVRAM block only before the job is put to the job queue. In case of failure an NVM_E_WRITE_PROTECTED / (during production) error shall be reported.] ()

[SWS_NvM_00665] [The NvM module shall not recheck write protection before fetching the job from the job queue.] ()

[SWS_NvM_00274] If the referenced NVRAM block is of type NVM_BLOCK_REDUNDANT, the function NvM_InvalidateNvBlock shall only set the request result NvM_RequestResultType to NVM_REQ_OK when both NV blocks have been invalidated. | ()

[SWS_NvM_00275] [The function NvM_InvalidateNvBlock shall set the job result to NVM_REQ_NOT_OK if the processing of this service fails.] ()

[SWS_NvM_00666] [The function NvM_InvalidateNvBlock shall report NVM_E_REQ_FAILED to the DEM if the processing of this service fails.] ()

[SWS_NvM_00717] [The NvM module's environment shall have initialized the NvM module before it calls the function function NvM InvalidateNvBlock. | ()

NVRAM common block configuration parameters [<u>SWS_NvM_00028</u>], block management types [<u>ECUC_NvM_00061</u>] and one configured NVRAM block descriptor are needed in the configuration with respect to the function NvM_InvalidateBlock [NVM062].



8.1.3.2.7 NvM ReadPRAMBlock

[SWS NvM 00764] [

[<u>3442_144141_0074</u>	
Service name:	NvM_ReadPRAMBlock
Syntax:	Std_ReturnType NvM_ReadPRAMBlock NvM_BlockIdType BlockI
Service ID[hex]:	0x16
Sync/Async:	Asynchronous
Reentrancy:	Reentrant
Parameters (in):	BlockId The block identifier uniquely identifies one NVRAM block descriptor. A NVRAM block descriptor contains all neede information about a single NVRAM block.
Parameters (inout):	None
Parameters (out):	None
Return value:	Std_ReturnType E_OK: request has been accepte E_NOT_OK: request has not been accepted
Description:	Service to copy the data of the NV block to its corresponding permanent RAN block.

] (SRS_LIBS_08533, BSW176, SRS_Mem_00016)

[SWS_NvM_00765] [The job of the function NvM_ReadPRAMBlock shall copy the data of the NV block to the permanent RAM block.] (SRS_Mem_00016)

[SWS_NvM_00766] [The function NvM_ReadPRAMBlock shall take over the given parameters, queue the read request in the job queue and return.] (SRS_Mem_00016)

[SWS_NvM_00767] The function NvM_ReadPRAMBlock shall invalidate a permanent RAM block immediately when the block is successfully enqueued or the job processing starts, i.e. copying data from NV memory or ROM to RAM. If the block has a synchronization callback (NvM_ReadRamBlockFromNvm) configured the invalidation will be done just before NvMReadRamBlockFromNvM is called. ()

[SWS_NvM_00768] The job of the function NvM_ReadPRAMBlock shall initiate a read attempt on the second NV block if the passed BlockId references a NVRAM block of type NVM_BLOCK_REDUNDANT and the read attempts on the first NV block fail.] ()

[SWS_NvM_00769] [In case of NVRAM block management type NVM_BLOCK_DATASET, the job of the function NvM_ReadPRAMBlock shall copy only that NV block to the corresponding RAM block which is selected via the data index in the administrative block. | ()

[SWS_NvM_00770] The job of the function NvM_ReadPRAMBlock shall not copy the NV block to the corresponding RAM block if the NVRAM block management type



is NVM_BLOCK_DATASET and the NV block selected by the dataset index is invalidate.] ()

[SWS_NvM_00771] The job of the function NvM_ReadPRAMBlock shall not copy the NV block to the corresponding RAM block if the NVRAM block management type is NVM_BLOCK_DATASET and the NV block selected by the dataset index is inconsistent.] ()

[SWS_NvM_00772] [The job of the function NvM_ReadPRAMBlock shall copy the ROM block to RAM and set the job result to NVM_REQ_OK if the NVRAM block management type is NVM_BLOCK_DATASET and the dataset index points at a ROM block. | ()

[SWS_NvM_00773] The job of the function NvM_ReadPRAMBlock shall set the RAM block to valid and assume it to be unchanged after a successful copy process of the NV block to RAM.] ()

[SWS_NvM_00774] [The job of the function NvM_ReadPRAMBlock shall set the RAM block to valid and assume it to be changed if the default values are copied to the RAM successfully.] ()

[SWS_NvM_00775] [The job of the function NvM_ReadPRAMBlock shall set the job result to NVM_REQ_OK if the NV block was copied successfully from NV memory to RAM.] ()

[SWS_NvM_00776] The job of the function NvM_ReadPRAMBlock shall set the request result to NVM_REQ_NV_INVALIDATED if the MemIf reports MEMIF BLOCK INVALID. | ()

[SWS_NvM_00777] [The job of the function NvM_ReadPRAMBlock shall report no error to the DEM if the MemIf reports MEMIF_BLOCK_INVALID. | ()

[SWS_NvM_00778] [The job of the function NvM_ReadPRAMBlock shall set the request result to NVM_REQ_INTEGRITY_FAILED if the MemIf reports MEMIF_BLOCK_INCONSISTENT.] ()

[SWS_NvM_00779] [The job of the function NvM_ReadPRAMBlock shall report NVM_E_INTEGRITY_FAILED to the DEM if the MemIf reports MEMIF_BLOCK_INCONSISTENT. | ()

[SWS_NvM_00780] The job of the function NvM_ReadPRAMBlock shall set the request result to NVM_REQ_NOT_OK if the MemIf reports MEMIF_JOB_FAILED.] (



[SWS_NvM_00781] [The job of the function NvM_ReadPRAMBlock shall report NVM_E_REQ_FAILED to the DEM if the MemIf reports MEMIF_JOB_FAILED.] ()

[SWS_NvM_00782] [The job of the function NvM_ReadPRAMBlock shall set the job result to NVM_REQ_OK if the block management type of the given NVRAM block is NVM_BLOCK_REDUNDANT and one of the NV blocks was copied successfully from NV memory to RAM.] ()

[SWS_NvM_00783] [The job of the function NvM_ReadPRAMBlock shall report no error to the DEM if the block management type of the given NVRAM block is NVM_BLOCK_REDUNDANT and one of the NV blocks was copied successfully from NV memory to RAM.] ()

[SWS_NvM_00784] [The job of the function NvM_ReadPRAMBlock shall mark every NVRAM block that has been configured with NVM_WRITE_BLOCK_ONCE (TRUE) that is not detected by underlying SW as being invalidated, shall be marked as write protected.] ()

[SWS_NvM_00785] [The job of the function NvM_ReadPRAMBlock shall invalidate a NVRAM block of management type redundant if both NV blocks have been invalidated. | ()

[SWS_NvM_00786] [The job of the function NvM_ReadPRAMBlock shall request a CRC recalculation over the RAM block data after the copy process [SWS_NvM_00180] if the NV block is configured with CRC, i.e. if NvMCalRamBlockCrC == TRUE for the NV block.] ()

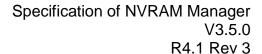
[SWS_NvM_00787] [The job of the function NvM_ ReadPRAMBlock shall load the default values according to processing of NvM_RestorePRAMBlockDefaults if the recalculated CRC is not equal to the CRC stored in NV memory. | ()

[SWS_NvM_00788] [NvM_ReadPRAMBlock: If there are no default values available, the RAM blocks shall remain invalid. | ()

[SWS_NvM_00789] [The job of the function NvM_ReadPRAMBlock shall load the default values according to processing of NvM_RestorePRAMBlockDefaults if the read request passed to the underlying layer fails.] ()

[SWS_NvM_00790] [The job of the function NvM_ReadPRAMBlock shall report NVM_E_INTEGRITY_FAILED to the DEM if a CRC mismatch occurs.] ()

[SWS_NvM_00791] [The job of the function NvM_ReadPRAMBlock shall set the job result NVM_REQ_INTEGRITY_FAILED if a CRC mismatch occurs.] ()





[SWS_NvM_00792] [The NvM module's environment shall have initialized the NvM module before it calls the function NvM_ReadPRAMBlock.] ()

Hint: NVRAM common block configuration parameters [SWS NvM 00028], block management types [ECUC NvM 00061] and one configured NVRAM block descriptor are needed for configuration with respected to the function NvM_ReadPRAMBlock [NVM062].



8.1.3.2.8 NvM_WritePRAMBlock

[SWS NvM 00793] [

[<u>5445_144141_007</u> ;	
Service name:	NvM_WritePRAMBlock
Syntax:	Std_ReturnType NvM_BlockIdType NvM_BlockIdType BlockI
Service ID[hex]:	0x17
Sync/Async:	Asynchronous
Reentrancy:	Reentrant
Parameters (in):	BlockId The block identifier uniquely identifies one NVRAM block descriptor. A NVRAM block descriptor contains all neede information about a single NVRAM block.
Parameters (inout):	None
Parameters (out):	None
Return value:	Std_ReturnType E_OK: request has been accepte E_NOT_OK: request has not been accepted
Description:	Service to copy the data of the RAM block to its corresponding permanent RAN block.

(BSW176, SRS Mem 00017)

[SWS_NvM_00794] [The job of the function NvM_WritePRAMBlock shall copy the data of the permanent RAM block to its corresponding NV block.] (SRS_Mem_00017)

[SWS_NvM_00795] [The function NvM_WritePRAMBlock shall test the write protection attribute of the NV block in the administrative part of the corresponding RAM block. In case of failure an NVM_E_WRITE_PROTECTED / (during production) error shall be reported. | (SRS_Mem_00017)

[SWS_NvM_00796] [The function NvM_WritePRAMBlock shall return with E_NOT_OK, if a write protected NVRAM block is referenced by the passed BlockId parameter.] ()

[SWS_NvM_00797] [The function NvM_WritePRAMBlock shall return with E_NOT_OK, if a locked NVRAM block is referenced by the passed BlockId parameter. and a DET error (see Section 7.6) shall be emitted.] ()

[SWS_NvM_00798] [The function NvM_WritePRAMBlock shall take over the given parameters, queue the write request in the job queue and return.] (SRS_Mem_08541)

[SWS_NvM_00799] The function NvM_WritePRAMBlock shall check the NVRAM block protection when the request is enqueued but not again before the request is executed. ()



[SWS_NvM_00800] [The function NvM_WritePRAMBlock shall cancel a pending job immediately in a destructive way if the passed BlockId references a NVRAM block configured to have immediate priority. The immediate job shall be the next active job to be processed. | ()

[SWS_NvM_00801] [The job of the function NvM_WritePRAMBlock shall request a CRC recalculation before the RAM block will be copied to NV memory if the NV block is configured with CRC [SWS_NvM_00180].] ()

[SWS_NvM_00802] The job of the function NvM_WritePRAMBlock shall copy the RAM block to the corresponding NV block which is selected via the data index in the administrative block if the NVRAM block management type of the given NVRAM block is NVM_BLOCK_DATASET.] ()

[SWS_NvM_00803] [The job of the function NvM_WritePRAMBlock shall assume a referenced permanent RAM block or the RAM mirror in the NvM module in case of explicit synchronization to be valid when the request is passed to the NvM module. If the permanent RAM block is still in an invalid state, the function NvM_WritePRAMBlock shall validate it automatically before copying the RAM block contents to NV memory or after calling explicit synchronization callback (NvM_WriteRamBlockToNvm). | ()

[SWS_NvM_00804] [The job of the function NvM_WritePRAMBlock shall check the number of write retries using a write retry counter to avoid infinite loops. Each negative result reported by the memory interface shall be followed by an increment of the retry counter. In case of a retry counter overrun, the job of the function NvM_WritePRAMBlock shall set the job result to NVM_REQ_NOT_OK.] (SRS Mem 08554)

[SWS_NvM_00805] [The job of the function NvM_WritePRAMBlock shall check the number of write retries using a write retry counter to avoid infinite loops. Each negative result reported by the memory interface shall be followed by an increment of the retry counter. In case of a retry counter overrun, the job of the function NvM_WritePRAMBlock shall report NVM_E_REQ_FAILED to the DEM. | ()

[SWS_NvM_00806] [The configuration parameter NVM_MAX_NUM_OF_WRITE_RETRIES [SWS_NvM_00028] shall prescribe the maximum number of write retries for the job of the function NvM_WritePRAMBlock when RAM block data cannot be written successfully to the corresponding NV block. | ()

[SWS_NvM_00807] [The job of the function NvM_WritePRAMBlock shall copy the data content of the RAM block to both corresponding NV blocks if the NVRAM block



management type of the processed NVRAM block is NVM_BLOCK_REDUNDANT.
()

[SWS_NvM_00808] [If the processed NVRAM block is of type NVM_BLOCK_REDUNDANT the job of the function NvM_WritePRAMBlock shall start to copy the data of the RAM block to NV block which has not been read during the jobs started by NvM_ReadBlock, NvM_ReadPRAMBlock or NvM_ReadAll then continue to copy the other NV block.] ()

[SWS_NvM_00809] [The job of the function NvM_WritePRAMBlock shall set NVM_REQ_OK as job result if the passed BlockId references a NVRAM block of type NVM_BLOCK_REDUNDANT and at least one of the NV blocks have been written successfully.] ()

[SWS_NvM_00810] The job of the function NvM_WritePRAMBlock shall set the write protection flag in the administrative block immediately if the NVRAM block is configured with NvMWriteBlockOnce == TRUE and the data has been written successfully to the NV block.] ()

[SWS_NvM_00811] The NvM module's environment shall have initialized the NvM module before it calls the function NvM_WritePRAMBlock.] () Hint:

To avoid the situation that in case of redundant NVRAM blocks two different NV blocks are containing different but valid data at the same time, each client of the function NvM_WritePRAMBlock may call NvM_InvalidateNvBlock in advance. NVRAM common block configuration parameters [SWS_NvM_00028], block management types [ECUC_NvM_00061] and one configured NVRAM block descriptor are needed in the configuration with respect to the function NvM_WritePRAMBlock [NVM062].

[SWS_NvM_00812] The job of the function NvM_WritePRAMBlock with Block ID 1 shall write the compiled NVRAM configuration ID to the stored NVRAM configuration ID (block 1).] ()

Hint: If a pristine ECU is flashed for the first time, such a call invoked by will ensure that after a power-off without a proper shutdown, everything is as expected at the next start-up. Otherwise, the new configuration ID would not be stored in NV RAM and all ROM defaultd would be used.

A macro scan be used to indicate this usage.



8.1.3.2.9 NvM RestorePRAMBlockDefaults

[SWS NvM 00813] [

Service name:	NvM RestorePR	AMBlockDe	afaulte			
			rauits	NI-M Doo	toro DD AMD	la als Dafa vilta /
Syntax:	Std_ReturnType				StorePRAINIB	lockDefaults(
			NvM_Blockle	dType		BlockId
)					
Service ID[hex]:	0x18					
Sync/Async:	Asynchronous					
Reentrancy:	Non Reentrant					
	BlockId	The block	identifier u	niquely identifi	es one NV	/RAM block
Parameters (in):		descriptor.	A NVRAM	block descript	or contains	all needed
		information	about a single	e NVRAM block		
Parameters	None					
(inout):						
Parameters (out):	None					
Detama	Std_ReturnType	E_OK:	request	has	been	accepted
Return value:		E_NOT_O	: request has	not been accep	oted	·
Description:	Service to restor	e the defaul	t data to its co	orresponding pe	rmanent RAI	M block.

] (SRS_Mem_00018)

[SWS_NvM_00814] [The job of the function NvM_RestorePRAMBlockDefaults shall restore the default data to its corresponding permanent RAM block.] (SRS_Mem_00018)

[SWS_NvM_00815] [The function NvM_RestorePRAMBlockDefaults shall take over the given parameters, queue the request in the job queue and return. | ()

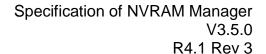
[SWS_NvM_00816] [The job of the function NvM_RestorePRAMBlockDefaults shall load the default data from a ROM block if a ROM block is configured.] (SRS_Mem_00018)

[SWS_NvM_00817] The NvM module's environment shall call the function NvM_RestorePRAMBlockDefaults to obtain the default data if no ROM block is configured for a NVRAM block and an application callback routine is configured via the parameter NvMInitBlockCallback. | (SRS_Mem_00018)

[SWS_NvM_00818] [The function NvM_RestorePRAMBlockDefaults shall return with E_NOT_OK if the block management type of the given NVRAM block is NVM_BLOCK_DATASET, at least one ROM block is configured and the data index points at a NV block.] ()

[SWS_NvM_00819] [The job of the function NvM_RestorePRAMBlockDefaults shall invalidate a RAM block before copying default data to the permanent RAM block or before explicit synchronization callback (NvMReadRamBlockFromNvM) is called. | ()

[SWS_NvM_00820] [The job of the function NvM_RestorePRAMBlockDefaults shall validate and assume a RAM block to be changed if the requested RAM block is Document ID 033: AUTOSAR_SWS_NVRAMManager





permanent or after explicit synchronization callback (NvMReadRamBlockFromNvM) that is called returns E_OK and the copy process of the default data to RAM was successful .] ()

[SWS_NvM_00821] [The job of the function NvM_RestorePRAMBlockDefaults shall request a recalculation of CRC from a RAM block after the copy process/validation if a CRC is configured for this RAM block.] ()

[SWS_NvM_00822] [The NvM module's environment shall have initialized the NvM module before it calls the function NvM_RestorePRAMBlockDefaults.] ()

Hint: For the block management type NVM_BLOCK_DATASET, the application has to ensure that a valid dataset index is selected (pointing to ROM data). NVRAM common block configuration parameters [SWS_NVM_00028], block management types [ECUC_NVM_00061] and one configured NVRAM block descriptor are needed in the configuration with respect to the function NvM_RestorePRAMBlockDefaults [NVM062].



8.1.3.3 Asynchronous multi block requests

8.1.3.3.1 NvM_ReadAll

[SWS	NvM	0046	301 [

Service name:	NvM_ReadAll
Syntax:	void NvM_ReadAll(
	void
Service ID[hex]:	0x0c
Sync/Async:	Asynchronous
Reentrancy:	Non Reentrant
Parameters (in):	None
Parameters	None
(inout):	
Parameters (out):	None
Return value:	None
Description:	Initiates a multi block read request.

(SRS_LIBS_08533, BSW176)

[SWS_NvM_00356] [The multi block service NvM_ReadAll shall provide two distinct functionalities.

- Initialize the management data for all NVRAM blocks (see <u>SWS_NvM_00304</u> ff)
- Copy data to the permanent RAM blocks or call explicit synchronization callback(NvM_ReadRamBlockFromNvm) for those NVRAM blocks which are configured accordingly.

Note: The two functionalities can be implemented in one loop.] ()

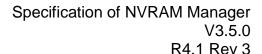
[SWS_NvM_00243] [The function NvM_ReadAll shall signal the request to the NvM module and return. The NVRAM Manager shall defer the processing of the requested ReadAll until all single block job queues are empty. | ()

[SWS_NvM_00304] [The job of the function NvM_ReadAll shall set each proceeding block specific job result for NVRAM blocks in advance.] ()

[SWS_NvM_00667] [The job of the function NvM_ReadAll shall set the multi block job result to NVM_REQ_PENDING in advance.] ()

[SWS_NvM_00244] [The job of the function NvM_ReadAll shall iterate over all user NVRAM blocks, i.e. except for reserved Block Ids 0 (multi block request result) and 1 (NV configuration ID), beginning with the lowest Block Id.] ()

[SWS_NvM_00245] [Blocks of management type NVM_BLOCK_DATASET shall not be loaded automatically upon start-up. Thus the selection of blocks, which belong to





block management type NVM_BLOCK_DATASET, shall not be possible for the service NvM ReadAll. | ()

[SWS_NvM_00362] [The NvM module shall initiate the recalculation of the RAM CRC for every NVRAM block with a valid permanent RAM block or explicit synchronization callback configured and NvmCalcRamBlockCrc == TRUE during the processing of NvM ReadAll.]()

[SWS_NvM_00364] [The job of the function NvM_ReadAll shall treat the data for every recalculated RAM CRC which matches the stored RAM CRC as valid and set the block specific request result to NVM REQ OK.

Note: This mechanism enables the NVRAM Manager to avoid overwriting of maybe still valid RAM data with outdated NV data. | ()

[SWS_NvM_00363] The job of the function NvM_ReadAll shall load default values in case the blocks are marked as invalid or if the recalculated CRC does not match. ()

[SWS_NvM_00246] [The job of the function NvM_ReadAll shall validate the configuration ID by comparing the stored NVRAM configuration ID vs. the compiled NVRAM configuration ID. | ()

[SWS_NvM_00669] [NvM_ReadAll: The NVRAM block with the block ID 1 (redundant type with CRC) shall be reserved to contain the stored NVRAM configuration ID.] ()

[SWS_NvM_00247] [The job of the function NvM_ReadAll shall process the normal runtime preparation for all configured NVRAM blocks in case of configuration ID match. | ()

[SWS_NvM_00670] [The job of the function NvM_ReadAll shall set the error/status information field of the corresponding NVRAM block's administrative block to NVM_REQ_OK in case of configuration ID match.] ()

[SWS_NvM_00305] [The job of the function NvM_ReadAll shall report the extended production error NVM_E_REQ_FAILED to the DEM if the configuration ID cannot be read because of an error detected by one of the subsequent SW layers.] ()

[SWS_NvM_00671] [The job of the function NvM_ReadAll shall set the error status field of the reserved NVRAM block to NVM_REQ_INTEGRITY_FAILED if the configuration ID cannot be read because of an error detected by one of the subsequent SW layers. The NvM module shall behave in the same way as if a configuration ID mismatch was detected. | ()



[SWS_NvM_00307] [The job of the function NvM_ReadAll shall set the error/status information field of the reserved NVRAM block with ID 1 to NVM_REQ_NOT_OK in the case of configuration ID mismatch.] ()

[SWS_NvM_00306] In case the NvM module can not read the configuration ID because the corresponding NV blocks are empty or invalidated, the job of the function NvM_ReadAll shall not report an extended production error or a production error to the DEM.] ()

[SWS_NvM_00672] [In case the NvM module can not read the configuration ID because the corresponding NV blocks are empty or invalidated, the job of the function NvM_ReadAll shall set the error/status information field in this NVRAM block's administrative block to NVM REQ NV INVALIDATED.] ()

[SWS_NvM_00673] [NvM_ReadAll: In case the NvM module can not read the configuration ID because the corresponding NV blocks are empty or invalidated, NVM module shall update the configuration ID from the RAM block assigned to the reserved NVRAM block with ID 1 according to the new (compiled) configuration ID. The NvM module shall behave the same way as if the configuration ID matched.] ()

[SWS_NvM_00248] [The job of the function NvM_ReadAll shall ignore a configuration ID mismatch and behave normal if NvMDynamicConfiguration == FALSE [SWS_NvM_00028].] ()

[SWS_NvM_00249] [The job of the function NvM_ReadAll shall process an extended runtime preparation for all blocks which are configured with NvMResistantToChangedSw == FALSE and NvMDynamicConfiguration == TRUE and configuration ID mismatch occurs. | ()

[SWS_NvM_00674] [The job of the function NvM_ReadAll shall process the normal runtime preparation of all NVRAM blocks when they are configured with NvMResistantToChangedSw == TRUE and NvMDynamicConfiguration == TRUE and if a configuration ID mismatch occurs. | ()

[SWS_NvM_00314] The job of the function NvM_ReadAll shall mark every NVRAM block that has been configured with NVM_WRITE_BLOCK_ONCE (TRUE) and that is not detected by underlying SW as being invalidated, shall be marked as write protected. This write protection cannot be cleared by NvM_SetBlockProtection. J ()

[SWS_NvM_00315] The job of the function NvM_ReadAll shall only invalidate a NVRAM block of management type NVM_BLOCK_REDUNDANT if both NV blocks have been invalidated.] ()



[SWS_NvM_00718] [The NvM module's environment shall use the multi block request NvM_ReadAll to load and validate the content of configured permanent RAM or to do the explicit synchronization for configured blocks during start-up [SWS_NvM_00091].] ()

[SWS_NvM_00118] [The job of the function NvM ReadAll shall process only the permanent RAM blocks call explicit synchronization callback or (NvM_ReadRamBlockFromNvm) for blocks which are configured with NvmSelectBlockForReadall == TRUE. ()

[SWS_NvM_00287] [The job of the function NvM_ReadAll shall set the job result to NVM_REQ_BLOCK_SKIPPED for all NVRAM blocks which are not loaded automatically during processing of the NvM_ReadAll job.] ()

[SWS_NvM_00426] [If configured by NvMDrvModeSwitch, the job of the function NvM_ReadAll shall switch the mode of each memory device to "fast-mode" before starting to iterate over all user NVRAM blocks.] ()

[SWS_NvM_00427] [If configured by NvMDrvModeSwitch, the job of the function NvM_ReadAll shall switch the mode of each memory device to "slow-mode" after having processed all user NVRAM blocks. | ()

[SWS_NvM_00308] [The job of the function NvM_ReadAll shall load the ROM default data to the corresponding RAM blocks and set the error/status field in the administrative block to NVM_REQ_OK when processing the extended runtime preparation.] ()

[SWS_NvM_00309] [When executing the extended runtime preparation, the job of the function NvM_ReadAll shall treat the affected NVRAM blocks as invalid or blank in order to allow rewriting of blocks configured with NVM_BLOCK_WRITE_ONCE == TRUE.] ()

[SWS_NvM_00310] The job of the function NvM_ReadAll shall update the configuration ID from the RAM block assigned to the reserved NVRAM block with ID 1 according to the new (compiled) configuration ID, mark the NVRAM block to be written during NvM_WriteAll and request a CRC recalculation if a configuration ID mismatch occurs and if the NVRAM block is configured with NvMDynamicConfiguration == TRUE.] ()

[SWS_NvM_00311] [The NvM module shall allow applications to send any request for the reserved NVRAM Block ID 1 if (and only if) NvMDynamicConfiguration is set to TRUE, including NvM_WriteBlock and NvM_WritePRAMBlock.] ()



[SWS_NvM_00312] [The NvM module shall not send a request for invalidation of the reserved configuration ID NVRAM block to the underlying layer, unless requested so by the application. This shall ensure that the NvM module's environment can rely on this block to be only invalidated at the first start-up of the ECU or if desired by the application.] ()

[SWS_NvM_00313] In case of a Configuration ID match, the job of the function NvM_ReadAll shall not automatically write to the Configuration ID block stored in the reserved NVRAM block 1. | ()

[SWS_NvM_00288] [The job of the function NvM_ReadAll shall initiate a read attempt on the second NV block for each NVRAM block of type NVM_BLOCK_REDUNDANT [SWS_NvM_00118], where the read attempt of the first block fails (see also SWS_NvM_00531).] ()

[SWS_NvM_00290] [The job of the function NvM_ReadAll shall set the block specific job result to NVM_REQ_OK if the job has successfully copied the corresponding NV block from NV memory to RAM. | ()

[SWS_NvM_00342] [The job of the function NvM_ReadAll shall set the block specific job result to NVM_REQ_NV_INVALIDATED if the MemIf reports MEMIF_BLOCK_INVALID.]()

[SWS_NvM_00676] [The job of the function NvM_ReadAll shall report no error to the DEM if the MemIf reports MEMIF_BLOCK_INVALID.] ()

[SWS_NvM_00360] [The job of the function NvM_ReadAll shall set the block specific job result to NVM_REQ_INTEGRITY_FAILED if the MemIf reports MEMIF BLOCK INCONSISTENT. | ()

[SWS_NvM_00677] [The job of the function NvM_ReadAll shall report NVM_E_INTEGRITY_FAILED to the DEM if the MemIf reports MEMIF_BLOCK_INCONSISTENT.]()

Note: After the production of an ECU / a car, on the production line all blocks shall have been written with valid data (may be default data) and all diagnostic events (errors) shall have been deleted. If the process does not allow to write all NV blocks during production than the NvM will report diagnostic events (errors) because of blocks that were never written and reported as MEMIF_BLOCK_INCONSISTENT by MemIf.

[SWS_NvM_00361] [The job of the function NvM_ReadAll shall set the block specific job result to NVM_REQ_NOT_OK if the MemIf reports MEMIF_JOB_FAILED.] ()



[SWS_NvM_00678] [The job of the function NvM_ReadAll shall report NVM_E_REQ_FAILED to the DEM, if the MemIf reports MEMIF_JOB_FAILED.] ()

[SWS_NvM_00291] [The job of the function NvM_ReadAll shall set the block specific job result to NVM_REQ_OK if the corresponding block management type is NVM_BLOCK_REDUNDANT and the function has successfully copied one of the NV blocks from NV memory to RAM.] ()

[SWS_NvM_00292] The job of the function NvM_ReadAll shall request a CRC recalculation over the RAM block data after the copy process SWS_NvM_00180 if the NV block is configured with CRC, , i.e. if NvMCalRamBlockCrC == TRUE for the NV block. |()

[SWS_NvM_00293] The job of the function NvM_ReadAll shall load the default values to the RAM blocks according to the processing of NvM_RestoreBlockDefaults if the recalculated CRC is not equal to the CRC stored in NV memory and if the default values are available. | ()

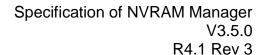
[SWS_NvM_00679] The job of the function NvM_ReadAll shall load the default values to the RAM blocks according to the processing of NvM_RestoreBlockDefaults if the read request passed to the underlying layer fails and if the default values are available.] ()

[SWS_NvM_00680] [NvM_ReadAll: If the read request passed to the underlying layer fails and there are no default values available, the job shall leave the RAM blocks invalid.] ()

[SWS_NvM_00295] The job of the function NvM_ReadAll shall set a block specific job result to NVM_REQ_INTEGRITY_FAILED if a CRC mismatch occurs.] ()

[SWS_NvM_00302] [The job of the function NvM_ReadAll shall report NVM_E_REQ_FAILED to the DEM if the referenced NVRAM Block is not configured with CRC and the corresponding job process has failed.] ()

[SWS_NvM_00301] [The job of the function NvM_ReadAll shall set the multi block job result to NVM_REQ_NOT_OK if the job fails with the processing of at least one NVRAM block. | ()





[SWS_NvM_00281] [If configured by NvMSingleBlockCallback, the job of the function NvM_ReadAll shall call the single block callback after having completely processed a NVRAM block.] ()

Note: The idea behind using the single block callbacks also for multi-block requests is to speed up the software initialization process:

- A single-block callback issued from a multi-block request (e.g. NvM_ReadAll) will result in an RTE event.
- If the RTE is initialized after or during the asynchronous multi-block request (e.g. NvM_ReadAll), all or some of these RTE events will get lost because they are overwritten during the RTE initialization (see SWS Rte 2536).
- After its initialization, the RTE can use the "surviving" RTE events to start software components even before the complete multi-block request (e.g. NvM ReadAll) has been finished.
- For those RTE events that got lost during the initialization: the RTE will start
 those software components and the software components either query the
 status of the NV block they want to access or request that NV block to be read.
 This is exactly the same behavior if the single-block callbacks would not be
 used in multi-block requests.

[SWS_NvM_00251] The job of the function NvM_ReadAll shall mark a NVRAM block as "valid/unmodified" if NV data has been successfully loaded to the RAM Block. ()

[SWS_NvM_00367] [The job of the function NvM_ReadAll shall set a RAM block to valid and assume it to be changed if the job has successfully copied default values to the corresponding RAM.] ()

[SWS_NvM_00719] [The NvM module's environment shall have initialized the NvM module before it calls the function NvM_ReadAll. | ()

The DEM shall already be able to accept error notifications.

NVRAM common block configuration parameters [<u>SWS NvM 00028</u>], block management types [<u>ECUC NvM 00061</u>] and all configured NVRAM block descriptors are needed in the configuration with respect to the function NvM_ReadAll [NVM062], [<u>SWS NvM 00069</u>].



8.1.3.3.2 NvM_WriteAll

<u> </u>	_NvM_	_0046	31]	
Sarvice	namo		NIV/N	Λ

Service name:	NvM WriteAll
Syntax:	void NvM_WriteAll(void void
Service ID[hex]:	0x0d
Sync/Async:	Asynchronous
Reentrancy:	Non Reentrant
Parameters (in):	None
Parameters (inout):	None
Parameters (out):	None
Return value:	None
Description:	Initiates a multi block write request.

(BSW176, SRS_LIBS_08535)

[SWS_NvM_00018] [The job of the function $NvM_WriteAll$ shall synchronize the contents of permanent RAM blocks to their corresponding NV blocks or call explicit synchronization callback (NvM_WriteRamBlockToNvm) on shutdown.] (SRS LIBS 08535)

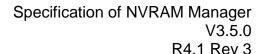
[SWS_NvM_00733] [If NVRAM block ID 1 (which holds the configuration ID of the memory layout) is marked as "to be written during NvM_WriteAll", the job of the function NvM_WriteAll shall write this block in a final step (last write operation) to prevent memory layout mismatch in case of a power loss failure during write operation.] ()

[SWS_NvM_00254] [The function NvM_WriteAll shall signal the request to the NvM module and return. The NVRAM Manager shall defer the processing of the requested WriteAll until all single block job queues are empty. | ()

[SWS_NvM_00549] [The job of the function NvM_ WriteAll shall set each proceeding block specific job result for NVRAM blocks and the multi block job result to NVM_REQ_PENDING in advance.] ()

[SWS_NvM_00252] [The job of the function NvM_WriteAll shall process only the permanent RAM blocks or call explicit synchronization callback (NvM_WriteRamBlockToNvm) for all blocks for which the corresponding NVRAM block parameter NvMSelectBlockForWriteAll is configured to true.] ()

[SWS_NvM_00430] If configured by NvMDrvModeSwitch, the job of the function NvM_WriteAll shall set the mode of each memory device to "fast-mode" before starting to iterate over all non-reserved NVRAM blocks.] ()





[SWS_NvM_00431] [If configured by NvMDrvModeSwitch, the job of the function NvM_WriteAll shall set the mode of each memory device to "slow-mode" after having processed all non-reserved NVRAM blocks. | ()

[SWS_NvM_00681] [If configured by NvMDrvModeSwitch, the job of the function NvM_WriteAll shall set the mode of each memory device to "slow-mode" after the function NvM_CancelWriteAll has canceled the job.] ()

[SWS_NvM_00432] The job of the function NvM_WriteAll shall check the write-protection for each RAM block in advance. | ()

[SWS_NvM_00682] [The job of the function NvM_WriteAll shall check the "valid/modified" state for each RAM block in advance.] ()

[SWS_NvM_00433] [The job of the function NvM_WriteAll shall only write the content of a RAM block to its corresponding NV block for non write-protected NVRAM blocks.] ()

[SWS_NvM_00474] The job of the function NvM_WriteAll shall correct the redundant data (if configured) if the redundancy has been lost. In this case the job of the function NvM_WriteAll shall ignore write protection for this block in order to be able to repair it.] ()

Note: If NvM implementation detects loss of redundancy during read operation the user (application) should ensure that redundant block is read (e.g. during NvM_ReadAll by configuring the block to be read during NvM_ReadAll). If the block is not read then the NVM will not be able to correct the redundant block's data.

[SWS_NvM_00434] [The job of the function NvM_WriteAll shall skip every write-protected NVRAM block without error notification.] ()

[SWS_NvM_00750] The job of the function NvM_WriteAll shall skip every locked NVRAM block without error notification. | ()

[SWS_NvM_00298] [The job of the function NvM_WriteAll shall set the job result for each NVRAM block which has not been written automatically by the job to NVM_REQ_BLOCK_SKIPPED.] ()

[SWS_NvM_00339] [In case of NVRAM block management type NVM_BLOCK_DATASET, the job of the function NvM_WriteAll shall copy only the RAM block to the corresponding NV block which is selected via the data index in the administrative block. | ()



[SWS_NvM_00253] The job of the function NvM_WriteAll shall request a CRC recalculation and renew the CRC from a NVRAM block before writing the data if a CRC is configured for this NVRAM block. | (SRS_LIBS_08535)

[SWS_NvM_00296] [The job of the function NvM_WriteAll shall check the number of write retries by a write retry counter to avoid infinite loops. Each unsuccessful result reported by the Memlf module shall be followed by an increment of the retry counter.] ()

[SWS_NvM_00683] [The job of the function NvM_WriteAll shall set the block specific job result to NVM_REQ_NOT_OK if the write retry counter becomes greater than the configured NVM_MAX_NUM_OF_WRITE_RETRIES.] ()

[SWS_NvM_00684] [The job of the function NvM_WriteAll shall report NVM_E_REQ_FAILED to the DEM if the write retry counter becomes greater than the configured NVM MAX NUM OF WRITE RETRIES. | ()

[SWS_NvM_00762] [The job of the function NvM_WriteAll shall copy the data content of the RAM block to both corresponding NV blocks if the NVRAM block management type of the processed NVRAM block is NVM_BLOCK_REDUNDANT.]
()

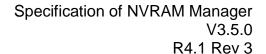
[SWS_NvM_00763] [If the processed NVRAM block is of type NVM_BLOCK_REDUNDANT the job of the function NvM_WriteAll shall start to copy the data of the RAM block to NV block which has _not_ been read during the jobs started by NvM_ReadBlock, NvM_ReadPRAMBlock or NvM_ReadAll then continue to copy the other NV block.] ()

[SWS_NvM_00337] The job of the function NvM_WriteAll shall set the single block job result to NVM_REQ_OK if the processed NVRAM block is of type NVM_BLOCK_REDUNDANT and at least one of the NV blocks has been written successfully.] ()

[SWS_NvM_00238] [The job of the function NvM_WriteAll shall complete the job in a non-destructive way for the NVRAM block currently being processed if a cancellation of NvM_WriteAll is signaled by a call of NvM_CancelWriteAll.] ()

[SWS_NvM_00237] [The NvM module shall set the multi block request result to NVM_REQ_CANCELED in case of cancellation of NvM_WriteAll.] ()

[SWS_NvM_00685] [NvM_WriteAll: The NvM module shall anyway report the error code condition, due to a failed NVRAM block write, to the DEM. | ()





[SWS_NvM_00318] [The job of the function NvM_WriteAll shall set the multi block request result to NVM_REQ_NOT_OK if processing of one or even more NVRAM blocks fails.] ()

[SWS_NvM_00329] [If the job of the function NvM_WriteAll has successfully written data to NV memory for a NVRAM block configured with NvMWriteBlockOnce == TRUE, the job shall immediately set the corresponding write protection flag in the administrative block.] ()

[SWS_NvM_00720] [The NvM module's environment shall have initialized the NvM module before it calls the function NvM_WriteAll. | ()

No other multiblock request shall be pending when the NvM module's environment calls the function NvM_WriteAll.

Note: To avoid the situation that in case of redundant NVRAM blocks two different NV blocks are containing different but valid data at the same time, each client of the NvM_WriteAll service may call NvM_InvalidateNvBlock in advance.

NVRAM common block configuration parameters [<u>SWS_NvM_00028</u>], block management types [<u>ECUC_NvM_00061</u>] and all configured NVRAM block descriptors are needed in the configuration with respect to the NvM_WriteAll function [NVM062], [<u>SWS_NvM_00069</u>].



8.1.3.4 Callback notification of the NvM module

[SWS_NvM_00438] [The NvM module shall provide callback functions to be used by the underlying memory abstraction (EEPROM abstraction / FLASH EEPROM Emulation) to signal end of job state with or without error.

Note: The file NvM_Cbk.h is to be included by the underlying memory driver layers.]

8.1.3.4.1 NVRAM Manager job end notification without error

[SWS NvM 00462] [

Service name:	NvM_JobEndNotification
Syntax:	void NvM_JobEndNotification(
	void
Service ID[hex]:	0x11
Sync/Async:	Synchronous
Reentrancy:	Non Reentrant
Parameters (in):	None
Parameters	None
(inout):	
Parameters (out):	None
Return value:	None
Description:	Function to be used by the underlying memory abstraction to signal end of job
	without error.

]()

[SWS_NvM_00111] [The callback function NvM_JobEndNotification is used by the underlying memory abstraction to signal end of job without error.

Note: Successful job end notification of the memory abstraction:

- Read finished & OK
- Write finished & OK
- Erase finished & OK

This routine might be called in interrupt context, depending on the calling function. All memory abstraction modules should be configured to use the same mode (callback/polling). | ()

[SWS_NvM_00440] [The NvM module shall only provide the callback function NvM_JobEndNotification if polling mode is disabled via NvMPollingMode. The function NvM_JobEndNotification is affected by the common [SWS_NvM_00028] configuration parameters.] ()



8.1.3.4.2 NVRAM Manager job end notification with error

SW	<u> </u>	∨M_(<u>0046</u>	63] <u> </u>

Service name:	NvM_JobErrorNotification
Syntax:	void NvM_JobErrorNotification(
Service ID[hex]:	0x12
Sync/Async:	Synchronous
Reentrancy:	Non Reentrant
Parameters (in):	None
Parameters (inout):	None
Parameters (out):	None
Return value:	None
Description:	Function to be used by the underlying memory abstraction to signal end of job with error.

(SRS_Mem_00125)

[SWS_NvM_00112] [The callback function NvM_JobErrorNotification is to be used by the underlying memory abstraction to signal end of job with error.

Note: Unsuccessful job end notification of the memory abstraction:

- Read aborted or failed
- Write aborted or failed
- Erase aborted or failed

This routine might be called in interrupt context, depending on the calling function. All memory abstraction modules should be configured to use the same mode (callback/polling).] ()

[SWS_NvM_00441] [The NvM module shall only provide the callback function NvM_JobErrorNotification if polling mode is disabled via NvMPollingMode. The function NvM_JoberrorNotification is affected by the common [SWS_NvM_00028] configuration parameters.] ()



8.1.3.5 Scheduled functions

Description:

These functions are directly called by the Basic Software Scheduler. The following functions shall have no return value and no parameter. All functions shall be non reentrant.

[SWS_NvM_004	[SWS_NvM_00464] [
Service name:	NvM_MainFunction				
Syntax:	void)	NvM_MainFunction(void			
Service ID[hex]:	0x0e				

J (SRS_BSW_00425, SRS_BSW_00373, SRS_BSW_00376, SRS_BSW_00172) **[SWS_NvM_00256]** [The function NvM_MainFunction shall perform the processing of the NvM module jobs. ∣ ()

Service for performing the processing of the NvM jobs.

[SWS_NvM_00333] [The function NvM_MainFunction shall perform the CRC recalculation if requested for a NVRAM block in addition to SWS_NvM_00256. | ()

[SWS_NvM_00334] [The NvM module shall only start writing of a block (i.e. hand over the job to the lower layers) after CRC calculation for this block has been finished. | ()

[SWS_NvM_00257] The NvM module shall only do/start job processing, queue management and CRC recalculation if the NvM_Init function has internally set an "INIT DONE" signal. | ()

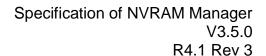
[SWS_NvM_00258] [The function NvM_MainFunction shall restart a destructively canceled request caused by an immediate priority request after the NvM module has processed the immediate priority request [SWS_NvM_00182].] ()

[SWS_NvM_00259] [The function NvM_MainFunction shall supervise the immediate priority queue (if configured) regarding the existence of immediate priority requests.] ()

[SWS_NvM_00346] [If polling mode is enabled, the function NvM_MainFunction shall check the status of the requested job sent to the lower layer.] ()

[SWS_NvM_00347] [If callback routines are configured, the function NvM_MainFunction shall call callback routines to the upper layer after completion of an asynchronous service. | ()

[SWS_NvM_00350] [In case of processing an NvM_WriteAll multi block request, the function NvM_MainFunction shall not call callback routines to the upper layer as long





as the service MemIf_GetStatus returns MEMIF_BUSY_INTERNAL for the reserved device ID MEMIF_BROADCAST_ID [7]. For this purpose (status is MEMIF_BUSY_INTERNAL), the function NvM_MainFunction shall cyclically poll the status of the Memory Hardware Abstraction independent of being configured for polling or callback mode.] ()

[SWS_NvM_00349] [The function NvM_MainFunction shall return immediately if no further job processing is possible.] ()

[SWS_NvM_00721] [NVRAM blocks with immediate priority are not expected to be configured to have a CRC.] ()



8.1.4 Expected Interfaces

In this chapter, all interfaces required by other modules are listed.

8.1.4.1 Mandatory Interfaces

The following table defines all interfaces which are required to fulfill the core functionality of the module.

[SWS NvM 00465][

API function	Description
EcuM_CB_NfyNvMJobEnd	Used to notify about the end of NVRAM jobs initiated by EcuM The callback must be callable from normal and interrupt execution contexts.
MemIf_Cancel	Invokes the "Cancel" function of the underlying memory abstraction module selected by the parameter DeviceIndex.
MemIf_EraseImmediateBlock	Invokes the "EraseImmediateBlock" function of the underlying memory abstraction module selected by the parameter DeviceIndex.
MemIf_GetJobResult	Invokes the "GetJobResult" function of the underlying memory abstraction module selected by the parameter DeviceIndex.
MemIf_GetStatus	Invokes the "GetStatus" function of the underlying memory abstraction module selected by the parameter DeviceIndex.
MemIf_InvalidateBlock	Invokes the "InvalidateBlock" function of the underlying memory abstraction module selected by the parameter DeviceIndex.
MemIf_Read	Invokes the "Read" function of the underlying memory abstraction module selected by the parameter DeviceIndex.
MemIf_Write	Invokes the "Write" function of the underlying memory abstraction module selected by the parameter DeviceIndex.

(SRS_BSW_00383, SRS_BSW_00384, BSW00421)

8.1.4.2 Optional Interfaces

The following table defines all interfaces which are required to fulfill an optional functionality of the module.

[SWS_NvM_00466] [

- · · · · · · · · · · · · · · · · · · ·		
API function	Description	
Crc_CalculateCRC16	This service makes a CRC16 calculation on Crc_Length data bytes.	
Crc_CalculateCRC32	This service makes a CRC32 calculation on Crc_Length data bytes.	
Crc_CalculateCRC8	This service makes a CRC8 calculation on Crc_Length data bytes, with SAE J1850 parameters	
Dem_ReportErrorStatus	Queues the reported events from the BSW modules (API is only used by BSW modules). The interface has an asynchronous behavior, because the processing of the event is done within the Dem main function. OBD Events Suppression shall be ignored for this computation.	
Det_ReportError	Service to report development errors.	
MemIf_SetMode	Invokes the "SetMode" functions of all underlying memory abstraction modules.	

J (SRS_BSW_00383, SRS_BSW_00384)



8.1.4.3 Configurable interfaces

In this chapter, all interfaces are listed for which the target function can be configured. The target function is usually a callback function. The names of these interfaces are not fixed because they are configurable.

[SWS_NvM_00113] The notification of a caller via an asynchronous callback routine (NvMSingleBlockCallback) shall be optionally configurable for all NV blocks (see ECUC_NvM_00061). ()

[SWS_NvM_00740] [If a callback is configured for a NVRAM block, every asynchronous block request to the block itself shall be terminated with an invocation of the callback routine. | ()

[SWS_NvM_00741] [The ID identifying the NVRAM service, shall be passed to the callback routine.] ()

[SWS_NvM_00742] [If no callback is configured for a NVRAM block, there shall be no asynchronous notification of the caller in case of an asynchronous block request.] ()

[SWS_NvM_00260] [A common callback entry (NvMMultiBlockCallback) which is not bound to any NVRAM block shall be optionally configurable for all asynchronous multi block requests (including NvM_CancelWriteAll). | ()

[SWS_NvM_00686] [The ID identifying the NVRAM service shall be passed to the common callback routine (NvMMultiBlockCallback). | ()

8.1.4.3.1 Single block job end notification

[SWS NvM 00467] [

Service name:	NvM_SingleBlockC	CallbackFunction	
Syntax:	Std_ReturnType NvM_SingleBlockCallbackFunction(
•		uint8 ServiceId,	
		NvM_RequestResultType JobResult	
)		
Sync/Async:	Synchronous		
Reentrancy:	Non Reentrant		
	ServiceId	Unique Service ID of NVRAM manager service.	
Parameters (in):	JobResult	Covers the job result of the previous processed single block job.	
Parameters (inout):	None		
Parameters (out):	None		
Return value:	Std_ReturnType	E_OK: Callback function has been processed successfully. E_NOT_OK: Callback function has not been processed successfully.	
Description:	Per block callback routine to notify the upper layer that an asynchronous single block request has been finished.		

]()



[SWS_NvM_00368] [The single block callback function shall always return with E OK.

There is no need for the NvM module to evaluate the return value of the single block callback function because of <u>SWS_NvM_00386</u>.] ()

[SWS_NvM_00330] [The single block callback function shall be a function pointer. Note: Please refer to NvMSingleBlockCallback in chapter 10. The Single block job end notification might be called in interrupt context, depending on the calling function.] (SRS_BSW_00387)

8.1.4.3.2 Multi block job end notification

[SWS_NvM_00468] [

Service name:	NvM MultiBlockCallbackFunction		
Syntax:	void	NvM_MultiBlockCallbackFunction(uint8 ServiceId, NvM_RequestResultType JobResult	
Sync/Async:	Synchronous		
Reentrancy:	Non Reentrant		
Doromotoro (in)	ServiceId	Unique Service ID of NVRAM manager service.	
Parameters (in):	JobResult	Covers the job result of the previous processed multi block job.	
Parameters (inout):	None		
Parameters (out):	None		
Return value:	None		
Description:	Common callback routine to notify the upper layer that an asynchronous multiblock request has been finished.		

]()

[SWS_NvM_00331] [The Multi block job end notification shall be a function pointer. Note: Please refer to NvMMultiBlockCallback in chapter 10. The Multi block job end notification might be called in interrupt context, depending on the calling function.] (SRS_BSW_00387)



8.1.4.3.3 Callback function for block initialization

ISWS NvM 004691

<u> </u>	00]		
Service name:	InitBlockCallbackFunct	ion	
Syntax:	Std_ReturnType	InitBlockCallbackFunction(
		void	
)		
Sync/Async:	Synchronous		
Reentrancy:	Non Reentrant		
Parameters (in):	None		
Parameters	None		
(inout):			
Parameters (out):	None		
	Std_ReturnType	E_OK: callback function has been processed successfully	
Return value:		E_NOT_OK: callback function has not been processed	
		successfully	
Description:	Per block callback routine which shall be called by the NvM module when default		
	data needs to be restored in RAM, even if a ROM block is configured.		
	Note: Here the application should copy default data to a RAM block if a ROM		
	block isn't configured and/or it could set some flags to know that default data was		
	restored.		

()

[SWS_NvM_00369] [The Init block callback for block initialization shall always return with E_OK. | ()

There is no need for the NvM module to evaluate the return value of the callback function because of <u>SWS_NvM_00369</u>.

[SWS_NvM_00352] [The Init block callback shall be a function pointer.

Note: Please refer to NvMInitBlockCallback in chapter 10. The init block callback function might be called in interrupt context. | ()



8.1.4.3.4 Callback function for RAM to NvM copy

[SWS NvM 00539] [

Service name:	NvM_WriteRamBlock	kToNvm	
Syntax:	Std_ReturnType	NvM_WriteRamBlockToNvm(
		void* NvMBuffer	
	7		
Service ID[hex]:			
Sync/Async:	Synchronous		
Reentrancy:	Non Reentrant		
Parameters (in):	None		
Parameters	None		
(inout):			
Parameters (out):	NvMBuffer	the address of the buffer where the data shall be written to	
	Std_ReturnType	E_OK: callback function has been processed successfully	
Return value:		E_NOT_OK: callback function has not been processed	
		successfully	
Description:	Block specific callback routine which shall be called in order to let the application		
	copy data from RAM	block to NvM module's mirror.	

]()

[SWS_NvM_00541] [The RAM to NvM copy callback shall be a function pointer.] () Note: Please refer to NvMWriteRamBlockToNvM in chapter 10.

8.1.4.3.5 Callback function for NvM to RAM copy

[SWS_NvM_00540] [

Service name:	NvM_ReadRamBloc	kFromNvm		
Syntax:	Std_ReturnType			amBlockFromNvm(
)	const	void*	NvMBuffer
Service ID[hex]:				
Sync/Async:	Synchronous			
Reentrancy:	Non Reentrant			
Parameters (in):	NvMBuffer	the address of t	he buffer where the data c	an be read from
Parameters (inout):	None			
Parameters (out):	None			
Return value:	Std_ReturnType		function has been proc allback function has no	
Description:	Block specific callba copy data from NvM		shall be called in order to RAM block.	let the application

(SRS_LIBS_08533, BSW176)

[SWS_NvM_00542] [The NvM to RAM copy callback shall be a function pointer.] () Note: Please refer to NvMReadRamBlockFromNvM in chapter 10.



8.1.5 API Overview

Request Types	Characteristics of Request Types
Type 1: - NvM_SetDataIndex () - NvM_GetDataIndex () - NvM_SetBlockProtection () - NvM_GetErrorStatus() - NvM_SetRamBlockStatus()	- synchronous request - affects one RAM block - available for all SW-Cs
Type 2: - NvM_ReadBlock() - NvM_WriteBlock() - NvM_RestoreBlockDefaults() - NvM_EraseNvBlock() - NvM_InvalidateNvBlock() - NvM_CancelJobs() - NvM_ReadPRAMBlock() - NvM_WritePRAMBlock() - NvM_RestorePRAMBlockDefaults()	- asynchronous request (result via callback or polling) - affects one NVRAM block - handled by NVRAM manager task via request list - available for all SW-Cs
Type 3: - NvM_ReadAll() - NvM_WriteAll() - NvM_CancelWriteAll()	asynchronous request (result via callback or polling) affects all NVRAM blocks with permanent RAM data
Type 4: - NvM_Init()	 synchronous request basic initialization success signaled to the task via command interface inside the function itself



8.2 Service Interfaces

This chapter is an addition to the specification of the NvM module. Whereas the other parts of the specification define the behavior and the C-interfaces of the corresponding basic software module, this chapter formally specifies the corresponding AUTOSAR service in terms of the SWC template. The interfaces described here will be visible on the VFB and are used to generate the RTE between application software and the NvM module.

8.2.1 Client-Server-Interfaces

8.2.1.1 **NvM_Admin**

[SWS_NvM_00737] [

[0110_111111_00101]			
Name	NvMAdm	nin	
Comment			
IsService	true		
Variation			
Danaihla Evrava	0	E_OK	
Possible Errors	1	E_NOT_OK	

Operations

SetBlockProtection			
Comments	Service for setting/resetting the write protection for a NV block.		
Variation	{ecuc(NvM/NvMCommon/NvMApiConfigClass)} == NVM_API_CONFIG_CLASS_3		
	ProtectionEnabled		
	Comment		
Parameters	Туре	boolean	
	Variation		
	Direction	IN	
Possible Errors	E_OK	Operation successful	
	E_NOT_OK		

]()



8.2.1.2 NvM_Mirror

[SWS_NvM_00738] [

[orro=rrim=orros]				
Name	N∨MMirro	or		
Comment				
IsService	true			
Variation				
Dansible France	0	E_OK		
Possible Errors	1	E_NOT_OK		

Operations

ReadRamBlo	ckFromNvM		
Comments	Block specific callback routine which shall be called in order to let the application copy data from NvM module's mirror to RAM block.		
Variation			
	SrcPtr		
Parameters	Comment	The parameter "SrcPtr" shall be typed by a ImplementationDataType of category DATA_REFERENCE wi the pointer target void to pass an address (pointer) to the RAI Block.	
	Туре	ConstVoidPtr	
	Variation		
	Direction	IN	
Possible Errors	E_OK	Operation successful	
	E_NOT_OK		

WriteRamBlockToNvM

Comments	Block specific callback routine which shall be called in order to let the application copy data from RAM block to NvM module's mirror.	
Variation		
	DstPtr	
Parameters	Comment	The parameter "DstPtr" shall be typed by an ImplementationDataType of category DATA_REFERENCE with the pointer target void to pass an address (pointer) to the RAM



		Block.
	Туре	VoidPtr
	Variation	
	Direction	IN
Possible Errors	E_OK	Operation successful
	E_NOT_OK	

]()

8.2.1.3 NvM_NotifyInitBlock

[SWS_NvM_00736] [

Name	NvMNotifyInitBlock	
Comment	Callback that is called by the NvM module when default data needs to be restored to the RAM image	
IsService	true	
Variation		
Possible Errors	0	E_OK

Operations

InitBlock		
Comments	This callback is called if the initialization of a block has completed.	
Variation		
Possible Errors	E_OK	Operation successful

]()



8.2.1.4 NvM_NotifyJobFinished

[SWS_NvM_00735][

	[0110_111111_00100]		
Name	NvMNotifyJobFinished		
Comment	Callback that is called when a job has finished		
IsService	true		
Variation			
Possible Errors	0	E_OK	

Operations

JobFinished			
Jobrinished			
Comments	Callback that gets called if a job has finished		
Variation			
	ServiceId		
	Comment		
	Туре	uint8	
	Variation		
Parameters	Direction	IN	
Farameters	JobResult		
	Comment		
	Туре	NvM_RequestResultType	
	Variation		
	Direction	IN	
Possible Errors	E_OK	Operation successful	

]()

8.2.1.5 NvM_Service

[SWS NvM 00734][

Name	NvMService	
Comment		
IsService	true	



Variation		
Dogojihla Erroro	0	E_OK
Possible Errors	1	E_NOT_OK

Operations

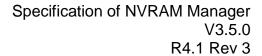
-			
EraseBlock			
Comments	Service to erase a NV block.		
Variation	{ecuc(NvM/NvMCommon/NvMApiConfigClass)} == NVM_API_CONFIG_CLASS_3		
Possible	E_OK	Operation successful	
Errors	E_NOT_OK		
GetDataIndex			

GetDataIndex			
Comments	Service for getting the currently set DataIndex of a dataset NVRAM block		
Variation	({ecuc(NvM/NvMCommon/NvMApiConfigClass)} == NVM_API_CONFIG_CLASS_2 {ecuc(NvM/NvMCommon/NvMApiConfigClass)} == NVM_API_CONFIG_CLASS_3)		
	DataIndexPtr		
	Comment		
Parameters	Туре	uint8	
	Variation		
	Direction	OUT	
Possible Errors	E_OK	Operation successful	
	E_NOT_OK		

GetErrorStatus Comments Service to read the block dependent error/status information. Variation - RequestResultPtr Comment Type NvM_RequestResultType



	Variation		
	Direction	OUT	
Possible	E_OK	Operation successful	
Errors	E_NOT_OK		
InvalidateNvE	Block		
Comments	Service to invalidate a NV block.		
Variation	{ecuc(NvM/NvMCommon/NvMApiC	ConfigClass)} == NVM_API_CONFIG_CLASS_3	
Possible	E_OK	Operation successful	
Errors	E_NOT_OK		
ReadBlock			
Comments	Service to copy the data of the NV block to its corresponding RAM block.		
Variation	({ecuc(NvM/NvMCommon/NvMApiConfigClass)} == NVM_API_CONFIG_CLASS_2 {ecuc(NvM/NvMCommon/NvMApiConfigClass)} == NVM_API_CONFIG_CLASS_3)		
	DstPtr		
Parameters	Comment	The parameter "DstPtr" shall be typed by an ImplementationDataType of category DATA_REFERENCE with the pointer target void to pass an address (pointer) to the RAM Block.	
	Туре	VoidPtr	
	Variation		
	Direction	IN	
Possible Errors	E_OK	Operation successful	
Ellois	E_NOT_OK		
ReadPRAMB	ReadPRAMBlock		
Comments			
Variation	({ecuc(NvM/NvMCommon/NvMApiConfigClass)} == NVM_API_CONFIG_CLASS_2 {ecuc(NvM/NvMCommon/NvMApiConfigClass)} == NVM_API_CONFIG_CLASS_3)		





Possible	E_OK	Operation successful	
Errors	E_NOT_OK		
RestoreBlockDe	efaults		
Comments S	Service to restore the default data to its	corresponding RAM block.	
Variation ({	{ecuc(NvM/NvMCommon/NvMApi0 ecuc(NvM/NvMCommon/NvMApiC	ConfigClass)} == NVM_API_CONFIG_CLASS_2 onfigClass)} == NVM_API_CONFIG_CLASS_3)	
С	OstPtr		
Parameters	Comment	The parameter "DstPtr" shall be typed by an ImplementationDataType of category DATA_REFERENCE with the pointer target void to pass an address (pointer) to the RAM Block.	
Т	Гуре	VoidPtr	
V	/ariation		
	Direction	IN	
Possible	E_OK	Operation successful	
Errors	E_NOT_OK		
RestorePRAMB	BlockDefaults		
Comments			
	({ecuc(NvM/NvMCommon/NvMApiConfigClass)} == NVM_API_CONFIG_CLASS_2 {ecuc(NvM/NvMCommon/NvMApiConfigClass)} == NVM_API_CONFIG_CLASS_3)		
Possible	E_OK	Operation successful	
Errors	E_NOT_OK		
SetDataIndex			
Comments S	Service for setting the DataIndex of a dataset NVRAM block.		
44	({ecuc(NvM/NvMCommon/NvMApiConfigClass)} == NVM_API_CONFIG_CLASS_2 {ecuc(NvM/NvMCommon/NvMApiConfigClass)} == NVM_API_CONFIG_CLASS_3)		
Variation 8	ecuc(NvM/NvMCommon/NvMApiC & {ecuc(NvM/NvMBlockD	onfigClass)} == NVM_API_CONFIG_CLASS_3)	



	T	T	
	Comment		
	Туре	uint8	
	Variation		
	Direction	IN	
Possible	E_OK	Operation successful	
Errors	E_NOT_OK		
SetRamBlock	Status		
Comments	Service for setting the RAM block stat	us of an NVRAM block.	
Variation	{ecuc(NvM/NvMBlockDescriptor/Nv	/MBlockUseSetRamBlockStatus)} == true	
	BlockChanged		
	Comment		
Parameters	Туре	boolean	
	Variation		
	Direction	IN	
Possible	E_OK	Operation successful	
Errors	E_NOT_OK		
WriteBlock			
Comments	Service to copy the data of the RAM block to its corresponding NV block.		
Variation	({ecuc(NvM/NvMCommon/NvMApiConfigClass)} == NVM_API_CONFIG_CLASS_2 {ecuc(NvM/NvMCommon/NvMApiConfigClass)} == NVM_API_CONFIG_CLASS_3)		
	SrcPtr		
Parameters	Comment	The parameter "SrcPtr" shall be typed by an ImplementationDataType of category DATA_REFERENCE with the pointer target void to pass an address (pointer) to the RAM Block.	
	Туре	ConstVoidPtr	
	Variation		
	Direction	IN	
		•	



Possible Errors	E_OK	Operation successful		
	E_NOT_OK			
WritePRAMB	WritePRAMBlock			
Comments				
Variation	({ecuc(NvM/NvMCommon/NvMApiConfigClass)} == NVM_API_CONFIG_CLASS_2 {ecuc(NvM/NvMCommon/NvMApiConfigClass)} == NVM_API_CONFIG_CLASS_3)			
Possible Errors	E_OK	Operation successful		
	E_NOT_OK			

]()

8.2.2 Implementation Data Types

8.2.2.1 ImplementationDataType NvM_RequestResultType

[SWS_NvM_00841]

Name	NvM_RequestResultType		
Kind	Туре		
Derived from	uint8		
Description	This is an asynchronous request result returned by the API service NvM_GetErrorStatus. The availability of an asynchronous request result can be additionally signaled via a callback function.		
Range	NVM_REQ_OK	0x00	0x00: The last asynchronous read/write/control request has been finished successfully. This shall be the default value after reset. This status shall have the value 0.
	NVM_REQ_NOT_OK	0x01	0x01: The last asynchronous read/write/control request has been finished unsuccessfully.
	NVM_REQ_PENDING	0x02	0x02: An asynchronous read/write/control request is currently pending.
	NVM_REQ_INTEGRITY_FAILED	0x03	0x03: The result of the last asynchronous request NvM_ReadBlock or NvM_ReadAll is a data integrity failure. Note: In case of NvM_ReadBlock the content of the



			RAM block has changed but has become invalid. The application is responsible to renew and validate the RAM block content.
	NVM_REQ_BLOCK_SKIPPED	0x04	0x04: The referenced block was skipped during execution of NvM_ReadAll or NvM_WriteAll, e.g. Dataset NVRAM blocks (NvM_ReadAll) or NVRAM blocks without a permanently configured RAM block.
	NVM_REQ_NV_INVALIDATED	0x05	0x05: The referenced NV block is invalidated.
	NVM_REQ_CANCELED	0x06	0x06: The multi block request NvM_WriteAll was canceled by calling NvM_CancelWriteAll. Or Any single block job request (NvM_ReadBlock, NvM_WriteBlock, NvM_EraseNvBlock, NvM_InvalidateNvBlock and NvM_RestoreBlockDefaults) was canceled by calling NvM_CancelJobs.
	NVM_REQ_REDUNDANCY_FAILED	0x07	0x07: The required redundancy of the referenced NV block is lost. (obsolete)
	NVM_REQ_RESTORED_FROM_ROM		0x08: The referenced NV block has been implicitly restored from ROM.
Variation			

ر)ر

8.2.2.2 ImplementationDataType NvM_BlockIdType

[SWS_NvM_00842]

Name	NvM_BlockIdType
Kind	Туре
Derived from	uint16
Description	Identification of a NVRAM block via a unique block identifier. Reserved NVRAM block IDs: 0 -> to derive multi block request results via NvM_GetErrorStatus 1 -> redundant NVRAM block which holds the configuration ID
Range	02^(16- NvMDatasetSelectionBits)-1
Variation	

() ا



8.2.2.3 ImplementationDataType ConstVoidPtr

[SWS_NvM_00848][

Name	ConstVoidPtr
Kind	Туре
ImplementationPolicy	const
Derived from	void
Description	
Variation	

J()

8.2.3 Ports

8.2.3.1 NvM_PAdmin_{Block}

[SWS_NvM_00843]

Name	PAdmin_{Block}		
Kind	ProvidedPort	Interface	NvMAdmin
Description			
Variation	Block = {ecuc(NvM/NvMBlockDescriptor.SHORT-NAME)}		

]()

8.2.3.2 NvM_PM_{Block}

[SWS_NvM_00844]

Name	PM_{Block}		
Kind	RequiredPort	Interface	N∨MMirror
Description			
Variation	Block = {ecuc(NvM/NvMBlockDescriptor.SHORT-NAME)}		

]()

8.2.3.3 NvM_PNIB_{Block}

[SWS_NvM_00845]

Name	PNIB_{Block}		
Kind	RequiredPort	Interface	NvMNotifyInitBlock
Description			



Variation	Block = {ecuc(NvM/NvMBlockDescriptor.SHORT-NAME)}
-----------	---

」()

8.2.3.4 NvM_PNJF_{Block}

[SWS_NvM_00846]

Name	PNJF_{Block}		
Kind	RequiredPort Interface NvMNotifyJobFinished		
Description			
Variation	Block = {ecuc(NvM/NvMBlockDescriptor.SHORT-NAME)}		

]()

8.2.3.5 NvM_PS_{Block}

[SWS_NvM_00847]

Name	PS_{Block}		
Kind	ProvidedPort Interface NvMService		
Description			
Variation	Block = {ecuc(NvM/NvMBlockDescriptor.SHORT-NAME)}		

J()



9 Sequence Diagrams

9.1 Synchronous calls

9.1.1 NvM_Init

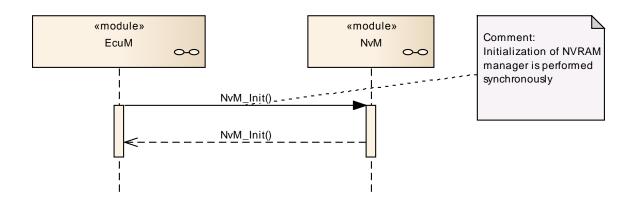


Figure 12: UML sequence diagram NvM_Init

9.1.2 NvM_SetDataIndex

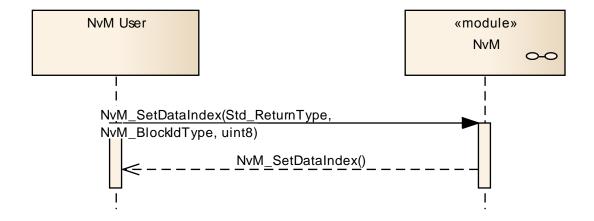


Figure 13: UML sequence diagram NvM_SetDataIndex



9.1.3 NvM_GetDataIndex

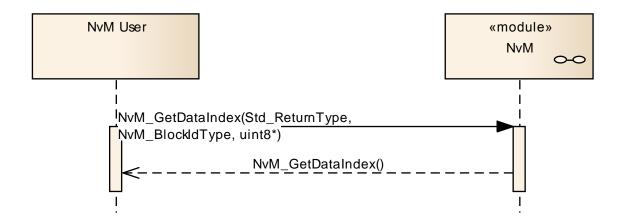


Figure 14: UML sequence diagram NvM_GetDataIndex

9.1.4 NvM_SetBlockProtection

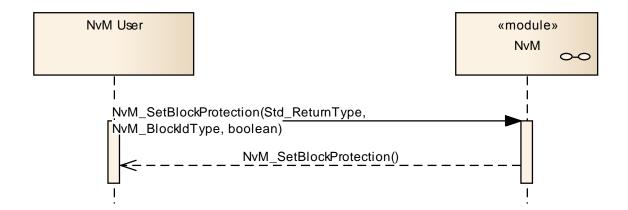


Figure 15: UML sequence diagram NvM_SetBlockProtection



9.1.5 NvM_GetErrorStatus

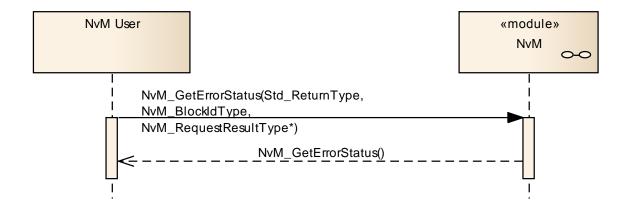


Figure 16: UML sequence diagram NvM_GetErrorStatus

9.1.6 NvM_GetVersionInfo

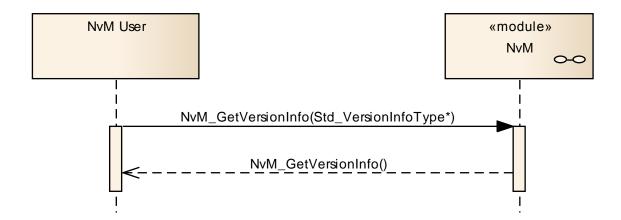


Figure 17: UML sequence diagram NvM_GetVersionInfo



9.2 Asynchronous calls

The following sequence diagrams concentrate on the interaction between the NvM module and SW-C's or the ECU state manager. For interaction regarding the Memory Interface please ref. to [5] or [6].

9.2.1 Asynchronous call with polling

The following diagram shows the function NvM_WriteBlock as an example of a request that is performed asynchronously. The sequence for all other asynchronous functions is the same, only the processed number of blocks and the block types may vary. The result of the asynchronous function is obtained by polling requests to the error/status information.

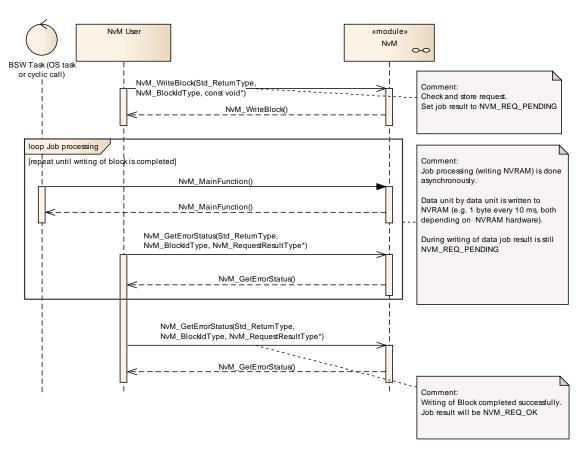


Figure 18: UML sequence diagram for asynchronous call with polling



9.2.2 Asynchronous call with callback

The following diagram shows the function NvM_WriteBlock as an example of a request that is performed asynchronously. The sequence for all other asynchronous functions is the same, only the processed number of blocks and the block types may vary. The result of the asynchronous function is obtained after an asynchronous notification (callback) by requesting the error/status information.

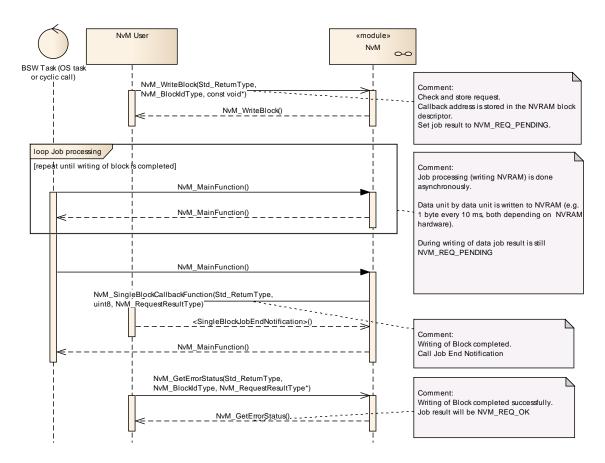


Figure 19: UML sequence diagram for asynchronous call with callback



9.2.3 Cancellation of a Multi Block Request

The following diagram shows the effect of a cancel operation applied to a running NvM_WriteAll multi block request. The running NvM_WriteAll function completes the actual NVRAM block and stops further writes.

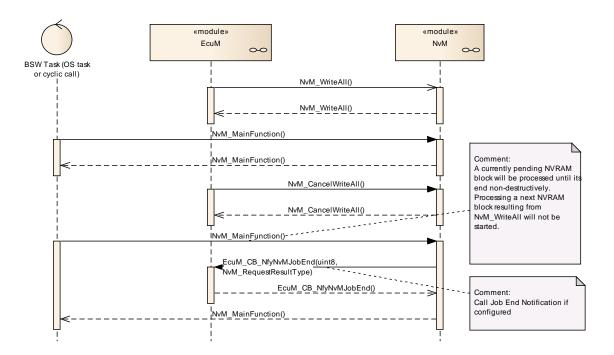


Figure 20: UML sequence diagram for cancellation of asynchronous call



10 Configuration specification

In general, this chapter defines configuration parameters and their clustering into containers. In order to support the specification Chapter 10.1 describes fundamentals. It also specifies a template (table) you shall use for the parameter specification.

Chapter 10.2 specifies the structure (containers) and the parameters of the module NvM.

Chapter 10.2.8 specifies published information of the module NvM.

10.1 How to read this chapter

For details refer to the chapter 10.1 "Introduction to configuration specification" in SWS_BSWGeneral.

10.2 Containers and configuration parameters

The following chapters summarize all configuration parameters. The detailed meanings of the parameters describe chapter 7.2 and chapter 8.

10.2.1 Variants

[SWS_NvM_00727] [The NVM module shall support the configuration variants VARIANT-PRE-COMPILE and VARIANT-LINK-TIME.

The VARIANT-PRE-COMPILE is designed for all parameters to be fixed at compile time.

The VARIANT-LINK-TIME is designed for the use cases where parameters are fixed at link-time. This variant is particularly useful for integrators not in possession of the NvM source code.] ()

10.2.2NvM

SWS Item	ECUC_NvM_00539:
Module Name	NvM
Module Description	Configuration of the NvM (NvRam Manager) module.

Included Containers	ncluded Containers				
Container Name	Multiplicity	Scope / Dependency			
NvMBlockDescriptor	165536	Container for a management structure to configure the composition of a given NVRAM Block Management Type. Its multiplicity describes the number of configured NVRAM blocks, one block is required to be configured. The NVRAM block descriptors are condensed in the NVRAM block descriptor table.			
NvMCommon	1	Container for common configuration options.			
NvmDemEventParameterRef s	01	Container for the references to DemEventParameter elements which shall be invoked using the API Dem_ReportErrorStatus API in case the corresponding error occurs. The EventId is			



taken from the referenced DemEventParameter's DemEventId
value. The standardized errors are provided in the container
and can be extended by vendor specific error references.

10.2.3NvMCommon

SWS Item	ECUC_NvM_00028:
Container Name	NvMCommon
Description	Container for common configuration options.
Configuration Parameters	

SWS Item	ECUC_NvM_00491:						
Name	NvMApiConfigClass {NVM_API_CONFIG_CLASS}						
Description	Preprocessor switch to enable some API calls which are related to NVM API						
	configuration classes.						
Multiplicity	1						
Туре	EcucEnumerationParamDef						
Range	NVM_API_CONFIG_CLASS_1	ΑII	API	calls	belo	nging	to
		cor	nfigurati	on d	class	1	are
	available.						
	NVM_API_CONFIG_CLASS_2	All	API	calls	belo	nging	to
		cor	nfigurati	on o	class	2	are
		ava	ailable.				
	NVM_API_CONFIG_CLASS_3	ΑII	API	calls	belo	nging	to
		cor	nfigurati	on o	class	3	are
		ava	ailable.				
ConfigurationClass	Pre-compile time	X	All Vari	ants			
	Link time						
	Post-build time						
Scope / Dependency	scope: local		-				

SWS Item	ECUC_NvM_00550:	ECUC_NvM_00550:			
Name	NvMBswMMultiBlockJobSta	NvMBswMMultiBlockJobStatusInformation			
	{NVM_BSWM_MULTI_BLO	{NVM_BSWM_MULTI_BLOCK_JOB_STATUS_INFORMATION}			
Description	This parameter specifies w	hethe	er BswM is informed about the current		
	status of the multiblock job.				
	True: call BswM_NvM_Cu	True: call BswM_NvM_CurrentJobMode if ReadAll and WriteAll are			
	started, finished, canceled F	started, finished, canceled False: do not inform BswM at all			
Multiplicity	1	1			
Type	EcucBooleanParamDef	EcucBooleanParamDef			
Default value	true				
ConfigurationClass	Pre-compile time	Pre-compile time X VARIANT-PRE-COMPILE			
	Link time X VARIANT-LINK-TIME				
	Post-build time				
Scope / Dependency	scope: local				

SWS Item	ECUC_NvM_00492 :
Name	NvMCompiledConfigId {NVM_COMPILED_CONFIG_ID}
Description	Configuration ID regarding the NV memory layout. This configuration ID shall be published as e.g. a SW-C shall have the possibility to write it to NV memory.
Multiplicity	1
Туре	EcucIntegerParamDef
Range	0 65535
Default value	



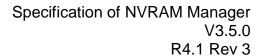
ConfigurationClass	Pre-compile time	X	All Variants
	Link time		
	Post-build time		
Scope / Dependency	scope: local		

SWS Item	ECUC_NvM_00493:	ECUC_NvM_00493:			
Name	NvMCrcNumOfBytes (N)	NvMCrcNumOfBytes {NVM_CRC_NUM_OF_BYTES}			
Description		If CRC is configured for at least one NVRAM block, this parameter defines the maximum number of bytes which shall be processed within one cycle of job processing.			
Multiplicity	1	1			
Type	EcucIntegerParamDef				
Range	1 65535				
Default value					
ConfigurationClass	Pre-compile time	X	VARIANT-PRE-COMPILE		
_	Link time				
	Post-build time	Post-build time			
Scope / Dependency	scope: local				

SWS Item	ECUC_NvM_00494:	ECUC_NvM_00494:			
Name	NvMDatasetSelectionBits {N	NvMDatasetSelectionBits {NVM_DATASET_SELECTION_BITS}			
Description	a certain dataset of a NVF hardware abstraction. 08: Number of bits whice addressing. 0: No dataset or redundate selection bits required.	08: Number of bits which are used for dataset or redundant block addressing.0: No dataset or redundant NVRAM blocks are configured at all, no selection bits required.1: In case of redundant NVRAM blocks are configured, but no dataset			
Multiplicity	1	1			
Туре	EcucIntegerParamDef				
Range	0 8				
Default value					
ConfigurationClass	Pre-compile time	Х	VARIANT-PRE-COMPILE		
	Link time	Link time X VARIANT-LINK-TIME			
	Post-build time				
Scope / Dependency	scope: dependency: MemHw NVM_BLOCK_MANAGEME		local NVM_NVRAM_BLOCK_IDENTIFIER, YPE		

SWS Item	ECUC_NvM_00495 :	ECUC_NvM_00495:			
Name	NvMDevErrorDetect (NVI	NvMDevErrorDetect {NVM_DEV_ERROR_DETECT}			
Description		Pre-processor switch to enable and disable development error detection. true: Development error detection enabled. false: Development error detection disabled.			
Multiplicity	1	1			
Туре	EcucBooleanParamDef	EcucBooleanParamDef			
Default value					
ConfigurationClass	Pre-compile time	X	All Variants		
	Link time	Link time			
	Post-build time				
Scope / Dependency	scope: local	•	•		

SWS Item	ECUC_NvM_00496:
Name	NvMDrvModeSwitch {NVM_DRV_MODE_SWITCH}





Description	Preprocessor switch to enable switching memory drivers to fast mode during performing NvM_ReadAll and NvM_WriteAll true: Fast mode enabled. false: Fast mode disabled.			
Multiplicity	1			
Type	EcucBooleanParamDef			
Default value				
ConfigurationClass	Pre-compile time X VARIANT-PRE-COMPILE			
	Link time X VARIANT-LINK-TIME			
	Post-build time			
Scope / Dependency	scope: local			

SWS Item	ECUC_NvM_00497:	ECUC_NvM_00497:			
Name	NvMDynamicConfiguration	NvMDynamicConfiguration {NVM_DYNAMIC_CONFIGURATION}			
Description	handling by the NvM_Read, true: Dynamic configurat	Preprocessor switch to enable the dynamic configuration management handling by the NvM_ReadAll request. true: Dynamic configuration management handling enabled. false: Dynamic configuration management handling disabled.			
Multiplicity	1	1			
Туре	EcucBooleanParamDef	EcucBooleanParamDef			
Default value					
ConfigurationClass	Pre-compile time	Pre-compile time X VARIANT-PRE-COMPILE			
	Link time	Link time X VARIANT-LINK-TIME			
	Post-build time	Post-build time			
Scope / Dependency	scope: local				

SWS Item	ECUC_NvM_00498:				
Name	NvMJobPrioritization (NV	NvMJobPrioritization {NVM_JOB_PRIORITIZATION}			
Description	Preprocessor switch to enable job prioritization handling true: Job prioritization handling enabled. false: Job prioritization handling disabled.				
Multiplicity	1	1			
Туре	EcucBooleanParamDef	EcucBooleanParamDef			
Default value					
ConfigurationClass	Pre-compile time	X	VARIANT-PRE-COMPILE		
	Link time X VARIANT-LINK-TIME				
	Post-build time				
Scope / Dependency	scope: local				

SWS Item	ECUC_NvM_00555 :		
Name	NvMMainFunctionPeriod {NVM_MAIN_FUNCTION_PERIOD}		
Description	The period between successive calls to the main function in seconds.		
Multiplicity	1		
Туре	EcucFloatParamDef		
Range	1E-7 INF		
Default value			
ConfigurationClass	Pre-compile time	Χ	All Variants
	Link time		
	Post-build time		
Scope / Dependency	scope: ECU		

SWS Item	ECUC_NvM_00500:
Name	NvMMultiBlockCallback {NVM_MULTI_BLOCK_CALLBACK}
Description	Entry address of the common callback routine which shall be invoked on termination of each asynchronous multi block request
Multiplicity	01
Туре	EcucFunctionNameDef



Default value				
maxLength				
minLength				
regularExpression				
ConfigurationClass	Pre-compile time	X	VARIANT-PRE-COMPILE	
	Link time	Х	VARIANT-LINK-TIME	
	Post-build time			
Scope / Dependency	scope: local			

SWS Item	ECUC_NvM_00501:	ECUC_NvM_00501:		
Name	NvMPollingMode {NVM_PO	NvMPollingMode {NVM_POLLING_MODE}		
Description	Manager and at the same useable by lower layers true: Polling mode enabled,	Preprocessor switch to enable/disable the polling mode in the NVRAM Manager and at the same time disable/enable the callback functions useable by lower layers true: Polling mode enabled, callback function usage disabled. false: Polling mode disabled, callback function usage enabled.		
Multiplicity	1	1		
Туре	EcucBooleanParamDef	EcucBooleanParamDef		
Default value				
ConfigurationClass	Pre-compile time	Х	VARIANT-PRE-COMPILE	
	Link time	Х	VARIANT-LINK-TIME	
	Post-build time			
Scope / Dependency	scope: local			

SWS Item	ECUC_NvM_00518:			
Name	NvMRepeatMirrorOperat	NvMRepeatMirrorOperations {NVM_REPEAT_MIRROR_OPERATIONS}		
Description	Defines the number of retries to let the application copy data to or from the NvM module's mirror before postponing the current job.			
Multiplicity	1	1		
Туре	EcucIntegerParamDef			
Range	0 7			
Default value	0			
ConfigurationClass	Pre-compile time	X	VARIANT-PRE-COMPILE	
	Link time	X	VARIANT-LINK-TIME	
	Post-build time			
Scope / Dependency	scope: local			

SWS Item	ECUC_NvM_00502:		
Name	NvMSetRamBlockStatusApi {NVM_SET_RAM_BLOCK_STATUS_API}		
Description	Preprocessor switch to enable the API NvM_SetRamBlockStatus. true: API NvM_SetRamBlockStatus enabled. false: API NvM_SetRamBlockStatus disabled.		
Multiplicity	1		
Type	EcucBooleanParamDef		
Default value			
ConfigurationClass	Pre-compile time	Χ	All Variants
	Link time		
	Post-build time		
Scope / Dependency	scope: local	•	

SWS Item	ECUC_NvM_00503:
Name	NvMSizeImmediateJobQueue {NVM_SIZE_IMMEDIATE_JOB_QUEUE}
	Defines the number of queue entries for the immediate priority job queue. If NVM_JOB_PRIORITIZATION is switched OFF this parameter shall be out of scope.
Multiplicity	01



Туре	EcucIntegerParamDef	EcucIntegerParamDef		
Range	1 65535			
Default value				
ConfigurationClass	Pre-compile time	X	VARIANT-PRE-COMPILE	
	Link time	Х	VARIANT-LINK-TIME	
	Post-build time			
Scope / Dependency	scope:		local	
	dependency: NVM_JOB_PRIORITIZATION			

SWS Item	ECUC_NvM_00504:		
Name	NvMSizeStandardJobQueue {NVM_SIZE_STANDARD_JOB_QUEUE}		
Description	Defines the number of queue entries for the standard job queue.		
Multiplicity	1		
Туре	EcucIntegerParamDef		
Range	1 65535		
Default value			
ConfigurationClass	Pre-compile time	Х	VARIANT-PRE-COMPILE
	Link time	Х	VARIANT-LINK-TIME
	Post-build time		
Scope / Dependency	scope: local		

SWS Item	ECUC_NvM_00505:			
Name	NvMVersionInfoApi {NVM_VERSION_INFO_API}			
Description	Pre-processor switch to enable / disable the API to read out the modules version information [NVM285], [NVM286]. true: Version info API enabled. false: Version info API disabled.			
Multiplicity	1	1		
Туре	EcucBooleanParamDef			
Default value				
ConfigurationClass	Pre-compile time	Х	All Variants	
	Link time			
	Post-build time			
Scope / Dependency	scope: local			

No Included Containers

[SWS_NvM_00028] [The following tables specify parameters that shall be definable in the module's configuration file (NvM_Cfg.h).] (SRS_BSW_00167, SRS_BSW_00381, SRS_BSW_00388, SRS_BSW_00389, SRS_BSW_00390, SRS_BSW_00391, SRS_BSW_00392, SRS_BSW_00393, SRS_BSW_00394, SRS_BSW_00395, SRS_BSW_00396, SRS_BSW_00397, SRS_BSW_00171)

10.2.4NvMBlockDescriptor

SWS Item	ECUC_NvM_00061:
Container Name	NvMBlockDescriptor
Description	Container for a management structure to configure the composition of a given NVRAM Block Management Type. Its multiplicity describes the number of configured NVRAM blocks, one block is required to be configured. The NVRAM block descriptors are condensed in the NVRAM block descriptor table.
Configuration Parameters	

SWS Item	ECUC_NvM_00476:

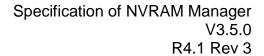


Name	NvMBlockCrcType {NVM_BLOCK_CRC_TYPE}							
Description	Defines CRC data width for the NVRAM block. Default: NVM_CRC16, i.e. CRC16 will be used if NVM_BLOCK_USE_CRC==true							
Multiplicity	01							
Туре	EcucEnumerationParamDef							
Range	NVM_CRC16	`	ault) (BLOC	CRC16 K_USE_			used	if
	NVM_CRC32	CRC32 is selected for this NVRAM block NVM BLOCK USE CRC==true.					ck if	
	NVM_CRC8		C8 is sel /I_BLOC				AM bloc	ck if
ConfigurationClass	Pre-compile time	Х	VARIAN	Γ-PRE-	COMPI	LE		
	Link time	Х	VARIAN	Γ-LINK-	TIME			
	Post-build time							
	scope: dependency: NVM_BLOCK_USE_	CRO	, NVM_C	CALC_F	RAM_B	LOCK_		ocal

SWS Item	ECUC_NvM_00554:					
Name	NvMBlockHeaderInclude	NvMBlockHeaderInclude				
Description	Defines the header file where the owner of the NVRAM block has the declarations of the permanent RAM data block, ROM data block (if configured) and the callback function prototype for each configured callback. If no permanent RAM block, ROM block or callback functions are configured then this configuration parameter shall be ignored.					
Multiplicity	01	01				
Туре	EcucStringParamDef					
Default value		Ī				
maxLength						
minLength						
regularExpression						
ConfigurationClass	Pre-compile time	Χ	VARIANT-PRE-COMPILE			
	Link time X VARIANT-LINK-TIME					
	Post-build time					
Scope / Dependency	scope: local					

SWS Item	ECUC_NvM_00477:					
Name	NvMBlockJobPriority {NVM_	NvMBlockJobPriority {NVM_BLOCK_JOB_PRIORITY}				
Description	Defines the job priority for a	NVR/	AM block (0 = Immediate priority).			
Multiplicity	1					
Type	EcucIntegerParamDef	EcucIntegerParamDef				
Range	0 255	0 255				
Default value						
ConfigurationClass	Pre-compile time	Х	VARIANT-PRE-COMPILE			
	Link time	Х	VARIANT-LINK-TIME			
	Post-build time					
Scope / Dependency	scope: local					

SWS Item	ECUC_NvM_00062:				
Name	NvMBlockManagementType {NVM_BLOCK_N	NvMBlockManagementType {NVM_BLOCK_MANAGEMENT_TYPE}			
Description	Defines the block management type for the NVRAM block.[NVM137]				
Multiplicity	1				
Туре	EcucEnumerationParamDef				
Range		NVRAM block is configured to be of dataset type.			
	NVM_BLOCK_NATIVE	NVRAM block is configured to be of native type.			





	NVM_BLOCK_REDUNDANT	ΝV	RAM block is configured to be
		of r	edundant type.
ConfigurationClass	Pre-compile time	Х	VARIANT-PRE-COMPILE
	Link time	Х	VARIANT-LINK-TIME
	Post-build time		
Scope / Dependency	scope: local		

SWS Item	ECUC_NvM_00036 :	ECUC_NvM_00036:				
Name	NvMBlockUseCrc {NVM_E	NvMBlockUseCrc {NVM_BLOCK_USE_CRC}				
Description	reserved in RAM and NV r	Defines CRC usage for the NVRAM block, i.e. memory space for CRC is reserved in RAM and NV memory. true: CRC will be used for this NVRAM block. false: CRC will not be used for this NVRAM block.				
Multiplicity	1	1				
Туре	EcucBooleanParamDef	EcucBooleanParamDef				
Default value						
ConfigurationClass	Pre-compile time	X	VARIANT-PRE-COMPILE			
	Link time	Link time X VARIANT-LINK-TIME				
	Post-build time	Post-build time				
Scope / Dependency	scope: local		•			

SWS Item	ECUC_NvM_00552 :					
Name	NvMBlockUseSetRamBlock	NvMBlockUseSetRamBlockStatus				
Description	Note: If NvMSetRamBlooparameter shall be ignored. true: calling of NvMSetRamstatus of the RAM block.	true: calling of NvMSetRamBlockStatus for this RAM block shall set the status of the RAM block. false: calling of NvMSetRamBlockStatus for this RAM block shall be				
Multiplicity	1					
Туре	EcucBooleanParamDef	EcucBooleanParamDef				
Default value						
ConfigurationClass	Pre-compile time	Х	VARIANT-PRE-COMPILE			
	Link time X VARIANT-LINK-TIME					
	Post-build time					
Scope / Dependency	scope: local					

SWS Item	ECUC_NvM_00519:	ECUC_NvM_00519:				
Name	NvMBlockUseSyncMechanis	NvMBlockUseSyncMechanism				
	{NVM_BLOCK_USE_SYNC	_MEC	CHANISM}			
Description	and callback routines for tra	Defines whether an explicit synchronization mechanism with a RAM mirror and callback routines for transferring data to and from NvM module's RAM mirror is used for NV block. true if synchronization mechanism is used, false otherwise.				
Multiplicity	1					
Туре	EcucBooleanParamDef	EcucBooleanParamDef				
Default value	false					
ConfigurationClass	Pre-compile time	Χ	VARIANT-PRE-COMPILE			
_	Link time X VARIANT-LINK-TIME					
	Post-build time					
Scope / Dependency	scope: local					

SWS Item	ECUC_NvM_00033:				
Name	NvMBlockWriteProt {NVM_BLOCK_WRITE_PROT}				
Description	Defines an initial write protection of the NV block				
	true: Initial block write protection is enabled. false: Initial block write				



	protection is disabled.					
Multiplicity	1					
Туре	EcucBooleanParamDef	EcucBooleanParamDef				
Default value						
ConfigurationClass	Pre-compile time	Х	VARIANT-PRE-COMPILE			
	Link time	Х	VARIANT-LINK-TIME			
	Post-build time					
Scope / Dependency	scope: local					

SWS Item	ECUC_NvM_00551:				
Name		NvMBswMBlockStatusInformation			
	NVM_BSWM_BLOCK_STA	TUS_	_INFORMATION}		
Description	status of the specified block.	This parameter specifies whether BswM is informed about the current status of the specified block.			
	True: Call BswM_NvM_Curr	entBl	ockMode on changes False: Don't inform		
	BswM at all				
Multiplicity	1	1			
Туре	EcucBooleanParamDef	EcucBooleanParamDef			
Default value	false				
ConfigurationClass	Pre-compile time	Χ	VARIANT-PRE-COMPILE		
	Link time X VARIANT-LINK-TIME				
	Post-build time				
Scope / Dependency	scope: local				

SWS Item	ECUC_NvM_00119:	ECUC_NvM_00119:					
Name	NvMCalcRamBlockCrc (N)	NvMCalcRamBlockCrc {NVM_CALC_RAM_BLOCK_CRC}					
Description	blocks which are configure true: CRC will be (re)calcu	Defines CRC (re)calculation for the permanent RAM block or NVRAM blocks which are configured to use explicit synchronization mechanism. true: CRC will be (re)calculated for this permanent RAM block. false: CRC will not be (re)calculated for this permanent RAM block.					
Multiplicity	01	01					
Туре	EcucBooleanParamDef	EcucBooleanParamDef					
Default value							
ConfigurationClass	Pre-compile time	X	VARIANT-PRE-COMPILE				
	Link time	Х	VARIANT-LINK-TIME				
	Post-build time						
Scope / Dependency	scope: dependency: NVM_BLOCk	C_USE	local _CRC				

SWS Item	ECUC_NvM_00116:	ECUC_NvM_00116:				
Name	NvMInitBlockCallback {NVM_INIT_BLOCK_CALLBACK}					
Description	Entry address of a block specific callback routine which shall be called if no ROM data is available for initialization of the NVRAM block. If not configured, no specific callback routine shall be called for initialization of the NVRAM block with default data.					
Multiplicity	01					
Туре	EcucFunctionNameDef					
Default value						
maxLength						
minLength						
regularExpression						
ConfigurationClass	Pre-compile time	Χ	VARIANT-PRE-COMPILE			
	Link time X VARIANT-LINK-TIME					
	Post-build time					
Scope / Dependency	scope: local					



SWS Item	ECUC_NvM_00533:					
Name	NvMMaxNumOfReadRetri	NvMMaxNumOfReadRetries {NVM_MAX_NUM_OF_READ_RETRIES}				
Description	Defines the maximum num	ber of	read retries.			
Multiplicity	1					
Type	EcucIntegerParamDef	EcucIntegerParamDef				
Range	0 7					
Default value	0					
ConfigurationClass	Pre-compile time	X	VARIANT-PRE-COMPILE			
	Link time	Х	VARIANT-LINK-TIME			
	Post-build time	Post-build time				
Scope / Dependency	scope: local					

SWS Item	ECUC_NvM_00499:	ECUC_NvM_00499:				
Name	NvMMaxNumOfWriteRetrie	NvMMaxNumOfWriteRetries {NVM_MAX_NUM_OF_WRITE_RETRIES}				
Description	Defines the maximum number of write retries for a NVRAM block with [ECUC_NvM_00061]. Regardless of configuration a consistency check (and maybe write retries) are always forced for each block which is processed by the request NvM_WriteAll and NvM_WriteBlock.					
Multiplicity	1					
Туре	EcucIntegerParamDef					
Range	07					
Default value						
ConfigurationClass	Pre-compile time	Х	VARIANT-PRE-COMPILE			
	Link time	Link time X VARIANT-LINK-TIME				
	Post-build time	Post-build time				
Scope / Dependency	scope: local					

SWS Item	ECUC_NvM_00478:							
Name	NvMNvBlockBaseNumber {NVM_NV_BLOCK_BASE_NUMBER}							
Description	Configuration parameter NVM_NVRAM_BLOCK_IDE FEE_BLOCK_NUMBER expression parameter value EA_BLOCK_NUMBER shift bits. (ref. to chapter 7.1.2.1). Calculation FargetBlockReference.[Ea/F>> NvMDatasetSelectionBits	NTIFI pected equal ed to ormul ee]Blo	ER used If by the right the right	by t nemory FEE_E by Nv	he SW- / abstrac BLOCK_I /MDatase value	-Cs a ction n NUMB etSeled	and nodu ER ction	les. or Bits =
Multiplicity	1							
Туре	EcucIntegerParamDef							
Range	1 65534							
Default value								
ConfigurationClass	Pre-compile time	Χ	VARIANT-	PRE-C	OMPILE			
	Link time	Χ	VARIANT-	LINK-T	ΠME			
	Post-build time	1						
Scope / Dependency	scope: dependency: FEE_BLOCK_I	NUME	BER, EA_B	LOCK_	_NUMBE	R	le	ocal

SWS Item	ECUC_NvM_00479:
Name	NvMNvBlockLength {NVM_NV_BLOCK_LENGTH}
Description	Defines the NV block data length in bytes. Note: The implementer can add the attribute 'withAuto' to the parameter definition which indicates that the length can be calculated by the generator automatically (e.g. by using the sizeof operator). When 'withAuto' is set to 'true' for this parameter definition the 'isAutoValue' can be set to 'true'. If 'isAutoValue' is set to 'true' the actual value will not be considered during ECU Configuration but will be (re-)calculated by the



	code generator and stored in the value attribute afterwards.				
Multiplicity	1				
Туре	EcucIntegerParamDef				
Range	1 65535				
Default value					
ConfigurationClass	Pre-compile time	Χ	VARIANT-PRE-COMPILE		
	Link time	Χ	VARIANT-LINK-TIME		
	Post-build time				
Scope / Dependency	scope: local				

SWS Item	ECUC_NvM_00480 :	ECUC_NvM_00480:					
Name	NvMNvBlockNum {NVM_NV	NvMNvBlockNum {NVM_NV_BLOCK_NUM}					
Description	Defines the number of multiple NV blocks in a contiguous area according to the given block management type. 1-255 For NVRAM blocks to be configured of block management type NVM_BLOCK_DATASET. The actual range is limited according to NVM444. 1 For NVRAM blocks to be configured of block management type NVM_BLOCK_NATIVE 2 For NVRAM blocks to be configured of block management type NVM BLOCK REDUNDANT						
Multiplicity	1	1					
Туре	EcucIntegerParamDef						
Range	1 255						
Default value							
ConfigurationClass	Pre-compile time	Χ	VARIANT-PRE-COMPILE				
	Link time X VARIANT-LINK-TIME						
	Post-build time						
Scope / Dependency	scope: dependency: NVM_BLOCK_	MAN	local AGEMENT_TYPE				

SWS Item	ECUC_NvM_00481:	ECUC_NvM_00481:				
Name	NvMNvramBlockIdentifier {N	NvMNvramBlockIdentifier {NVM_NVRAM_BLOCK_IDENTIFIER}				
Description	Implementation Type: NvM_min = 1 max = 2^(16- NVM_Reserved NVRAM block ID: NvM_GetErrorStatus 1 -> configuration ID (generation	Identification of a NVRAM block via a unique block identifier. Implementation Type: NvM_BlockIdType. min = 1 max = 2^(16- NVM_DATASET_SELECTION_BITS)-1 Reserved NVRAM block IDs: 0 -> to derive multi block request results via NvM_GetErrorStatus 1 -> redundant NVRAM block which holds the configuration ID (generation tool should check that this block is correctly configured from type,CRC and size point of view)				
Multiplicity	1	1				
Туре	EcucIntegerParamDef (Sym	bolic I	Name generated for this parameter)			
Range	1 65535					
Default value						
ConfigurationClass	Pre-compile time	Pre-compile time X VARIANT-PRE-COMPILE				
	Link time	Link time X VARIANT-LINK-TIME				
	Post-build time					
Scope / Dependency	scope: local dependency: NVM_DATASET_SELECTION_BITS					

SWS Item	ECUC_NvM_00035:
Name	NvMNvramDeviceId {NVM_NVRAM_DEVICE_ID}
Description	Defines the NVRAM device ID where the NVRAM block is located. Calculation Formula: value = TargetBlockReference.[Ea/Fee]BlockConfiguration.[Ea/Fee]DeviceIndex
Multiplicity	1
Туре	EcucIntegerParamDef



Range	0 254			
Default value				
ConfigurationClass	Pre-compile time	Χ	VARIANT-PRE-COMPILE	
	Link time	Χ	VARIANT-LINK-TIME	
	Post-build time			
Scope / Dependency	scope: loc			
	dependency: EA_DEVICE_INDEX, FEE_DEVICE_INDEX			

SWS Item	ECUC_NvM_00482:	ECUC_NvM_00482:					
Name	NvMRamBlockDataAddre	NvMRamBlockDataAddress {NVM_RAM_BLOCK_DATA_ADDRESS}					
Description	Defines the start address of the RAM block data. If this is not configured, no permanent RAM data block is available for the selected block management type.						
Multiplicity	01						
Type	EcucStringParamDef						
Default value							
maxLength							
minLength							
regularExpression							
ConfigurationClass	Pre-compile time	Х	VARIANT-PRE-COMPILE				
	Link time	Link time X VARIANT-LINK-TIME					
	Post-build time	Post-build time					
Scope / Dependency	scope: local						

SWS Item	ECUC_NvM_00521:					
Name	NvMReadRamBlockFromNv	NvMReadRamBlockFromNvCallback				
	{NVM_READ_RAM_BLOCK	{NVM_READ_RAM_BLOCK_FROM_NVM}				
Description	Entry address of a block specific callback routine which shall be called in order to let the application copy data from the NvM module's mirror to RAM block. Implementation type: Std_ReturnType E_OK: copy was successful E_NOT_OK: copy was not successful, callback routine to be called again					
Multiplicity	01					
Туре	EcucFunctionNameDef	EcucFunctionNameDef				
Default value						
maxLength						
minLength						
regularExpression						
ConfigurationClass	Pre-compile time	Pre-compile time X VARIANT-PRE-COMPILE				
	Link time X VARIANT-LINK-TIME					
	Post-build time					
Scope / Dependency	scope: local					

SWS Item	ECUC_NvM_00483:
Name	NvMResistantToChangedSw {NVM_RESISTANT_TO_CHANGED_SW}
Description	Defines whether a NVRAM block shall be treated resistant to configuration changes or not. If there is no default data available at configuration time then the application shall be responsible for providing the default initialization data. In this case the application has to use NvM_GetErrorStatus()to be able to distinguish between first initialization and corrupted data. true: NVRAM block is resistant to changed software. false: NVRAM block is not resistant to changed software.
Multiplicity	1
Type	EcucBooleanParamDef
Default value	



ConfigurationClass	Pre-compile time	X	VARIANT-PRE-COMPILE
	Link time	X	VARIANT-LINK-TIME
	Post-build time		
Scope / Dependency	scope: local		

SWS Item	ECUC_NvM_00484:				
Name	NvMRomBlockDataAddress {NVM_ROM_BLOCK_DATA_ADDRESS}				
Description	Defines the start address of the ROM block data. If not configured, no ROM block is available for the selected block management type.				
Multiplicity	01				
Туре	EcucStringParamDef				
Default value					
maxLength					
minLength					
regularExpression					
ConfigurationClass	Pre-compile time X VARIANT-PRE-COMPILE				
	Link time X VARIANT-LINK-TIME				
	Post-build time				
Scope / Dependency	scope: local				

SWS Item	ECUC_NvM_00485 :					
Name	NvMRomBlockNum {NVM_	NvMRomBlockNum {NVM_ROM_BLOCK_NUM}				
Description	Defines the number of multiple ROM blocks in a contiguous area according					
-	to the given block management type.					
	0-255 For NVRAM blocks	to be	configured of block management type			
			actual range is limited according to			
	NVM444.					
	0-1 For NVRAM blocks t	o be	configured of block management type			
	NVM_BLOCK_NATIVE					
	0-1 For NVRAM blocks t	0-1 For NVRAM blocks to be configured of block management type				
	NVM_BLOCK_REDUNDAN	NVM_BLOCK_REDUNDANT				
Multiplicity	1					
Туре	EcucIntegerParamDef	EcucIntegerParamDef				
Range	0 255					
Default value						
ConfigurationClass	Pre-compile time	Х	VARIANT-PRE-COMPILE			
Link time X VARIANT-LINK-TIME						
	Post-build time					
Scope / Dependency	scope: loc					
	dependency: NVM_BLOCK_MANAGEME					
	NVM_NV_BLOCK_NUM					

SWS Item	ECUC_NvM_00117:	ECUC_NvM_00117:			
Name	NvMSelectBlockForRead/	MVN} III	I_SELECT_BLOCK_FOR_READALL}		
Description	or not. This configuration blocks which are configur configured to use explicit true: NVRAM block shall	Defines whether a NVRAM block shall be processed during NvM_ReadAll or not. This configuration parameter has only influence on those NVRAM blocks which are configured to have a permanent RAM block or which are configured to use explicit synchronization mechanism. true: NVRAM block shall be processed by NvM_ReadAll false: NVRAM block shall not be processed by NvM_ReadAll			
Multiplicity	01	01			
Туре	EcucBooleanParamDef				
Default value					
ConfigurationClass	Pre-compile time	Pre-compile time X VARIANT-PRE-COMPILE			
	Link time	Link time X VARIANT-LINK-TIME			
	Post-build time	Post-build time			



Scope / Dependency	scope:	local
	dependency: NVM_RAM_BLOCK_DATA_ADDRESS	

SWS Item	ECUC_NvM_00549:	ECUC_NvM_00549:			
Name	NvMSelectBlockForWriteAll	NvMSelectBlockForWriteAll {NVM_SELECT_BLOCK_FOR_WRITEALL}			
Description	or not. This configuration p blocks which are configured configured to use explicit sy true: NVRAM block shall I	Defines whether a NVRAM block shall be processed during NvM_WriteAll or not. This configuration parameter has only influence on those NVRAM blocks which are configured to have a permanent RAM block or which are configured to use explicit synchronization mechanism. true: NVRAM block shall be processed by NvM_WriteAll false: NVRAM block shall not be processed by NvM WriteAll			
Multiplicity	01	01			
Туре	EcucBooleanParamDef	EcucBooleanParamDef			
Default value					
ConfigurationClass	Pre-compile time	Pre-compile time X VARIANT-PRE-COMPILE			
	Link time	Link time X VARIANT-LINK-TIME			
	Post-build time	Post-build time			
Scope / Dependency	scope: dependency: NVM_RAM_B	scope: local dependency: NVM_RAM_BLOCK_DATA_ADDRESS			

SWS Item	ECUC_NvM_00506:	ECUC_NvM_00506:		
Name	NvMSingleBlockCallback {N	NvMSingleBlockCallback {NVM_SINGLE_BLOCK_CALLBACK}		
Description	Entry address of the block specific callback routine which shall be invoked on termination of each asynchronous single block request [NVM113].			
Multiplicity	01			
Туре	EcucFunctionNameDef	EcucFunctionNameDef		
Default value				
maxLength				
minLength				
regularExpression				
ConfigurationClass	Pre-compile time X VARIANT-PRE-COMPILE			
	Link time X VARIANT-LINK-TIME			
	Post-build time			
Scope / Dependency	scope: local			

SWS Item	ECUC_NvM_00532:			
Name	NvMStaticBlockIDCheck {NVM_STATIC_BLOCK_ID_CHECK}			
Description	Defines if the Static Block ID check is enabled. false: Static Block ID check is disabled. true: Static Block ID check is enabled.			
Multiplicity	1			
Туре	EcucBooleanParamDef	EcucBooleanParamDef		
Default value	false			
ConfigurationClass	Pre-compile time	Pre-compile time X VARIANT-PRE-COMPILE		
	Link time X VARIANT-LINK-TIME			
	Post-build time			
Scope / Dependency	scope: local			

SWS Item	ECUC_NvM_00072:
Name	NvMWriteBlockOnce {NVM_WRITE_BLOCK_ONCE}
Description	Defines write protection after first write. The NVRAM manager sets the write protection bit after the NV block was written the first time. This means that some of the NV blocks in the NVRAM should never be erased nor be replaced with the default ROM data after first initialization. [NVM276]. true: Defines write protection after first write is enabled. false: Defines write protection after first write is disabled.
Multiplicity	1



Туре	EcucBooleanParamDef		
Default value			
ConfigurationClass	Pre-compile time X VARIANT-PRE-COMPILE		
	Link time	Χ	VARIANT-LINK-TIME
	Post-build time		
Scope / Dependency	scope: local		

SWS Item	ECUC_NvM_00520 :	ECUC_NvM_00520:			
Name		NvMWriteRamBlockToNvCallback			
	NVM_WRITE_RAM_BLO				
Description	order to let the application mirror.				
	Implementation type: Std_				
	E_OK: copy was succe	ssful E	_NOT_OK: copy was not successful,		
	callback routine to be calle	callback routine to be called again			
Multiplicity	01	01			
Type	EcucFunctionNameDef				
Default value					
maxLength					
minLength					
regularExpression					
ConfigurationClass	Pre-compile time	Pre-compile time X VARIANT-PRE-COMPILE			
_	Link time	Link time X VARIANT-LINK-TIME			
	Post-build time	Post-build time			
Scope / Dependency	scope: local				

SWS Item	ECUC_NvM_00534:	ECUC_NvM_00534:			
Name	NvMWriteVerification (NVI	NvMWriteVerification {NVM_WRITE_VERIFICATION}			
Description		Defines if Write Verification is enabled. false: Write verification is disabled. true: Write Verification is enabled.			
Multiplicity	1	1			
Type	EcucBooleanParamDef	EcucBooleanParamDef			
Default value	false	false			
ConfigurationClass	Pre-compile time	Pre-compile time X VARIANT-PRE-COMPILE			
	Link time X VARIANT-LINK-TIME				
	Post-build time				
Scope / Dependency	scope: local				

SWS Item	ECUC_NvM_00538 :	ECUC_NvM_00538:			
Name		NvMWriteVerificationDataSize {NVM_WRITE_VERIFICATION_DATA_SIZE}			
Description	Defines the number of byte content of a RAM Block an	Defines the number of bytes to compare in each step when comparing the content of a RAM Block and a block read back.			
Multiplicity	1	1			
Type	EcucIntegerParamDef	EcucIntegerParamDef			
Range	1 65535	1 65535			
Default value					
ConfigurationClass	Pre-compile time	X	VARIANT-PRE-COMPILE		
	Link time	Link time X VARIANT-LINK-TIME			
	Post-build time	Post-build time			
Scope / Dependency	scope: local				

Included Containers		
Container Name	Multiplicity	Scope / Dependency
NvMTargetBlockReference	1	This parameter is just a container for the parameters for EA



and F	EE

10.2.5NvMTargetBlockReference

SWS Item	ECUC_NvM_00486:
Choice container Name	NvMTargetBlockReference
Description	This parameter is just a container for the parameters for EA and FEE

Container Choices		
Container Name	Multiplicity	Scope / Dependency
NvMEaRef	01	EEPROM Abstraction
NvMFeeRef	01	Flash EEPROM Emulation

10.2.6NvMEaRef

SWS Item	ECUC_NvM_00487:
Container Name	NvMEaRef
Description	EEPROM Abstraction
Configuration Parameters	

SWS Item	ECUC_NvM_00488:			
Name	NvMNameOfEaBlock	NvMNameOfEaBlock		
Description	reference to EaBlock			
Multiplicity	1	1		
Type	Symbolic name reference to	Symbolic name reference to [EaBlockConfiguration]		
ConfigurationClass	Pre-compile time X VARIANT-PRE-COMPILE			
	Link time X VARIANT-LINK-TIME			
	Post-build time			
Scope / Dependency	scope: local			

No Included Containers

10.2.7NvMFeeRef

SWS Item	ECUC_NvM_00489:
Container Name	NvMFeeRef
Description	Flash EEPROM Emulation
Configuration Parameters	

SWS Item	ECUC_NvM_00490:		
Name	NvMNameOfFeeBlock		
Description	reference to FeeBlock		
Multiplicity	1		
Туре	Symbolic name reference to [FeeBlockConfiguration]		
ConfigurationClass	Pre-compile time X VARIANT-PRE-COMPILE		
	Link time X VARIANT-LINK-TIME		
	Post-build time		
Scope / Dependency	scope: local		



No Included Containers

10.2.8NvmDemEventParameterRefs

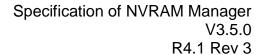
SWS Item	ECUC_NvM_00541:
Container Name	NvmDemEventParameterRefs
Description	Container for the references to DemEventParameter elements which shall be invoked using the API Dem_ReportErrorStatus API in case the corresponding error occurs. The EventId is taken from the referenced DemEventParameter's DemEventId value. The standardized errors are provided in the container and can be extended by vendor specific error references.
Configuration Parameters	

SWS Item	ECUC_NvM_00553 :	ECUC_NvM_00553:		
Name	NVM_E_HARDWARE			
Description		Reference to the DemEventParameter which shall be issued when the hardware error has occured.		
Multiplicity	01	01		
Type	Symbolic name reference to	Symbolic name reference to [DemEventParameter]		
ConfigurationClass	Pre-compile time	Pre-compile time X All Variants		
	Link time			
	Post-build time			
Scope / Dependency	scope: ECU			

SWS Item	ECUC_NvM_00542:			
Name	NVM_E_INTEGRITY_FAILED			
Description	Reference to the DemEventParameter which shall be issued when the error "API request integrity failed" has occurred.			
Multiplicity	01			
Type	Symbolic name reference to	Symbolic name reference to [DemEventParameter]		
ConfigurationClass	Pre-compile time X All Variants			
	Link time			
	Post-build time			
Scope / Dependency	scope: ECU			

SWS Item	ECUC_NvM_00546:			
Name	NVM_E_LOSS_OF_REDUNDANCY			
Description	Reference to the DemEventParameter which shall be issued when the error "loss of redundancy" has occurred.			
Multiplicity	01			
Type	Symbolic name reference to [DemEventParameter]			
ConfigurationClass	Pre-compile time X All Variants			
	Link time			
	Post-build time			
Scope / Dependency	scope: ECU			

SWS Item	ECUC_NvM_00547:
Name	NVM_E_QUEUE_OVERFLOW
	Reference to the DemEventParameter which shall be issued when the error "NVRAM Managers job queue overflow" has occurred.
Multiplicity	01
Туре	Symbolic name reference to [DemEventParameter]





ConfigurationClass	Pre-compile time	X	All Variants
	Link time		
	Post-build time		
Scope / Dependency	scope: ECU		

SWS Item	ECUC_NvM_00543:			
Name	NVM_E_REQ_FAILED			
Description	Reference to the DemEventParameter which shall be issued when the error "API request failed" has occurred.			
Multiplicity	01			
Туре	Symbolic name reference to	Symbolic name reference to [DemEventParameter]		
ConfigurationClass	Pre-compile time X All Variants			
	Link time			
	Post-build time			
Scope / Dependency	scope: ECU			

SWS Item	ECUC_NvM_00545:			
Name	NVM_E_VERIFY_FAILED			
Description	Reference to the DemEventParameter which shall be issued when the error "Write Verification failed" has occurred.			
Multiplicity	01			
Type	Symbolic name reference to	Symbolic name reference to [DemEventParameter]		
ConfigurationClass	Pre-compile time X All Variants			
	Link time			
	Post-build time			
Scope / Dependency	scope: ECU			

SWS Item	ECUC_NvM_00548:			
Name	NVM_E_WRITE_PROTECTED			
Description	Reference to the DemEventParameter which shall be issued when the error "write attempt to NVRAM block with write protection" has occurred.			
Multiplicity	01			
Type	Symbolic name reference to [DemEventParameter]			
ConfigurationClass	Pre-compile time X All Variants			
	Link time			
	Post-build time			
Scope / Dependency	scope: ECU			

SWS Item	ECUC_NvM_00544:				
Name	NVM_E_WRONG_BLOCK_	NVM_E_WRONG_BLOCK_ID			
Description		Reference to the DemEventParameter which shall be issued when the error "Static Block ID check failed" has occurred.			
Multiplicity	01	01			
Туре	Symbolic name reference to	Symbolic name reference to [DemEventParameter]			
ConfigurationClass	Pre-compile time	Pre-compile time X All Variants			
	Link time				
	Post-build time				
Scope / Dependency	scope: ECU				

No Included Containers



10.3 Common configuration options

[SWS_NvM_00030] [By use of configuration techniques, each application shall be enabled to declare the memory requirements at configuration time. This information shall be useable to assign memory areas and to generate the appropriate interfaces. Wrong memory assignments and conflicts in requirements (sufficient memory not available) shall be detected at configuration time.] ()

[SWS NvM 00034] [The NVRAM memory layout configuration shall have a unique ID. The NvM module shall have a configuration identifier that is a unique property of the memory layout configuration. The ID can be either statically assigned to the configuration or it can be calculated from the configuration properties. This should be supported by a configuration tool. The ID must be changed if the block configuration changes, i.e. if a block is added or removed, or if its size or type is changed. The ID shall be stored together with the data and shall be used in addition to the data NVRAM checksum determine the consistency of the contents. to (SRS_Mem_00135)

[SWS_NvM_00073] [The comparison between the stored configuration ID and the compiled configuration ID shall be done as the first step within the function NvM_ReadAll during startup.] ()

[SWS_NvM_00688] In case of a detected configuration ID mismatch, the behavior of the NvM module shall be defined by a configurable option. ()

[SWS_NvM_00052] [Provide information about used memory resources. The NvM module configuration shall provide information on how many resources of RAM, ROM and NVRAM are used. The configuration tool shall be responsible to provide detailed information about all reserved resources. The format of this information shall be commonly used (e.g. MAP file format). | ()

10.4 Published parameters

For details refer to the chapter 10.3 "Published Information" in SWS BSWGeneral.



11 Not applicable requirements

[SWS NvM 00744] [These requirements are not applicable to this specification.] (SRS_BSW_00344, SRS_BSW_00404, SRS_BSW_00405, SRS_BSW_00170, SRS BSW 00380, SRS BSW 00412, SRS BSW 00398, SRS BSW 00399, SRS_BSW_00400, SRS_BSW_00416, SRS_BSW_00168, SRS_BSW_00423, SRS BSW 00426, SRS BSW 00427, BSW00431, SRS BSW 00432, BSW00434, SRS BSW 00375, SRS BSW 00422, BSW00420. SRS BSW 00417, SRS BSW 00336. SRS BSW 00161. SRS BSW 00162. BSW00324, SRS BSW 00005, SRS BSW 00415, SRS BSW 00164, SRS BSW 00325, SRS BSW 00326, SRS BSW 00342, SRS BSW 00343, SRS BSW 00160, SRS BSW 00007. SRS BSW 00347. SRS_BSW_00307, SRS_BSW_00335, SRS BSW 00314, SRS BSW 00348, SRS BSW 00353, SRS_BSW_00361, SRS_BSW_00312, SRS_BSW_00302, SRS_BSW_00328, SRS_BSW_00006, SRS BSW 00304, SRS BSW 00355, SRS BSW 00378, SRS BSW 00306, SRS_BSW_00308, SRS_BSW_00309, SRS_BSW_00371, SRS_BSW_00330, SRS_BSW_00010, SRS_BSW_00321, SRS_BSW_00009, SRS_BSW_00341, SRS BSW 00334, SRS_Mem_00130)