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	Abstraction	
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1 Introduction and functional overview

This specification describes the functionality, API and configuration of the EEPROM Abstraction Layer (see Figure 1).

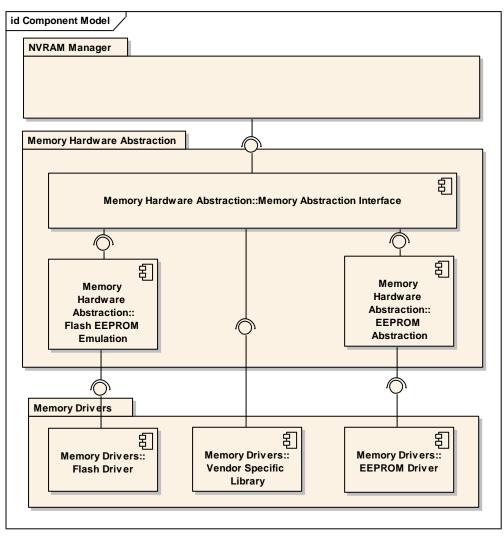


Figure 1: Module overview of memory hardware abstraction layer

The EEPROM Abstraction (EA) abstracts from the device specific addressing scheme and segmentation and provides the upper layers with a virtual addressing scheme and segmentation as well as a "virtually" unlimited number of erase cycles.



2 Acronyms and abbreviations

Acronyms and abbreviations which have a local scope and therefore are not contained in the AUTOSAR glossary must appear in a local glossary.

Abbreviation /	Description:
Acronym:	
EA	EEPROM Abstraction
EEPROM	Electrically Erasable and Programmable ROM (Read Only Memory)
FEE	Flash EEPROM Emulation
LSB	Least significant bit / byte (depending on context). Here it's bit.
Memlf	Memory Abstraction Interface
MSB	Most significant bit / byte (depending on context). Here it's bit.
NvM	NVRAM Manager
NVRAM	Non-volatile RAM (Random Access Memory)
NVRAM block	Management unit as seen by the NVRAM Manager
(Logical) block	Smallest writable / erasable unit as seen by the modules user. Consists of one or
	more virtual pages.
Virtual page	May consist of one or several physical pages to ease handling of logical blocks and
	address calculation.
Internal residue	Unused space at the end of the last virtual page if the configured block size isn't an
	integer multiple of the virtual page size (see Figure 3).
Virtual address	Consisting of 16 bit block number and 16 bit offset inside the logical block.
Physical	Address information in device specific format (depending on the underlying
address	EEPROM driver and device) that is used to access a logical block.
Dataset	Concept of the NVRAM manager: A user addressable array of blocks of the same
	size.
	E.g. could be used to provide different configuration settings for the CAN driver
	(CAN IDs, filter settings,) to an ECU which has otherwise identical application
	software (e.g. door module).
Redundant copy	Concept of the NVRAM manager: Storing the same information twice to enhance
	reliability of data storage.



3 Related documentation

3.1 Input documents

[1] List of Basic Software Modules AUTOSAR_TR_BSWModuleList.pdf

[2] Layered Software Architecture AUTOSAR_EXP_LayeredSoftwareArchitecture.pdf

[3] General Requirements on Basic Software Modules AUTOSAR_SRS_BSWGeneral.pdf

[4] General Requirements on SPAL AUTOSAR_SRS_SPALGeneral.pdf

[5] Requirements on Memory Hardware Abstraction Layer AUTOSAR_SRS_MemoryHWAbstractionLayer.doc

[6] Specification of Development Error Tracer AUTOSAR_SWS_DevelopmentErrorTracer.pdf

[7] Specification of ECU Configuration, AUTOSAR_TPS_ECUConfiguration.pdf

[8] Basic Software Module Description Template, AUTOSAR_TPS_BSWModuleDescriptionTemplate.pd

[9] General Specification of Basic Software Modules AUTOSAR_SWS_BSWGeneral.pdf

3.2 Related standards and norms

[7] Specification of NVRAM Manager AUTOSAR_SWS_NVRAMManager.doc

[8] Specification of Memory Abstraction Interface AUTOSAR_SWS_MemoryAbstractionInterface.pdf

[9] Specification of Flash EEPROM Emulation AUTOSAR_SWS_FlashEEPROMEmulation.pdf

3.3 Related specification

AUTOSAR provides a General Specification on Basic Software modules [9] (SWS BSW General), which is also valid for EEPROM Abstraction.



Thus, the specification SWS BSW General shall be considered as additional and required specification for EEPROM Abstraction.



4 Constraints and assumptions

4.1 Limitations

No limitations.

4.2 Applicability to car domains

No restrictions.



5 Dependencies to other modules

This module depends on the capabilities of the underlying EEPROM driver as well as the configuration of the NVRAM manager.

5.1 File structure

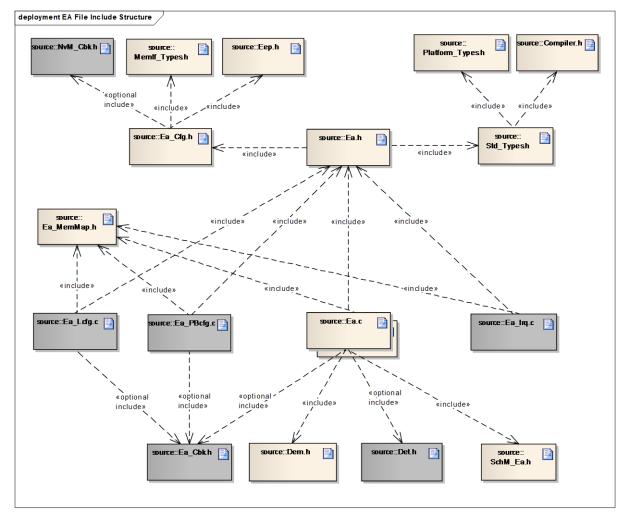
5.1.1 Code file structure

[SWS_Ea_00057] [The code file structure shall not be defined within this

specification. \downarrow ()

5.1.2 Header file structure

[SWS_Ea_00113] The Ea module shall comply with the following file include structure:



J(SRS_BSW_00346, SRS_BSW_00158, SRS_BSW_00370, SRS_BSW_00301)

Figure 2: EEPROM Abstraction Layer File Include Structure



Note: Files which are optional (depending on implementation / configuration) are shown in grey.

Note: Upper layer modules shall only include Ea.h



6 Requirements traceability

Requirement	Description	Satisfied by
-	-	SWS_Ea_00005
-	-	SWS_Ea_00020
-	-	SWS_Ea_00022
-	-	SWS_Ea_00025
-	-	SWS_Ea_00026
-	-	SWS_Ea_00034
-	-	SWS_Ea_00037
-	-	SWS_Ea_00051
-	-	SWS_Ea_00053
-	-	SWS_Ea_00054
-	-	SWS_Ea_00055
-	-	SWS_Ea_00056
-	-	SWS_Ea_00057
-	-	SWS_Ea_00066
-	-	SWS_Ea_00068
-	-	SWS_Ea_00072
-	-	SWS_Ea_00073
-	-	SWS_Ea_00074
-	-	SWS_Ea_00075
-	-	SWS_Ea_00077
-	-	SWS_Ea_00078
-	-	SWS_Ea_00079
-	-	SWS_Ea_00083
-	-	SWS_Ea_00084
-	-	SWS_Ea_00086
-	-	SWS_Ea_00087
-	-	SWS_Ea_00088
-	-	SWS_Ea_00089
-	-	SWS_Ea_00090
-	-	SWS_Ea_00091
-	-	SWS_Ea_00092
-	-	SWS_Ea_00094
-	-	SWS_Ea_00095
-	-	SWS_Ea_00097
-	-	SWS_Ea_00098
-	-	SWS_Ea_00114



-	-	SWS_Ea_00117
-	-	SWS_Ea_00135
-	-	SWS_Ea_00137
-	-	SWS_Ea_00141
-	-	SWS_Ea_00142
-	-	SWS_Ea_00143
-	-	SWS_Ea_00144
-	-	SWS_Ea_00145
-	-	SWS_Ea_00146
-	-	SWS_Ea_00150
-	-	SWS_Ea_00151
-	-	SWS_Ea_00153
-	-	SWS_Ea_00154
-	-	SWS_Ea_00155
-	-	SWS_Ea_00156
-	-	SWS_Ea_00157
-	-	SWS_Ea_00158
-	-	SWS_Ea_00159
-	-	SWS_Ea_00160
-	-	SWS_Ea_00161
-	-	SWS_Ea_00162
-	-	SWS_Ea_00163
-	-	SWS_Ea_00164
-	-	SWS_Ea_00165
-	-	SWS_Ea_00166
-	-	SWS_Ea_00167
-	-	SWS_Ea_00168
-	-	SWS_Ea_00169
-	-	SWS_Ea_00170
-	-	SWS_Ea_00171
-	-	SWS_Ea_00172
-	-	SWS_Ea_00173
-	-	SWS_Ea_00174
-	-	SWS_Ea_00175
-	-	SWS_Ea_00176
-	-	SWS_Ea_00178
BSW00324	-	SWS_Ea_00999
BSW00420	-	SWS_Ea_00999
BSW00421		SWS_Ea_00999



SRS_BSW_00172	The scheduling strategy that is built inside the Basic Software Modules shall be compatible with the strategy used in the system	SWS_Ea_00999
SRS_BSW_00168	SW components shall be tested by a function defined in a common API in the Basis-SW	SWS_Ea_00999
SRS_BSW_00164	The Implementation of interrupt service routines shall be done by the Operating System, complex drivers or modules	SWS_Ea_00999
SRS_BSW_00162	The AUTOSAR Basic Software shall provide a hardware abstraction layer	SWS_Ea_00999
SRS_BSW_00161	The AUTOSAR Basic Software shall provide a microcontroller abstraction layer which provides a standardized interface to higher software layers	SWS_Ea_00999
SRS_BSW_00160	Configuration files of AUTOSAR Basic SW module shall be readable for human beings	SWS_Ea_00999
SRS_BSW_00158	All modules of the AUTOSAR Basic Software shall strictly separate configuration from implementation	SWS_Ea_00113
SRS_BSW_00010	The memory consumption of all Basic SW Modules shall be documented for a defined configuration for all supported platforms.	SWS_Ea_00999
SRS_BSW_00009	All Basic SW Modules shall be documented according to a common standard.	SWS_Ea_00999
SRS_BSW_00007	All Basic SW Modules written in C language shall conform to the MISRA C 2004 Standard.	SWS_Ea_00999
SRS_BSW_00006	The source code of software modules above the æC Abstraction Layer (MCAL) shall not be processor and compiler dependent.	SWS_Ea_00999
SRS_BSW_00005	Modules of the æC Abstraction Layer (MCAL) may not have hard coded horizontal interfaces	SWS_Ea_00999
BSW12155	-	SWS_Ea_00999
BSW12081	-	SWS_Ea_00999
BSW12062	-	SWS_Ea_00999
BSW12060	-	SWS_Ea_00999
BSW12059	-	SWS_Ea_00999
BSW12058	-	SWS_Ea_00999
BSW00434	-	SWS_Ea_00999
BSW00431	-	SWS_Ea_00999



SRS_BSW_00300	All AUTOSAR Basic Software Modules shall be identified by an unambiguous name	SWS_Ea_00999
SRS_BSW_00301	All AUTOSAR Basic Software Modules shall only import the necessary information	SWS_Ea_00113
SRS_BSW_00302	All AUTOSAR Basic Software Modules shall only export information needed by other modules	SWS_Ea_00999
SRS_BSW_00304	-	SWS_Ea_00999
SRS_BSW_00305	Data types naming convention	SWS_Ea_00999
SRS_BSW_00306	AUTOSAR Basic Software Modules shall be compiler and platform independent	SWS_Ea_00999
SRS_BSW_00307	Global variables naming convention	SWS_Ea_00999
SRS_BSW_00308	AUTOSAR Basic Software Modules shall not define global data in their header files, but in the C file	SWS_Ea_00999
SRS_BSW_00309	All AUTOSAR Basic Software Modules shall indicate all global data with read-only purposes by explicitly assigning the const keyword	SWS_Ea_00999
SRS_BSW_00312	Shared code shall be reentrant	SWS_Ea_00999
SRS_BSW_00314	All internal driver modules shall separate the interrupt frame definition from the service routine	SWS_Ea_00999
SRS_BSW_00321	The version numbers of AUTOSAR Basic Software Modules shall be enumerated according specific rules	SWS_Ea_00999
SRS_BSW_00323	All AUTOSAR Basic Software Modules shall check passed API parameters for validity	SWS_Ea_00065, SWS_Ea_00147, SWS_Ea_00148, SWS_Ea_00149, SWS_Ea_00152
SRS_BSW_00326	-	SWS_Ea_00999
SRS_BSW_00328	All AUTOSAR Basic Software Modules shall avoid the duplication of code	SWS_Ea_00999
SRS_BSW_00330	It shall be allowed to use macros instead of functions where source code is used and runtime is critical	SWS_Ea_00999
SRS_BSW_00333	For each callback function it shall be specified if it is called from interrupt context or not	SWS_Ea_00999
SRS_BSW_00334	All Basic Software Modules shall provide an XML file that contains the meta data	SWS_Ea_00999
SRS_BSW_00336	Basic SW module shall be able to shutdown	SWS_Ea_00999
SRS_BSW_00339	Reporting of production relevant error status	SWS_Ea_00999



SRS_BSW_00341	Module documentation shall contains all needed informations	SWS_Ea_00999
SRS_BSW_00342	It shall be possible to create an AUTOSAR ECU out of modules provided as source code and modules provided as object code, even mixed	SWS_Ea_00999
SRS_BSW_00346	All AUTOSAR Basic Software Modules shall provide at least a basic set of module files	SWS_Ea_00113
SRS_BSW_00347	A Naming seperation of different instances of BSW drivers shall be in place	SWS_Ea_00999
SRS_BSW_00348	All AUTOSAR standard types and constants shall be placed and organized in a standard type header file	SWS_Ea_00999
SRS_BSW_00353	All integer type definitions of target and compiler specific scope shall be placed and organized in a single type header	SWS_Ea_00999
SRS_BSW_00355	-	SWS_Ea_00999
SRS_BSW_00361	All mappings of not standardized keywords of compiler specific scope shall be placed and organized in a compiler specific type and keyword header	SWS_Ea_00999
SRS_BSW_00370	-	SWS_Ea_00113
SRS_BSW_00371	The passing of function pointers as API parameter is forbidden for all AUTOSAR Basic Software Modules	SWS_Ea_00999
SRS_BSW_00373	The main processing function of each AUTOSAR Basic Software Module shall be named according the defined convention	SWS_Ea_00096
SRS_BSW_00378	AUTOSAR shall provide a boolean type	SWS_Ea_00999
SRS_BSW_00385	List possible error notifications	SWS_Ea_00099, SWS_Ea_00100
SRS_BSW_00401	Documentation of multiple instances of configuration parameters shall be available	SWS_Ea_00999
SRS_BSW_00406	A static status variable denoting if a BSW module is initialized shall be initialized with value 0 before any APIs of the BSW module is called	SWS_Ea_00035, SWS_Ea_00128, SWS_Ea_00129, SWS_Ea_00130, SWS_Ea_00131, SWS_Ea_00132, SWS_Ea_00134, SWS_Ea_00136
SRS_BSW_00415	Interfaces which are provided exclusively for one module shall be separated into a dedicated header file	SWS_Ea_00999
SRS_BSW_00416	The sequence of modules to be initialized shall be configurable	SWS_Ea_00999



	-	
SRS_BSW_00417	Software which is not part of the SW- C shall report error events only after the DEM is fully operational.	SWS_Ea_00999
SRS_BSW_00422	Pre-de-bouncing of error status information is done within the DEM	SWS_Ea_00999
SRS_BSW_00423	BSW modules with AUTOSAR interfaces shall be describable with the means of the SW-C Template	SWS_Ea_00999
SRS_BSW_00424	BSW module main processing functions shall not be allowed to enter a wait state	SWS_Ea_00999
SRS_BSW_00425	The BSW module description template shall provide means to model the defined trigger conditions of schedulable objects	SWS_Ea_00999
SRS_BSW_00426	BSW Modules shall ensure data consistency of data which is shared between BSW modules	SWS_Ea_00999
SRS_BSW_00427	ISR functions shall be defined and documented in the BSW module description template	SWS_Ea_00999
SRS_BSW_00428	A BSW module shall state if its main processing function(s) has to be executed in a specific order or sequence	SWS_Ea_00999
SRS_BSW_00429	BSW modules shall be only allowed to use OS objects and/or related OS services	SWS_Ea_00999
SRS_BSW_00432	Modules should have separate main processing functions for read/receive and write/transmit data path	SWS_Ea_00999
SRS_BSW_00433	Main processing functions are only allowed to be called from task bodies provided by the BSW Scheduler	SWS_Ea_00999
SRS_MemHwAb_14002	The FEE and EA modules shall allow the configuration of a required number of write cycles for each logical block	SWS_Ea_00080
SRS_MemHwAb_14006	The start address for a block erase or write operation shall always be aligned to the virtual 64K boundary	SWS_Ea_00024
SRS_MemHwAb_14007	The start address and length for reading a block shall not be limited to a certain alignment	SWS_Ea_00021
SRS_MemHwAb_14009	The FEE and EA modules shall provide a conversion between the logical linear addresses and the physical memory addresses	SWS_Ea_00007, SWS_Ea_00021, SWS_Ea_00024, SWS_Ea_00036, SWS_Ea_00063
SRS_MemHwAb_14014	The FEE and EA modules shall detect possible data inconsistencies due to aborted / interrupted write operations	SWS_Ea_00046, SWS_Ea_00047



SRS_MemHwAb_14015	The FEE and EA modules shall report possible data inconsistencies	SWS_Ea_00104
SRS_MemHwAb_14016	The FEE and EA modules shall not return inconsistent data to the caller	SWS_Ea_00104
SRS_MemHwAb_14018	The FEE module shall extend the functional scope of an internal flash driver	SWS_Ea_00999
SRS_MemHwAb_14026	The block numbers 0x0000 and 0xFFFF shall not be used	SWS_Ea_00006
SRS_MemHwAb_14032	The FEE and EA modules shall provide an erase service that operates only on complete logical blocks containing immediate data	SWS_Ea_00063, SWS_Ea_00064, SWS_Ea_00065, SWS_Ea_00093, SWS_Ea_00104
SRS_SPAL_00157	All drivers and handlers of the AUTOSAR Basic Software shall implement notification mechanisms of drivers and handlers	SWS_Ea_00999
SRS_SPAL_12057	All driver modules shall implement an interface for initialization	SWS_Ea_00017
SRS_SPAL_12063	All driver modules shall only support raw value mode	SWS_Ea_00999
SRS_SPAL_12064	All driver modules shall raise an error if the change of the operation mode leads to degradation of running operations	SWS_Ea_00999
SRS_SPAL_12067	All driver modules shall set their wake-up conditions depending on the selected operation mode	SWS_Ea_00999
SRS_SPAL_12068	The modules of the MCAL shall be initialized in a defined sequence	SWS_Ea_00999
SRS_SPAL_12069	All drivers of the SPAL that wake up from a wake-up interrupt shall report the wake-up reason	SWS_Ea_00999
SRS_SPAL_12077	All drivers shall provide a non blocking implementation	SWS_Ea_00999
SRS_SPAL_12078	The drivers shall be coded in a way that is most efficient in terms of memory and runtime resources	SWS_Ea_00999
SRS_SPAL_12092	The driver's API shall be accessed by its handler or manager	SWS_Ea_00999
SRS_SPAL_12125	All driver modules shall only initialize the configured resources	SWS_Ea_00999
SRS_SPAL_12129	The ISRs shall be responsible for resetting the interrupt flags and calling the according notification function	SWS_Ea_00999
SRS_SPAL_12163	All driver modules shall implement an interface for de-initialization	SWS_Ea_00999
SRS_SPAL_12169	All driver modules that provide different operation modes shall	SWS_Ea_00085



	provide a service for mode selection	
SRS_SPAL_12263	The implementation of all driver modules shall allow the configuration of specific module parameter types at link time	SWS_Ea_00999
SRS_SPAL_12265	Configuration data shall be kept constant	SWS_Ea_00999
SRS_SPAL_12267	Wakeup sources shall be initialized by MCAL drivers and/or the MCU driver	SWS_Ea_00999
SRS_SPAL_12461	Specific rules regarding initialization of controller registers shall apply to all driver implementations	SWS_Ea_00999
SRS_SPAL_12462	The register initialization settings shall be published	SWS_Ea_00999
SRS_SPAL_12463	The register initialization settings shall be combined and forwarded	SWS_Ea_00999

Document: General Requirements on Basic Software Modules

Requirement	Satisfied by
[SRS_BSW_00344] Reference to link-time	Not applicable
configuration	(this module does not provide any link-time
	parameters)
[SRS_BSW_00404] Reference to post build time	Not applicable
configuration	(this module does not provide post build time
	configuration)
[SRS_BSW_00405] Reference to multiple	Not applicable
configuration sets	(this module does not support multiple
	configuration sets)
[SRS_BSW_00345] Pre-compile-time	EA039, <u>ECUC_Ea_00040</u>
configuration	
[SRS_BSW_00159] Tool-based configuration	EA039, <u>ECUC Ea 00040</u>
[SRS_BSW_00167] Static configuration checking	<u>SWS Ea 00013, EA038</u>
[SRS_BSW_00171] Configurability of optional	<u>SWS_Ea_00150</u>
functionality	
[SRS_BSW_00170] Data for reconfiguration of	Not applicable
AUTOSAR SW-Components	(no reconfiguration supported)
[SRS_BSW_00380] Separate C-File for	Not applicable
configuration parameters	(no link-time or post build time configuration
	parameters)
[SRS_BSW_00381] Separate configuration	<u>SWS_Ea_00113</u>
header file for pre-compile time parameters	
[SRS_BSW_00412] Separate H-File for	Not applicable
configuration parameters	(no link-time or post build time configuration
	parameters)
[SRS_BSW_00383] List dependencies of	<u>SWS_Ea_00113</u>
configuration files	
[SRS_BSW_00384] List dependencies to other	Chapter 5
modules	
[SRS_BSW_00387] Specify the configuration	Chapter 8.6
class of callback function	
[SRS_BSW_00388] Introduce containers	Chapter 10.1



[SRS_BSW_00389] Containers shall have names	Chapter 10.1
[SRS_BSW_00390] Parameter content shall be	Chapter 8, Chapter 10.2.3, Chapter 10.2.4
unique within the module	
[SRS_BSW_00391] Parameter shall have unique	Chapter 8, Chapter 10.2.3, Chapter 10.2.4
names	
[SRS_BSW_00392] Parameters shall have a type	Chapter 8, Chapter 10.2.3, Chapter 10.2.4
[SRS_BSW_00393] Parameters shall have a	Chapter 8, Chapter 10.2.3, Chapter 10.2.4
range	
[SRS_BSW_00394] Specify the scope of the	Chapter 8, Chapter 10.2.3, Chapter 10.2.4
parameters	
[SRS_BSW_00395] List the required parameters	Chapter 8, Chapter 10.2.3, Chapter 10.2.4
(per parameter)	
[SRS_BSW_00396] Configuration classes	Chapter 8, Chapter 10.2.3, Chapter 10.2.4
[SRS_BSW_00397] Pre-compile-time parameters	Chapter 8, Chapter 10.2.3, Chapter 10.2.4
[SRS_BSW_00398] Link-time parameters	Not applicable
	(no link-time configuration parameters)
ISBS DSW 002001 Logdoble Doot build time	
[SRS_BSW_00399] Loadable Post-build time	Not applicable
parameters	(no post build time configuration parameters)
[SRS_BSW_00400] Selectable Post-build time	Not applicable
parameters	(no post build time configuration parameters)
[SRS_BSW_00402] Published information	Chapter 10.2
[SRS_BSW_00375] Notification of wake-up	Not applicable
reason	(this module does not provide wakeup
	capabilities)
[SRS_BSW_00101] Initialization interface	<u>SWS_Ea_00017</u>
[SRS_BSW_00416] Sequence of Initialization	Not applicable
	(requirement on system design, not a single
	module)
[SRS_BSW_00406] Check module initialization	EA139, SWS_Ea_00128, SWS_Ea_00129,
	<u>SWS_Ea_00130</u> , <u>SWS_Ea_00131</u> ,
	<u>SWS_Ea_00132</u> , <u>EA133</u> , <u>SWS_Ea_00134</u> ,
	<u>SWS_Ea_00135</u> , <u>SWS_Ea_00136</u>
[SRS_BSW_00168] Diagnostic Interface of SW	Not applicable
components	(this module does not provide special diagnostics
	support)
[SRS_BSW_00407] Function to read out	Chapter 8.3.9, ECUC Ea 00043
published parameters	
[SRS_BSW_00423] Usage of SW-C template to	Not applicable
describe BSW modules with AUTOSAR Interfaces	(this module does not provide an AUTOSAR
describe bow modules with A0100AR interfaces	interface)
	/
[SRS_BSW_00424] BSW main processing	Not applicable
function task allocation	(requirement on system design, not on a single
	module)
[SRS_BSW_00425] Trigger conditions for	Not applicable
schedulable objects	(requirement on the BSW module description
	template)
[SRS_BSW_00426] Exclusive areas in BSW	Not applicable
modules	(no exclusive areas defined in this module)
[SRS_BSW_00427] ISR description for BSW	Not applicable
modules	(this module does not directly implement any
	ISRs)
[SRS_BSW_00428] Execution order	Not applicable
dependencies of main processing functions	(only one main processing function in this module)
[SRS_BSW_00429] Restricted BSW OS	Not applicable
functionality access	(this module does not use any OS functionality)
[BSW00431] The BSW Scheduler module	Not applicable
implements task bodies	(requirement on the BSW scheduler)
[SRS_BSW_00432] Modules should have	Not applicable
separate main processing functions for	(only one main processing function in this module)
Separate main processing functions for	

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read/receive and write/transmit data path	
[SRS_BSW_00433] Calling of main processing	Not applicable
functions	(requirement on system design, not on a single
· · · · · · · · · · · · · · · · · · ·	module)
[BSW00434] The Schedule Module shall provide	Not applicable
an API for exclusive areas	(requirement on the –BSW scheduler, not this
	module)
ICDC DCW/ 002201 Chutdown interface	· · · · · · · · · · · · · · · · · · ·
[SRS_BSW_00336] Shutdown interface	Not applicable
	(this module does not provide shutdown
	capabilities)
[SRS_BSW_00337] Classification of errors	<u>EA139, EA140</u>
[SRS_BSW_00338] Detection and Reporting of	<u>SWS_Ea_00011, SWS_Ea_00045</u>
development errors	
[SRS_BSW_00369] Do not return development	SWS_Ea_00045
error codes via API	
[SRS_BSW_00339] Reporting of production	Not applicable
relevant error status	(no production relevant errors)
[BSW00421] Reporting of production relevant	Not applicable
error events	(no production relevant errors)
[SRS_BSW_00422] Debouncing of production	Not applicable
relevant error status	(requirement on the DEM, not this module)
[BSW00420] Production relevant error event rate	Not applicable
detection	(requirement on the DEM, not this module)
[SRS_BSW_00417] Reporting of Error Events by	Not applicable
Non-Basic Software	(requirement on non BSW modules)
[SRS_BSW_00323] API parameter checking	EA038, <u>SWS_Ea_00065</u> , <u>SWS_Ea_00147</u> ,
	<u>SWS_Ea_00148, SWS_Ea_00149,</u>
	SWS_Ea_00152
[SRS_BSW_00004] Version check	SWS_Ea_00013
[SRS_BSW_00409] Header files for production	SWS_Ea_00048
code error IDs	
[SRS_BSW_00385] List possible error	<u>SWS_Ea_00099, SWS_Ea_00100</u>
notifications	<u>000_La_00000</u> ; <u>000_La_00100</u>
[SRS_BSW_00386] Configuration for detecting an	SWS_Ea_00011, SWS_Ea_00045
	<u>SWS_Ea_00011</u> , <u>SWS_Ea_00045</u> SWS_Ea_00059
error	
[SRS_BSW_00161] Microcontroller abstraction	Not applicable
	(requirement on AUTOSAR architecture, not a
	single module)
[SRS_BSW_00162] ECU layout abstraction	Not applicable
	(requirement on AUTOSAR architecture, not a
	single module)
[BSW00324] Do not use HIS I/O Library	Not applicable
	(requirement on AUTOSAR architecture, not a
	single module)
[SRS_BSW_00005] No hard coded horizontal	Not applicable
interfaces within MCAL	(requirement on AUTOSAR architecture, not a
	single module)
[SRS_BSW_00415] User dependent include files	Not applicable
	(only one user for this module)
[SRS_BSW_00164] Implementation of interrupt	Not applicable
service routines	
	(this module does not directly implement any
	ISRs)
[SRS_BSW_00325] Runtime of interrupt service	See note in chapter 8.4 and chapter 8.6.3
routines	
[SRS_BSW_00326] Transition from ISRs to OS	Not applicable
tasks	(requirement on implementation, not on
	specification)
	specification)
[SRS_BSW_00342] Usage of source code and	Not applicable

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	single module)
[SRS_BSW_00343] Specification and	ECUC_Ea_00070
configuration of time	
[SRS_BSW_00160] Human-readable	Not applicable
configuration data	(requirement on documentation, not on
	specification)
[SRS_BSW_00007] HIS MISRA C	Not applicable
	(requirement on implementation, not on
	specification)
ICRC RCW 002001 Medule naming convention	
[SRS_BSW_00300] Module naming convention	Not applicable
	(requirement on implementation, not on
	specification)
[SRS_BSW_00413] Accessing instances of BSW	Requirement can not be implemented in R2.0
modules	timeframe.
[SRS_BSW_00347] Naming separation of	Not applicable
different instances of BSW drivers	(requirement on the implementation, not on the
	specification)
[SRS_BSW_00305] Self-defined data types	Not applicable
naming convention	(this module does not define any data types)
[SRS_BSW_00307] Global variables naming	Not applicable
convention	(requirement on the implementation, not on the
Convention	specification)
[SRS_BSW_00310] API naming convention	Chapter 8.3
[SRS_BSW_00373] Main processing function	Chapter 8.5.1 (<u>SWS_Ea_00096</u>)
naming convention	EA 400 EA 40
[SRS_BSW_00327] Error values naming	<u>EA139, EA140</u>
convention	
[SRS_BSW_00335] Status values naming	Chapter 8.1
convention	
[SRS_BSW_00350] Development error detection	<u>SWS_Ea_00011, SWS_Ea_00059</u> , EA039
keyword	
[SRS_BSW_00408] Configuration parameter	Chapter 10.1
naming convention	
[SRS_BSW_00410] Compiler switches shall have	Chapter 10.1
defined values	
[SRS_BSW_00411] Get version info keyword	Chapter 10.2.3
[SRS_BSW_00346] Basic set of module files	SWS_Ea_00113
[SRS_BSW_00158] Separation of configuration	<u>SWS_Ea_00113</u>
from implementation	
[SRS_BSW_00314] Separation of interrupt frames	Not applicable
and service routines	(this module does not directly implement any
	ISRs)
[SRS_BSW_00370] Separation of callback	SWS_Ea_00113, Chapter 8.4
interface from API	
[SRS_BSW_00348] Standard type header	Not applicable
	(requirement on the standard header file)
[SRS_BSW_00353] Platform specific type header	Not applicable
	(requirement on the platform specific header file)
[SRS_BSW_00361] Compiler specific language	Not applicable
extension header	(requirement on the compiler specific header file)
[SRS_BSW_00301] Limit imported information	SWS_Ea_00113
[SRS_BSW_00302] Limit exported information	Not applicable
	(requirement on the implementation, not on the
	specification)
[SRS_BSW_00328] Avoid duplication of code	Not applicable
	(requirement on the implementation, not on the
	specification)
[SRS_BSW_00312] Shared code shall be	Not applicable
reentrant	(requirement on the implementation, not on the



	specification)
[SRS_BSW_00006] Platform independency	Not applicable
	(this is a module of the microcontroller abstraction
	layer)
[SRS_BSW_00357] Standard API return type	Chapter 8.3.3, Chapter 8.3.4. Chapter 8.3.8,
	Chapter 8.3.10
ISDS BSW 002771 Medule encoifie ADI return	Chapter 8.3.6, Chapter 8.3.7
[SRS_BSW_00377] Module specific API return	
types	Natanaka
[SRS_BSW_00304] AUTOSAR integer data types	Not applicable
	(requirement on implementation, not for
	specification)
[SRS_BSW_00355] Do not redefine AUTOSAR	Not applicable
integer data types	(requirement on implementation, not for
	specification)
[SRS_BSW_00378] AUTOSAR boolean type	Not applicable
	(requirement on implementation, not for
	specification)
[SRS_BSW_00306] Avoid direct use of compiler	Not applicable
and platform specific keywords	(requirement on implementation, not for
	specification)
[SRS_BSW_00308] Definition of global data	Not applicable
	(requirement on implementation, not for
	specification)
[SRS_BSW_00309] Global data with read-only	Not applicable
constraint	(requirement on implementation, not for
	specification)
[SRS_BSW_00371] Do not pass function pointers	Not applicable
via API	(no function pointers in this specification)
[SRS_BSW_00358] Return type of init() functions	Chapter 8.3.1
[SRS_BSW_00414] Parameter of init function	Chapter 8.3.1
[SRS_BSW_00376] Return type and parameters	Chapter 8.5.1
of main processing functions	Chapter 6.5.1
	Chapter 9.4
[SRS_BSW_00359] Return type of callback	Chapter 8.4
functions	Chapter 0.4
[SRS_BSW_00360] Parameters of callback	Chapter 8.4
[SRS_BSW_00329] Avoidance of generic	Chapter 8.3
	(explicit interfaces defined)
[SRS_BSW_00330] Usage of macros / inline	Not applicable
functions instead of functions	(requirement on implementation, not for
	specification)
[SRS_BSW_00331] Separation of error and status	<u>SWS_Ea_00045</u> , <u>EA139</u> , <u>EA140</u>
values	
[SRS_BSW_00009] Module User Documentation	Not applicable
	(requirement on documentation, not on
	specification)
[SRS_BSW_00401] Documentation of multiple	Not applicable
instances of configuration parameters	(all configuration parameters are single instance
	only)
[SRS_BSW_00172] Compatibility and	Not applicable
documentation of scheduling strategy	(no internal scheduling policy)
[SRS_BSW_00010] Memory resource	Not applicable
documentation	(requirement on documentation, not on
	specification)
[SRS_BSW_00333] Documentation of callback	Not applicable
function context	(requirement on documentation, not for
	specification)
[SPS_PS]M 00274] Madula yandar idantifiastian	
[SRS_BSW_00374] Module vendor identification	ECUC_Ea_00043
[SRS_BSW_00379] Module identification	ECUC_Ea_00043



[SRS_BSW_00003] Version identification	ECUC_Ea_00043
[SRS_BSW_00318] Format of module version	ECUC_Ea_00043
numbers	
[SRS_BSW_00321] Enumeration of module	Not applicable
version numbers	(requirement on implementation, not for
	specification)
[SRS_BSW_00341] Microcontroller compatibility	Not applicable
documentation	(requirement on documentation, not on
	specification)
[SRS_BSW_00334] Provision of XML file	Not applicable
	(requirement on documentation, not on
	specification)

Document: General Requirements on SPAL

Requirement	Satisfied by
[SRS_SPAL_12263] Object code compatible	Not applicable
configuration concept	(this module does not provide any post-build
	parameters)
[SRS_SPAL_12056] Configuration of notification	Chapter 8.6.3
mechanisms	
[SRS_SPAL_12267] Configuration of wake-up	Not applicable
sources	(this module does not provide any wakeup
	capabilities)
[SRS_SPAL_12057] Driver module initialization	<u>SWS_Ea_00017</u>
[SRS_SPAL_12125] Initialization of hardware	Not applicable
resources	(this module has no direct hardware access)
[SRS_SPAL_12163] Driver module de-	Not applicable
initialization	(this module does not provide any shutdown
	capabilities)
[BSW12058] Individual initialization of overall	Not applicable
registers	(this module has no direct hardware access)
[BSW12059] General initialization of overall	Not applicable
registers	(this module has no direct hardware access)
[BSW12060] Responsibility for initialization of	Not applicable
one-time writable registers	(this module has no direct hardware access)
[SRS_SPAL_12461] Responsibility for register	Not applicable
initialization	(this module has no direct hardware access)
[SRS_SPAL_12462] Provide settings for register	Not applicable
initialization	(this module has no direct hardware access)
[SRS_SPAL_12463] Combine and forward	Not applicable
settings for register initialization	(this module has no direct hardware access)
[BSW12062] Selection of static configuration sets	Not applicable
	(this module does not have configuration data)
[SRS_SPAL_12068] MCAL initialization sequence	Not applicable
· ·	(this module belongs to the ECU abstraction
	layer)
[SRS_SPAL_12069] Wake-up notification of ECU	Not applicable
State Manager	(this module does not provide any wakeup
-	capabilities)
[SRS_SPAL_00157] Notification mechanisms of	Not applicable
drivers and handlers	(this module does not provide any notification
	mechanisms)
[BSW12155] Prototypes of callback functions	Not applicable
	(this module does not implement any callback
	routines)
[SRS_SPAL_12169] Control of operation mode	<u>SWS_Ea_00085</u>
[SRS_SPAL_12063] Raw value mode	Not applicable
,	(this module does not handle or mishandle any
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	data)
[SRS_SPAL_12075] Use of application buffers	Chapters 8.3.3, and 8.3.4
[SRS_SPAL_12129] Resetting of interrupt flags	Not applicable
	(this module does not directly implement any
	ISRs)
[SRS_SPAL_12064] Change of operation mode	Not applicable
during running operation	(this module has no internal operation mode)
[SRS_SPAL_12448] Behavior after development	Chapter 7.2
error detection	
[SRS_SPAL_12067] Setting of wake-up	Not applicable
conditions	(this module does not provide any wakeup
	capabilities)
[SRS_SPAL_12077] Non-blocking implementation	Not applicable
	(this module does not implement any schedulable
	services)
[SRS_SPAL_12078] Runtime and memory	Not applicable
efficiency	(requirement on implementation, not on
	specification)
[SRS_SPAL_12092] Access to drivers	Not applicable
	(this module is the EEPROM driver's "manager")
[SRS_SPAL_12265] Configuration data shall be	Not applicable
kept constant	(this module does not have configuration data)
[SRS_SPAL_12264] Specification of configuration	EA039, <u>ECUC_Ea_00040</u> , <u>ECUC_Ea_00043</u>
items	
[BSW12081] Use HIS requirements as input	Not applicable
	(no corresponding HIS requirements available)

Document: Requirements on Memory Hardware Abstraction Layer

Requirement	Satisfied by
SRS_MemHwAb_14001 Configuration of address	EA004, EA039
alignment	
SRS_MemHwAb_14002 Configuration of number	SWS_Ea_00079_Conf, SWS_Ea_00080,
of required write cycles	ECUC_Ea_00040
SRS_MemHwAb_14003 Configuration of	ECUC_Ea_00070
maximum blocking time	
SRS_MemHwAb_14004 Configuration of	ECUC_Ea_00040, SWS_Ea_00131_Conf
"immediate" data blocks	
SRS_MemHwAb_14026 Don't use certain block	<u>SWS_Ea_00006</u>
numbers	
SRS_MemHwAb_14027 Publish overhead for	ECUC_Ea_00043, ECUC_Ea_00126
internal management data per block	
SRS_MemHwAb_14005 Virtual linear address	EA003
space and segmentation	
SRS_MemHwAb_14006 Alignment of block erase	EA004, <u>SWS_Ea_00024</u>
/ write addresses	
SRS_MemHwAb_14007 Alignment of block read	Note below SWS_Ea_00021
addresses	
SRS_MemHwAb_14008 Checking block read	EA038
addresses	
SRS_MemHwAb_14009 Conversion of logical to	<u>SWS_Ea_00007</u> , <u>SWS_Ea_00021</u> ,
physical addresses	<u>SWS_Ea_00024</u> , <u>SWS_Ea_00036</u> ,
	<u>SWS_Ea_00063</u>
SRS_MemHwAb_14010 Block-wise write service	Chapter 8.3.4
SRS_MemHwAb_14029 Block-wise read service	Chapter 8.3.3
SRS_MemHwAb_14031 Service to cancel an	Chapter 8.3.5
ongoing asynchronous operation	
SRS_MemHwAb_14028 Service to invalidate a	Chapter 8.3.8



memory block	
SRS_MemHwAb_14012 Spreading of write	SWS_Ea_00079, SWS_Ea_00080
access	
SRS_MemHwAb_14013 Writing of "immediate"	7.1.4
data must not be delayed	
SRS_MemHwAb_14032 Block-wise erase service	<u>SWS_Ea_00093</u> , <u>SWS_Ea_00063</u> ,
for immediate data	<u>SWS_Ea_00064</u> , <u>SWS_Ea_00065</u>
SRS_MemHwAb_14014 Detection of data	<u>SWS_Ea_00104</u> , <u>SWS_Ea_00046</u> ,
inconsistencies	<u>SWS Ea 00047</u>
SRS_MemHwAb_14015 Reporting of data	<u>SWS Ea 00104</u>
inconsistencies	
SRS_MemHwAb_14016 Don't return inconsistent	<u>SWS_Ea_00104</u>
data to the caller	
SRS_MemHwAb_14017 Scope of EEPROM	Chapter 1
Abstraction Layer	
SRS_MemHwAb_14018 Scope of Flash	Not applicable
EEPROM Emulation	(this is the EA modules specification)



7 Functional specification

7.1 General behavior

[SWS_Ea_00137] Gamma The EEPROM Abstraction (EA) shall only accept one job at a time, i.e. the module shall not provide a queue for pending jobs (that's the job of the NVRAM Manager). ()

Note: Since the NvM is the only caller for this module and in order to keep this module reasonably small, the modules functions shall not check, whether the module is currently busy or not. It is the responsibility of the NvM to serialize the pending jobs and only start a new job after the previous one has been finished or canceled.

7.1.1 Addressing scheme and segmentation

The EEPROM Abstraction (EA) provides upper layers with a 32bit virtual linear address space and uniform segmentation scheme. This virtual 32bit addresses consists of

- a 16bit block number allowing a (theoretical) number of 65536 logical blocks
- a 16bit block offset allowing a (theoretical) block size of 64Kbyte per block

The 16bit block number represents a configurable (virtual) paging mechanism. The values for this address alignment can be derived from that of the underlying EEPROM driver and device. This virtual paging is configurable via the parameter EA_VIRTUAL_PAGE_SIZE.

[SWS_Ea_00075] [[] The configuration of the Ea module shall be such that the virtual page size (defined in EA_VIRTUAL_PAGE_SIZE) is an integer multiple of the physical page size, i.e. it is not allowed to configure a smaller virtual page than the actual physical page size.]()

Example:

The size of a virtual page is configured to be eight bytes, thus the address alignment is eight bytes. The logical block with block number 1 is placed at physical address x. The logical block with the block number 2 then would be placed at x+8, block number 3 would be placed at x+16.

Note: This specification requirement allows the physical start address of a logical block to be calculated rather than making a lookup table necessary for the address mapping.

[SWS_Ea_00005] [Each configured logical block shall take up an integer multiple of the configured virtual page size (see also Chapter 10.2.3, configuration parameter EA VIRTUAL PAGE SIZE). J()



Example: If the virtual page size is configured to be eight bytes, logical blocks can be of size 8, 16, 24, 32, ... bytes but not e.g. 10, 20, 50, ... bytes.

[SWS_Ea_00068] [Logical blocks must not overlap each other and must not be

contained within one another. \downarrow ()

Example: The address alignment / virtual paging is configured to be eight bytes by setting the parameter EA_VIRTUAL_PAGE_SIZE accordingly. The logical block number 1 is configured to have a size of 32 bytes (see Figure 3). This logical block would use exactly 4 virtual pages. The next logical block thus would get the block number 5, since block numbers 2, 3 and 4 are "blocked" by the first logical block. This second block is configured to have a size of 100 bytes, taking up 13 virtual pages and leaving 4 bytes of the last page unused. The next available logical block number thus would be 17.

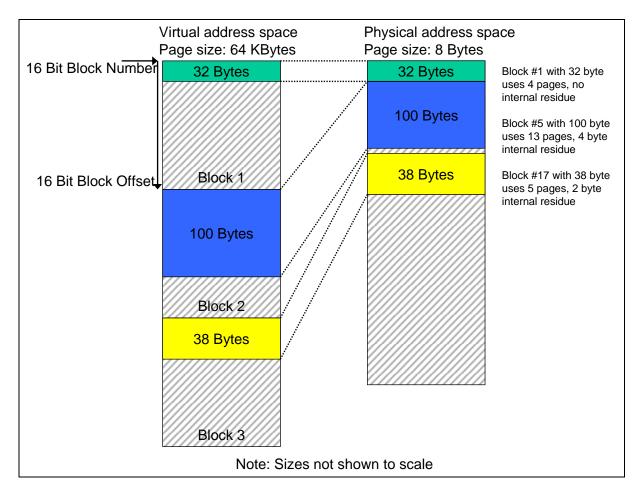


Figure 3: Virtual vs. physical memory layout

[SWS_Ea_00006] [The block numbers 0x0000 and 0xFFFF shall not be configurable for a logical block (see chapter 10.2.3, EaBlockNumber for details). J(SRS_MemHwAb_14026)



7.1.2 Address calculation

[SWS_Ea_00007] $\[Gamma]$ Depending on the implementation of the EA module and the exact address format used, the functions of the EA module shall combine the 16bit block number and 16bit block offset to derive the physical EEPROM address needed for the underlying EEPROM driver. $\[(SRS_MemHwAb_14009)\]$

Note: The exact address format needed by the underlying EEPROM driver and therefore the mechanism how to derive the physical EEPROM address from the given 16bit block number and 16bit block offset depends on the EEPROM device and the implementation of the EEPROM device driver and can therefore not be specified in this document.

[SWS_Ea_00066] [Only those bits of the 16bit block number, that do not denote a specific dataset or redundant copy shall be used for address calculation. |()

Note: Since this information is needed by the NVRAM manager, the number of bits to encode this can be configured for the NVRAM manager with the parameter NVM DATASET SELECTION BITS.

Example: Dataset information is configured to be encoded in the four LSB's of the 16bit block number (allowing for a maximum of 16 datasets per NVRAM block and a total of 4094 NVRAM blocks). An implementer decides to store all datasets of a logical block directly adjacent and using the length of the block and a pointer to access each dataset. To calculate the start address of the block (the address of the first dataset) she/he uses only the 12 MSB's, to access a specific dataset she/he adds the size of the block multiplied by the dataset index (the four MSB's) to this start address (Figure 4).



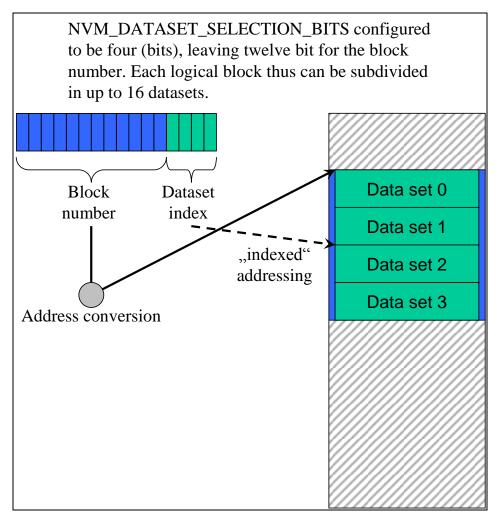


Figure 4: Block number and dataset index

7.1.3 Limitation of erase / write cycles

[SWS_Ea_00079] [The configuration of the Ea module shall define the expected number of erase/write cycles for each logical block in the configuration parameter EaNumberOfWriteCycles.]()

[SWS_Ea_00080] If the underlying EEPROM device or device driver does not provide at least the configured number of erase/write cycles per physical memory cell (given in the parameter EepAllowedWriteCycles), the EA module shall provide mechanisms to spread the erase/ write access such that the physical device is not overstressed. This shall also apply to all management data used internally by the EA module. (SRS_MemHwAb_14002)



Example: The logical block number 1 is configured for an expected 500.000 write cycles, the underlying EEPROM device and device driver are only specified for 100.000 erase cycles. In this case the EA module has to provide (at least) five separate memory areas and alternate the access between those areas internally, so that each physical memory location is only erased for a maximum of the specified 100.000 cycles.

7.1.4 Handling of "immediate" data

Blocks, containing immediate data, have to be written instantaneously, i.e. such blocks shall be writable without the need, to first erase the corresponding memory area (e.g. by using pre-erased memory). An ongoing lower priority read / erase / write or compare job shall be canceled by the NVRAM manager before immediate data is written.

Note: A running operation on the hardware (e.g. writing one page or erasing one sector) can usually not be aborted once it has been started. The maximum time of the longest hardware operation thus has to be accepted as delay even for immediate data.

Example: Three blocks with 10 bytes each have been configured for immediate data. The EA module / configuration tool reserves these 30 bytes (plus the implementation specific overhead per block / page if needed) for use by this immediate data only. That is this memory area shall not be used for storage of other data blocks.

Now, the NVRAM manager has requested the EA module to write a data block of 100 bytes. While this block is being written a situation occurs that one (or several) of the immediate data blocks need to be written. Therefore the NVRAM manager cancels the ongoing write request and subsequently issues the write request for the (first) block containing immediate data. The cancelation of the ongoing write request is performed synchronously by the EA module and the underlying EEPROM driver that is the write request for the immediate data can be started without any further delay. However, before the first bytes of immediate data can be written, the EA module respectively the underlying EEPROM driver have to wait for the end of an ongoing hardware access from the previous write request (e.g. writing of a page, erasing of a sector, transfer via SPI, ...).

7.1.5 Managing block consistency information

[SWS_Ea_00046] $\[Gamma]$ The Ea module shall manage for each block the information, whether this block is "correct" from the point of view of the EA module or not. This consistency information shall only concern the internal handling of the block, not the block's contents. $\[(SRS_MemHwAb_14014)]$



[SWS_Ea_00047] Γ When a block write operation is started the EA module shall mark the corresponding block as inconsistent¹. Upon the successful end of the block write operation, the EA module shall mark the block as consistent (again). (SRS_MemHwAb_14014)

Note: This internal management information should not be mixed up with the validity information of a block which can be manipulated by using the Ea_InvalidateBlock service, i.e. the EA module shall be able to distinguish between an inconsistent block and a block that has been deliberately invalidated by the upper layer.

7.2 Error classification

The Ea module shall detect the following errors and exceptions depending on its configuration (development/production):

Type or error	Relevance	Related error code	Value [hex]
API service called while module is not (yet) initialized	Development	EA_E_UNINIT	0x01
API service called with invalid block number	Development	EA_E_INVALID_BLOCK_NO	0x02
API service called with invalid block offset	Development	EA_E_INVALID_BLOCK_OFS	0x03
API service called with invalid pointer argument	Development	EA_E_INVALID_DATA_POINTER	0x04
API service called with invalid block length information	Development	EA_E_INVALID_BLOCK_LEN	0x05
API service called while module is busy	Development	EA_E_BUSY	0x06
API service called while module is busy doing internal management operations	Development	EA_E_BUSY_INTERNAL	0x07
Ea_Cancel called while no job was pending	Development	EA_E_INVALID_CANCEL	0x08

¹ This does not necessarily mean a write operation on the physical device. If there are other means to detect the consistency of a logical block, changing the management information stored with the block shall be avoided. ^{33 of 68} Document ID 287: AUTOSAR_SWS_EEPROMAbstraction



7.3 Debugging support

[SWS_Ea_00155] [¬] The module's job result, the status and the variables used for job control (for externally requested jobs as well as for internal management operations) shall be made globally accessible. J()



8 API specification

8.1 Imported Types

[SWS_Ea_00083]

Γ

Module	Imported Type
Eep	Eep_AddressType
	Eep_LengthType
Memlf	MemIf_JobResultType
	MemIf_ModeType
	MemIf_StatusType
Std_Types	Std_ReturnType
	Std_VersionInfoType
]()	

[SWS_Ea_00117] The types mentioned in <u>SWS_Ea_00083</u> shall not be changed or extended for a specific EA module or hardware platform. ()

8.2 Type definitions

This module does not define any module specific types.

8.3 Function definitions

8.3.1 Ea_Init

[SWS_Ea_00084]

Service name: Ea_Init Syntax: void Ea_Init(void Service ID[hex]: 0x00 Sync/Async: Synchronous Reentrancy: Non Reentrant Parameters (in): None Parameters None (inout): Parameters (out): None Return value: None Description: Initializes the EEPROM abstraction module.

()∟



[SWS_Ea_00017] [The function Ea_Init shall shall set the module state from MEMIF_UNINIT to MEMIF_BUSY_INTERNAL once it starts the module's initialization.](SRS_SPAL_12057)

[SWS_Ea_00128] [If initialization is finished within Ea_Init, the function Ea_Init shall set the module state from MEMIF_BUSY_INTERNAL to MEMIF_IDLE once initialization has been successfully finished.](SRS_BSW_00406)

Note: The Ea module's environment shall not call the function Ea_Init during a running operation of the EA module.

8.3.2 Ea_SetMode

[SWS_Ea_00085]

Γ

Service name:	Ea_SetMode		
Syntax:	void Ea_SetMode(
	MemIf_ModeType Mode		
)		
Service ID[hex]:	0x01		
Sync/Async:	Synchronous		
Reentrancy:	Non Reentrant		
Parameters (in):	Mode Desired mode for the underlying EEPROM driver		
Parameters	None		
(inout):			
Parameters (out):	None		
Return value:	None		
Description:	Sets the mode.		

」(SRS_SPAL_12169)

Example: During normal operation of an ECU the EA module and underlying device driver shall use as few (runtime) resources as possible, therefore the EEPROM driver is switched to "slow" mode. During startup and especially during shutdown it might be desirable to read / write the NV memory blocks as fast as possible, therefore the EA module and the underlying device driver could be switched into "fast" mode.

[SWS_Ea_00020] [If the current module state is MEMIF_IDLE and if supported by the underlying hardware and device driver, the function Ea_SetMode shall call the function "Eep_SetMode" of the underlying EEPROM driver with the given "Mode" parameter. J()

[SWS_Ea_00150] [The function Ea_SetMode shall be enabled / disabled via the pre-compile time parameter EaSetModeSupported such that the function is completely removed from the code if it is disabled. ()



[SWS_Ea_00129] 「 If development error detection is enabled for the module: the function Ea_SetMode shall check if the module state is MEMIF_UNINIT. If this is the case, the function Ea_SetMode shall raise the development error EA_E_UNINIT and return to the caller without executing the mode switch. J(SRS_BSW_00406)

[SWS_Ea_00165] [If development error detection is enabled for the module: the function Ea_SetMode shall check if the module state is MEMIF_BUSY. If this is the case, the function Ea_SetMode shall raise the development error EA_E_BUSY and return to the caller without executing the mode switch.]()

[SWS_Ea_00166] [If development error detection is enabled for the module: the function Ea_SetMode shall check if the module state is MEMIF_BUSY_INTERNAL. If this is the case, the function Ea_SetMode shall raise the development error EA_E_BUSY_INTERNAL and return to the caller without executing the mode switch.

8.3.3 Ea_Read

[SWS_Ea_00086]

a	I	
Service name:	Ea_Read	
Syntax:	Std_ReturnType Ea_Read(
	uint16 B	lockNumber,
	uint16 B	lockOffset,
	uint8* D	ataBufferPtr,
	uint16 L	ength
)	
Service ID[hex]:	0x02	
Sync/Async:	Asynchronous	
Reentrancy:	Non Reentrant	
		Number of logical block, also denoting start address of that block in EEPROM.
Parameters (in):	BlockOffset	Read address offset inside the block
	Length	Number of bytes to read
Parameters (inout):	None	
Parameters (out):	DataBufferPtr	Pointer to data buffer
Return value:		E_OK: The requested job has been accepted by the module. E_NOT_OK: The requested job has not been accepted by the EA module.
Description:	Reads Length by DataBufferPtr.	rtes of block Blocknumber at offset BlockOffset into the buffer

ן()

[SWS_Ea_00021] 「 The function Ea_Read shall take the block number and offset and calculate the corresponding memory read address. 」(SRS_MemHwAb_14007, SRS_MemHwAb_14009)



Note: The address offset and length parameter can take any value within the given types range, this allows reading of an arbitrary number of bytes from an arbitrary address inside a logical block.

[SWS_Ea_00072] [[] The EA module shall execute the read operation asynchronously within the EA module's main function.]()

[SWS_Ea_00022] Γ If the current module status is MEMIF_IDLE or if the current module status is MEMIF_BUSY INTERNAL and the internal management operation can be suspended or aborted, the function Ea_Read shall accept the read request, copy the given / computed parameters to module internal variables, initiate a read job, set the EA module status to MEMIF_BUSY, set the job result to MEMIF JOB PENDING and return with E OK. J()

EA179: [If the current module status is MEMIF_UNINIT or MEMIF_BUSY or MEMIF_BUSY_INTERNAL and the internal management operation can't be suspended or aborted, the function Ea_Read shall reject the job request and return with E_NOT_OK.]()

[SWS_Ea_00130] [If development error detection for the module EA is enabled: the function Ea_Read shall check if the module state is MEMIF_UNINIT. If this is the case, the function Ea_Read shall reject the read request, raise the development error EA_E_UNINIT and return with E_NOT_OK.](SRS_BSW_00406)

[SWS_Ea_00167] [If development error detection is enabled for the module: the function Ea_Read shall check if the module state is MEMIF_BUSY. If this is the case, the function Ea_Read shall reject the read request, raise the development error EA E BUSY and return with E NOT OK.]()

EA180: If development error detection is enabled for the module: if the current module status is MEMIF_BUSY_INTERNAL and if it is not possible to suspend or abort the internal management operation (because of data consistency / module implementation / hardware restrictions), the function Ea_Read shall reject the read request, raise the development error $EA_E_BUSY_INTERNAL$ and return with $E_NOT_OK.$] ()

[SWS_Ea_00147] [If development error detection is enabled for the module: the function Ea_Read shall check whether the given block number is valid (i.e. inside the configured range). If this is not the case, the function Ea_Read shall reject the read request, raise the development error EA_E_INVALID_BLOCK_NO and return E_NOT_OK. J(SRS_BSW_00323)



[SWS_Ea_00168] $\[\]$ If development error detection is enabled for the module: the function Ea_Read shall check that the given block offset is valid (i.e. that it is less than the block length configured for this block). If this is not the case, the function Ea_Read shall reject the read request, raise the development error EA_E_INVALID_BLOCK_OFS and return with E_NOT_OK. J()

[SWS_Ea_00169] $\[\]$ If development error detection is enabled for the module: the function Ea_Read shall check that the given length information is valid, i.e. that the requested length information plus the block offset do not exceed the block end address (block start address plus configured block length). If this is not the case, the function Ea_Read shall reject the read request, raise the development error EA_E_INVALID_BLOCK_LEN and return with E_NOT_OK. J()

[SWS_Ea_00170] [If development error detection is enabled for the module: the function Ea_Read shall check that the given data pointer is valid (i.e. that it is not NULL). If this is not the case, the function Ea_Read shall reject the read request, raise the development error EA_E_INVALID_DATA_PTR and return with E_NOT_OK.

[SWS_Ea_00158] [If a read request is rejected by the function Ea_Read, i.e. requirements <u>SWS Ea 00130</u>, <u>SWS Ea 00147</u>, <u>SWS Ea 00167</u>, <u>SWS Ea 00168</u>, <u>SWS_Ea_00169</u>, <u>SWS_Ea_00170</u>, <u>EA179</u> or <u>EA180</u> apply, the function Ea_Read shall not change the current module status or job result. J()

8.3.4 Ea_Write

[SWS_Ea_00087]

Service name:	Ea_Write	
Syntax:	Std ReturnType Ea Write(
	uint16 E	BlockNumber,
	const ui	nt8* DataBufferPtr
)	
Service ID[hex]:	0x03	
Sync/Async:	Asynchronous	
Reentrancy:	Non Reentrant	
Parameters (in):	BlockNumber	Number of logical block, also denoting start address of that block in EEPROM.
	DataBufferPtr	Pointer to data buffer
Parameters	None	
(inout):		
Parameters (out):	None	
	Std_ReturnType	E_OK: The requested job has been accepted by the module.
Return value:		E_NOT_OK: The requested job has not been accepted by the EA module.
Description:	Writes the conte	ents of the DataBufferPtr to the block BlockNumber.



]()

[SWS_Ea_00024] 「 The function Ea_Write shall take the block number and calculate the corresponding memory write address. The block offset shall be fixed to zero for this address calculation. J(SRS_MemHwAb_14006, SRS_MemHwAb_14009)

[SWS_Ea_00151] The function Ea_Write shall set the length parameter for the write job to the length configured for this logical block. ()

[SWS_Ea_00025] [If the current module status is MEMIF_IDLE or if the current module status is MEMIF_BUSY INTERNAL and the internal management operation can be suspended or aborted, the function Ea_Write shall accept the write request, copy the given / computed parameters to module internal variables, initiate a write job, set the EA module status to MEMIF_BUSY, set the job result to MEMIF JOB PENDING and return with E OK.]()

EA181: If the current module status is MEMIF_UNINIT or MEMIF_BUSY or MEMIF_BUSY_INTERNAL and the internal management operation can't be suspended or aborted, the function Ea_Write shall reject the job request and return with E NOT OK. J()

EA182: If the write request addresses a block containing immediate data, the function Ea_Write shall accept the write request, even if the current module status is MEMIF_BUSY_INTERNAL and the internal management operation can't be suspended or aborted. J()

Note: In this case the internal management operation shall be aborted without the chance to restart it and with the risk of unrecoverable errors for the "normal" data.

[SWS_Ea_00026] [The EA module shall execute the write job of the function Ea Write asynchronously within the EA module's main function.]()

[SWS_Ea_00131] [If development error detection for the module EA is enabled: the function Ea_Write shall check if the module state is MEMIF_UNINIT. If this is the case, the function Ea_Write shall reject the write request, raise the development error EA_E_UNINIT and return with E_NOT_OK.](SRS_BSW_00406)

[SWS_Ea_00171] [If development error detection is enabled for the module: the function Ea_Write shall check if the module state is MEMIF_BUSY. If this is the case, the function Ea_Write shall reject the write request, raise the development

error EA_E_BUSY and return with E_NOT_OK. ()



EA183: If development error detection is enabled for the module: if the current module status is MEMIF BUSY INTERNAL and if it is not possible to suspend or abort the internal management operation (because of data consistency / module implementation / hardware restrictions), the function Ea Write shall reject the write request, raise the development error EA E BUSY INTERNAL and return with E NOT OK. ()

[SWS_Ea_00148] [If development error detection for the module EA is enabled: the function Ea Write shall check whether the given block number is valid (i.e. inside the configured range). If this is not the case, the function Ea Write shall reject the write request, raise the development error EA E INVALID BLOCK NO and return with E NOT OK. (SRS BSW 00323)

[SWS_Ea_00172] [If development error detection is enabled for the module: the function Ea Write shall check that the given data pointer is valid (i.e. that it is not NULL). If this is not the case, the function Ea Write shall reject the write request, raise the development error EA E INVALID DATA PTR and return with E NOT OK. 1()

[SWS Ea 00159] [If a write request is rejected by the function Ea Write, i.e. requirements SWS Ea 00131, SWS Ea 00171, SWS Ea 00148, SWS Ea 00172, EA181 or EA183 apply, the function Ea Write shall not change the current module status or job result. ()

8.3.5 Ea_Cancel

[SWS Ea 00088]

Г

Service name:	Ea_Cancel
Syntax:	void Ea_Cancel(
	void
)
Service ID[hex]:	0x04
Sync/Async:	Synchronous
Reentrancy:	Non Reentrant
Parameters (in):	None
Parameters	None
(inout):	
Parameters (out):	None
Return value:	None
Description:	Cancels the ongoing asynchronous operation.



」()

Note: The function *Ea_Cancel* and the cancel function of the underlying EEPROM driver are synchronous in their bsolete, i.e. their job is done once they return to the caller. On the other hand, they are asynchronous w.r.t. an ongoing read, erase or write job in the EEPROM memory. The cancel functions shall only reset their modules internal variables so that a new job can be accepted by the modules. They do not cancel an ongoing job in the hardware and they do not wait for an ongoing job to be finished by the hardware. This might lead to the situation in which the module's state is reported as IDLE while there is still an ongoing job being executed by the hardware is indeed free before starting a new job (see chapter 9.4 for a detailed sequence diagram).

Note: The function *Ea_Cancel* should only be used by the *NvM* to abort a read or write request for an NV block if higher priority data (i.e. immediate data) has to be written.

[SWS_Ea_00132] [If development error detection for the module EA is enabled: the function Ea_Cancel shall check if the module state is MEMIF_UNINIT. If this is the case, the function Ea_Cancel shall raise the development error EA_E_UNINIT and return to the caller without changing any internal variables.](SRS_BSW_00406)

[SWS_Ea_00077] 「 If the current module status is MEMIF_BUSY (i.e. the request to cancel a pending job is accepted by the function Ea_Cancel), the function Ea_Cancel shall call the cancel function of the underlying EEPROM driver. J()

[SWS_Ea_00078] 「 If the current module status is MEMIF_BUSY (i.e. the request to cancel a pending job is accepted by the function Ea_Cancel), the function Ea_Cancel shall reset the EA module's internal variables to make the module ready for a new job request. I.e. the function Ea_Cancel shall set the job result to MEMIF JOB CANCELED and the module status to MEMIF IDLE.]()

[SWS_Ea_00160] [If the current module status is not MEMIF_BUSY (i.e. the request to cancel a pending job is rejected by the function Ea_Cancel), the function Ea_Cancel shall not change the current module status or job result. J()

[SWS_Ea_00173] [If development error detection is enabled for the module: If the current module status is not MEMIF_BUSY (i.e. there is no job to cancel and therefore the request to cancel a pending job is rejected by the function Ea_Cancel), the function Ea_Cancel shall raise the development error EA E INVALID CANCEL. J()



8.3.6 Ea_GetStatus

[SWS_Ea_00089]

Г

Service name:	Ea_GetStatus
Syntax:	MemIf_StatusType Ea_GetStatus(void)
Service ID[hex]:	0x05
Sync/Async:	Synchronous
Reentrancy:	Non Reentrant
Parameters (in):	None
Parameters (inout):	None
Parameters (out):	None
Return value:	MemIf_StatusType MEMIF_UNINIT: The EA module has not been initialized (yet). MEMIF_IDLE: The EA module is currently idle. MEMIF_BUSY: The EA module is currently busy. MEMIF_BUSY_INTERNAL: The EA module is currently busy with internal management operations.
Description:	Service to return the Status.

ן()

[SWS_Ea_00034] [The function Ea_GetStatus shall return MEMIF_UNINIT if the module has not (yet) been initialized. ()

[SWS_Ea_00156] [The function Ea_GetStatus shall return MEMIF_IDLE if the module is neither processing a request from the upper layer nor is it doing an internal management operation.]()

[SWS_Ea_00157] [The function Ea_GetStatus shall return MEMIF_BUSY if it is currently processing a request from the upper layer.]()

[SWS_Ea_00073] [The function Ea GetStatus shall return

MEMIF_BUSY_INTERNAL, if an internal management operation is currently ongoing.

Note: Internal management operation may e.g. be a re-organization of the used EEPROM memory (garbage collection). This may imply that the underlying device driver is – at least temporarily – busy.

8.3.7 Ea_GetJobResult

```
[SWS_Ea_00090]
```



Service name:	Ea_GetJobResult
Syntax:	MemIf_JobResultType
	void
)
Service ID[hex]:	0x06
Sync/Async:	Synchronous
Reentrancy:	Non Reentrant
Parameters (in):	None
Parameters	None
(inout):	
Parameters (out):	None
Return value:	MemIf_JobResultType MEMIF_JOB_OK: The last job has been finished successfully. MEMIF_JOB_PENDING: The last job is waiting for execution or currently being executed. MEMIF_JOB_CANCELED: The last job has been canceled (which means it failed). MEMIF_JOB_FAILED: The last job was not finished successfully (it failed). MEMIF_BLOCK_INCONSISTENT: The requested block is inconsistent, it may contain corrupted data. MEMIF_BLOCK_INVALID: The requested block has been invalidated, the requested operation can not be performed.
Description:	Service to return the JobResult.

ן()

[SWS_Ea_00134] [If development error detection for the module EA is enabled: the function Ea_GetJobResult shall check if the module state is MEMIF_UNINIT. If this is the case, the function Ea_GetJobResult shall raise the development error EA_E_UNINIT and return with MEMIF_JOB_FAILED. J(SRS_BSW_00406)

[SWS_Ea_00035] [The function Ea_GetJobResult shall return the status of the last job requested by the NVRAM manager. (SRS_BSW_00406)

[SWS_Ea_00174] [Only those jobs which have been requested directly by the upper layer shall have influence on the job result returned by the function Ea_GetJobResult. I.e. jobs which are issued by the EA module itself in the course of internal management operations shall not alter the job result.]()

Note: To facilitate this, the EA module may have to implement a second set of local variables to store the data for internal jobs.

Note: Internal management operations (e.g. "garbage collection") will only be invoked in the context of jobs requested from the NvM. Whether they have to be done before or after the requested job is the decision of the modules implementor and shall not be detailed in this specification.



8.3.8 Ea_InvalidateBlock

[SWS_Ea_00091]

Г

Service name:	Ea_InvalidateBlock	
Syntax:	Std ReturnType Ea InvalidateBlock(
	uint16 BlockNumber	
)	
Service ID[hex]:	0x07	
Sync/Async:	Asynchronous	
Reentrancy:	Non Reentrant	
Parameters (in):	BlockNumber Number of logical block, also denoting start address of that block in EEPROM.	
Parameters (inout):	None	
Parameters (out):	None	
Return value:	Std_ReturnType E_OK: The requested job has been accepted by the module. E_NOT_OK - only if DET is enabled: The requested job has not been accepted by the EA module.	
Description:	Invalidates the block BlockNumber.	

]()

[SWS_Ea_00036] [The function Ea_InvalidateBlock shall take the block number and calculate the corresponding memory block address. J(SRS_MemHwAb_14009)

[SWS_Ea_00037] [Depending on implementation, the function Ea_InvalidateBlock shall invalidate the block <BlockNumber> by either calling the erase function of the underlying device driver or changing some module internal management information accordingly. ()

Note: How exactly the requested block is invalidated depends on the module's implementation and will not be further detailed in this specification. The internal management information has to be stored in NV memory since it has to be resistant against resets. What this information is and how it is stored is not further detailed by this specification.

[SWS_Ea_00135] [If development error detection for the module Ea is enabled: the function Ea_InvalidateBlock shall check if the module state is MEMIF_UNINIT. If this is the case, the function Ea_InvalidateBlock shall reject the invalidation request, raise the development error EA_E_UNINIT and return with E_NOT_OK.]()

[SWS_Ea_00175] [If development error detection is enabled for the module: the function Ea_InvalidateBlock shall check if the module state is MEMIF_BUSY. If this is the case, the function Ea_InvalidateBlock shall reject the invalidation request, raise the development error EA E BUSY and return with E NOT OK.]()



EA184: If development error detection is enabled for the module: if the current module status is MEMIF_BUSY_INTERNAL and if it is not possible to suspend or abort the internal management operation (because of data consistency / module implementation / hardware restrictions), the function Ea_InvalidateBlock shall reject the invalidation request, raise the development error EA_E_BUSY_INTERNAL and return with E_NOT_OK. j()

[SWS_Ea_00149] [If development error detection for the module EA is enabled: the function Ea_InvalidateBlock shall check whether the given block number is valid (i.e. it has been configured). If this is not the case, the function Ea_InvalidateBlock shall reject the request, raise the development error EA E INVALID BLOCK NO and return with E NOT OK. J(SRS_BSW_00323)

[SWS_Ea_00161] [If an invalidation request is rejected by the function Ea_InvalidateBlock, i.e. requirements <u>SWS Ea 00135</u>, <u>SWS Ea 00149</u>, <u>SWS Ea 00175</u> or <u>EA184</u> apply, the function Ea_InvalidateBlock shall not change the current module status or job result. J()

8.3.9 Ea_GetVersionInfo

[SWS_Ea_00092]

Γ

Service name:	Ea_GetVersionInfo	
Syntax:	void Ea_GetVers Std_Version)	sionInfo(hInfoType* VersionInfoPtr
Service ID[hex]:	0x08	
Sync/Async:	Synchronous	
Reentrancy:	Reentrant	
Parameters (in):	None	
Parameters (inout):	None	
Parameters (out):	VersionInfoPtr	Pointer to standard version information structure.
Return value:	None	
Description:	Service to get the ve	ersion information of this module.

J()

[SWS_Ea_00164] [If development error detection for the module EA is enabled: the function EA_GetVersionInfo shall check that the given data pointer is valid (i.e.



that it is not NULL). If this is not the case, the function Ea_GetVersionInfo shall raise the development error EA_E_INVALID_DATA_PTR. J()

8.3.10 Ea_EraseImmediateBlock

[SWS_Ea_00093]

Γ

Service name:	Ea EraseImmediateBlock	
Service name.		
Syntax:	Std ReturnType Ea EraseImmediateBlock(
	uint16 BlockNumber	
Service ID[hex]:	0x09	
Sync/Async:	Asynchronous	
Reentrancy:	Non Reentrant	
Parameters (in):	BlockNumber Number of logical block, also denoting start address of that block in EEPROM.	
Parameters (inout):	None	
Parameters (out):	None	
	Std_ReturnTypeE_OK: The requested job has been accepted by the module.	
Return value:	E_NOT_OK - only if DET is enabled: The requested job has not	
	been accepted by the EA module.	
Description:	Erases the block BlockNumber.	

J(SRS_MemHwAb_14032)

Note: The function Ea_EraseImmediateBlock *shall only be called by e.g. diagnostic or similar system services to pre-erase the area for immediate data if necessary.*

[SWS_Ea_00063] [The function Ea_EraseImmediateBlock shall take the block number and calculate the corresponding memory block address. The block offset shall be fixed to zero for this address calculation.](SRS_MemHwAb_14009, SRS_MemHwAb_14032)

[SWS_Ea_00064] [The function Ea_EraseImmediateBlock shall ensure that the EA module can write immediate data. Whether this involves physically erasing a memory area and therefore calling the erase function of the underlying driver depends on the implementation.](SRS_MemHwAb_14032)

[SWS_Ea_00136] [If development error detection for the module EA is enabled: the function Ea_EraseImmediateBlock shall check if the module state is MEMIF_UNINIT. If this is the case, the function Ea_EraseImmediateBlock shall reject the erase request, raise the development error EA_E_UNINIT and return with E NOT OK. J(SRS_BSW_00406)



[SWS_Ea_00176] [If development error detection is enabled for the module: the function Ea_EraseImmediateBlock shall check if the module state is MEMIF_BUSY. If this is the case, the function Ea_EraseImmediateBlock shall reject the erase request, raise the development error EA_E_BUSY and return with E_NOT_OK.]()

EA185: If development error detection is enabled for the module: if the current module status is MEMIF_BUSY_INTERNAL and if it is not possible to suspend or abort the internal management operation (because of data consistency / module implementation / hardware restrictions), the function Ea_EraseImmediateBlock shall reject the request, raise the development error EA_E_BUSY_INTERNAL and return with E_NOT_OK. ()

[SWS_Ea_00152] [If development error detection for the module EA is enabled: the function Ea_EraseImmediateBlock shall check whether the given block number is valid (i.e. it has been configured). If this is not the case, the function Ea_EraseImmediateBlock shall reject the erase request, raise the development error EA E INVALID BLOCK NO and return with E NOT OK. J(SRS_BSW_00323)

[SWS_Ea_00065] [If development error detection for the EA module is enabled, the function Ea EraseImmediateBlock shall check whether the addressed logical block is configured as containing immediate data (configuration parameter EaImmediateData == TRUE). If not, the function Ea EraseImmediateBlock request, raise the deleopment shall reject the erase error EA_E_INVALID_BLOCK NO and return with E NOT OK. (SRS BSW 00323, SRS MemHwAb 14032)

[SWS_Ea_00162] [If an erase request for an immediate block is rejected by the function Ea_EraseImmediateBlock, i.e. requirements <u>SWS_Ea_00136</u>, <u>SWS_Ea_00176</u>, <u>SWS_Ea_00152</u>, <u>SWS_Ea_00065</u> or <u>EA185</u> apply, the function Ea_EraseImmediateBlock shall not change the current module status or job result. j()

8.4 Call-back notifications

This chaper lists all functions provided by the Ea module to lower layer modules.

[SWS_Ea_00114] [The Ea module shall provide function prototypes of the callback functions in the file Ea Cbk.h j()

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Note: Depending on the implementation of the modules making up the NV memory stack, callback routines provided by the EA module may be called on interrupt level. The implementation of the EA module therefore has to make sure that the runtime of those routines is reasonably short, i.e. since callbacks may be propagated upward through several software layers. Whether callback routines are allowable / feasible on interrupt level depends on the project specific needs (reaction time) and limitations (runtime in interrupt context). Therefore system design has to make sure that the configuration of the involved modules meets those requirements.

8.4.1 Ea_JobEndNotification

[SWS_Ea_00094]

Service name:	Ea_JobEndNotification
Syntax:	void Ea_JobEndNotification(
	void
)
Service ID[hex]:	0x10
Sync/Async:	Synchronous
Reentrancy:	Non Reentrant
Parameters (in):	None
Parameters	None
(inout):	
Parameters (out):	None
Return value:	None
Description:	Service to report to this module the successful end of an asynchronous operation.

J()

The underlying EEPROM driver shall call the function <code>Ea_JobEndNotification</code> to report the successful end of an asynchronous operation.

[SWS_Ea_00153] [If the job result is currently MEMIF_JOB_PENDING, the function Ea_JobEndNotification shall set the job result to MEMIF_JOB_OK, else it shall leave the job result untouched.]()

[SWS_Ea_00051] [The function Ea_JobEndNotification shall perform any necessary block management operations and shall call the corresponding callback routine of the upper layer module (Ea NvMJobEndNotification). j()

Note: The function Ea_JobEndNotification shall be callable on interrupt level.



8.4.2 Ea_JobErrorNotification

[SWS_Ea_00095]

Г

Service name:	Ea_JobErrorNotification
Syntax:	void Ea_JobErrorNotification(void
Service ID[hex]:	, 0x11
Sync/Async:	Synchronous
Reentrancy:	Non Reentrant
Parameters (in):	None
Parameters (inout):	None
Parameters (out):	None
Return value:	None
Description:	Service to report to this module the failure of an asynchronous operation.

]()

The underlying EEPROM driver shall call the function <code>Ea_JobErrorNotification</code> to report the failure of an asynchronous operation.

[SWS_Ea_00154] [If the job result is currently MEMIF_JOB_PENDING, the function Ea_JobErrorNotification shall set the job result to MEMIF_JOB_FAILED, else it shall leave the job result untouched.]()

[SWS_Ea_00053] [The function Ea_JobErrorNotification shall perform any necessary block management and error handling operations and shall call the corresponding callback routine of the upper layer module (Ea NvMJobErrorNotification). J()

Note: The function Ea_JobErrorNotification shall be callable on interrupt level.

8.5 Scheduled functions

These functions are directly called by the Basic Software Scheduler. The following functions shall have no return value and no parameter. All functions shall be non reentrant.

8.5.1 Ea_MainFunction

[SWS_Ea_00096]

• 	
Service name:	Ea_MainFunction
Syntax:	void Ea_MainFunction(

Г



	void
)
Service ID[hex]:	0x12
Description:	Service to handle the requested jobs and the internal management operations.

J(SRS_BSW_00373)

Note: The cycle time for the function *Ea_MainFunction* should be the same as that configured for the underlying EEPROM driver.

[SWS_Ea_00178] [If the module initialization (started in the function Ea_Init) is completed in the module's main function, the function Ea_MainFunction shall set the module status from MEMIF_BUSY_INTERNAL to MEMIF_IDLE once initialization of the module has been successfully finished. J()

[SWS_Ea_00056] [The function Ea_MainFunction shall asynchronously handle the read / write / erase / invalidate jobs requested by the upper layer and internal management operations.]()

[SWS_Ea_00074] [The function Ea_MainFunction shall check, whether the block requested for reading has been invalidated by the upper layer module. If so, the function Ea_MainFunction shall set the job result to MEMIF_BLOCK_INVALID and call the job error notification function if configured.]()

[SWS_Ea_00104] [The function Ea MainFunction shall check the consistency of the logical block being read before notifying the caller. If an inconsistency of the block is detected (see SWS_Ea_00046 and SWS_Ea_00047), the function Ea MainFunction shall set the job result to MEMIF BLOCK INCONSISTENT and call the notification if error routine upper laver configured. of the (SRS_MemHwAb_14032, SRS_MemHwAb_14015, SRS_MemHwAb_14016)

Note: In this case the upper layer shall not use the contents of the data buffer.

EA186: 「If the current module status is MEMIF_BUSY_INTERNAL and if the internal management operation can be suspended without jeopardizing the data consistency: the function Ea_MainFunction shall save all information which is necessary to resume the internal management operation, suspend the internal management operation and start processing the job requested by the upper layer. 」()

EA187: 「If the current module status is MEMIF_BUSY_INTERNAL and if the internal management operation can be aborted without jeopardizing the data consistency: the function Ea_MainFunction shall save all information which is necessary to restart the internal management operation, abort the internal management operation and start processing the job requested by the upper layer. 」()



Note: Whether an internal management operation can be suspended or aborted depends on the type of management operation, the implementation of the EA module and the capabilities of the underlying hardware and thus cannot be determined in this document.

EA188: $\[\]$ If an internal management operation has been suspended because of a job request from the upper layer, the function <code>Ea_MainFunction</code> shall resume this internal management operation once the job requested by the upper layer has been finished. $\[\]$ ()

EA189: $\[Gamma]$ If an internal management operation has been aborted because of a job request from the upper layer, the function <code>Ea_MainFunction</code> shall restart this internal management operation once the job requested by the upper layer has been finished. $\[()\]$

8.6 Expected Interfaces

In this chapter all interfaces required from other modules are listed.

8.6.1 Mandatory Interfaces

This chapter defines all interfaces which are required to fulfill the core functionality of the module.

[SWS_Ea_00097]

Γ

API function	Description	
Eep_Cancel	Cancels a running job.	
Eep_Erase	Service for erasing EEPROM sections.	
Eep_GetJobResult	This service returns the result of the last job.	
Eep_GetStatus	Returns the EEPROM status.	
Eep_Read	Reads from EEPROM.	
Eep_SetMode	Sets the mode.	
Eep_Write	Writes to EEPROM.	

]()

8.6.2 Optional Interfaces

This chapter defines all interfaces which are required to fulfill an optional functionality of the module.

[SWS_Ea_00098]

Г



API function	Description
Det_ReportError	Service to report development errors.

]()

8.6.3 Configurable interfaces

In this chapter all interfaces are listed where the target function could be configured. The target function is usually a callback function. The names of this kind of interfaces are not fixed because they are configurable.

Note: Depending on the implementation of the modules making up the NV memory stack, callback routines invoked by the EA module may be called on interrupt level. The implementor of the module providing these routines therefore has to make sure that their runtime is reasonably short, i.e. since callbacks may be propagated upward through several software layers. Whether callback routines are allowable / feasible on interrupt level depends on the project specific needs (reaction time) and limitations (runtime in interrupt context). Therefore system design has to make sure that the configuration of the involved modules meets those requirements.

[SWS_Ea_00099]

Service name:	NvM_JobEndNotification		
Syntax:	void NvM_JobEndNotification(void		
Sync/Async:	Synchronous		
Reentrancy:	Non Reentrant		
Parameters (in):	None		
Parameters (inout):	None		
Parameters (out):	None		
Return value:	None		
Description:	Function to be used by the underlying memory abstraction to signal end of job without error.		

J(SRS_BSW_00385)

[SWS_Ea_00054] [The Ea module shall call the function defined in the configuration parameter EaNvMJobEndNotification upon successful end of an asynchronous read operation after performing all necessary internal management operations. Successful end of an asynchronous read operation implies the read job is finished and the result is OK. ()

[SWS_Ea_00141] [[] The Ea module shall call the function defined in the configuration parameter EaNvMJobEndNotification upon successful end of an asynchronous write operation after performing all necessary internal management operations. Successful end of an asynchronous write operation implies the write job is finished, the result is OK and the block has been marked as valid. J()



[SWS_Ea_00142] [[] The Ea module shall call the function defined in the configuration parameter EaNvMJobEndNotification upon successful end of an asynchronous erase operation after performing all necessary internal management operations. Successful end of an asynchronous erase operation implies the erase job for immediate data is finished and the result is OK (see <u>SWS Ea_00064</u>). J()

[SWS_Ea_00143] The Ea module shall call the function defined in the configuration parameter EaNvMJobEndNotification upon successful end of an asynchronous block invalidation operation after performing all necessary internal management operations. Successful end of an asynchronous block invalidation operation implies the block invalidation job is finished and the result is OK (i.e. the block has been marked as invalid). ()

Service name:	NvM_JobErrorNotification
Syntax:	void NvM_JobErrorNotification(
	void
)
Sync/Async:	Synchronous
Reentrancy:	Non Reentrant
Parameters (in):	None
Parameters	None
(inout):	
Parameters (out):	None
Return value:	None
Description:	Function to be used by the underlying memory abstraction to signal end of job with
-	error.

[SWS_Ea_00100]

」(SRS_BSW_00385)

[SWS_Ea_00055] [[] The Ea module shall call the function defined in the configuration parameter EaNvMJobErrorNotification upon failure of an asynchronous read operation after performing all necessary internal management and error handling operations. Failure of an asynchronous read operation implies the read job is finished and has failed (i.e. block invalid or inconsistent). ()

[SWS_Ea_00144] [The Ea module shall call the function defined in the configuration parameter EaNvMJobErrorNotification upon failure of an asynchronous write operation after performing all necessary internal management and error handling operations. Failure of an asynchronous write operation implies the write job is finished and has failed and block has been marked as inconsistent.]()

[SWS_Ea_00145] [The Ea module shall call the function defined in the configuration parameter EaNvMJobErrorNotification upon failure of an asynchronous erase operation after performing all necessary internal management and error handling



operations. Failure of an asynchronous erase operation implies the erase job for immediate data is finished and has failed (see SWS_Ea_00064). ()

[SWS_Ea_00146] [The Ea module shall call the function defined in the configuration parameter EaNvMJobErrorNotification upon failure of an asynchronous block invalidation operation after performing all necessary internal management and error handling operations. Failure of an asynchronous block invalidation operation implies the block invalidation job is finished and has failed. 1()



9 Sequence diagrams

Note: For a vendor specific library the following sequence diagrams are valid only insofar as they show the relation to the calling modules (Ecu_StateManager resp. memory abstraction interface). The calling relations from a memory abstraction module to an underlying driver are not relevant / binding for a vendor specific library.

9.1 Ea_Init

The following figure shows the call sequence for the Ea_Init routine. It is different from that of all other services of this module as it is not called by the NVRAM manager and not called via the memory abstraction interface.

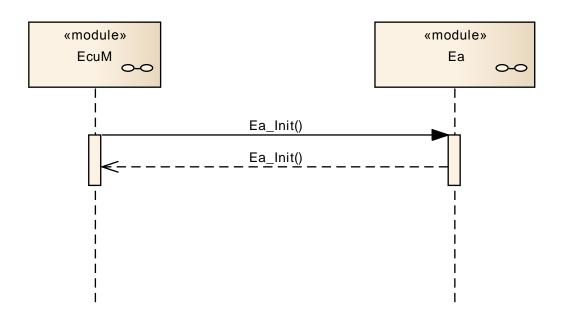


Figure 5: Sequence diagram of "Ea_Init" service



9.2 Ea_SetMode

The following figure shows as an example the call sequence for the Ea_SetMode service. This sequence diagram also applies to the other synchronous services of this module with exception of the Ea_Init routine (see above).

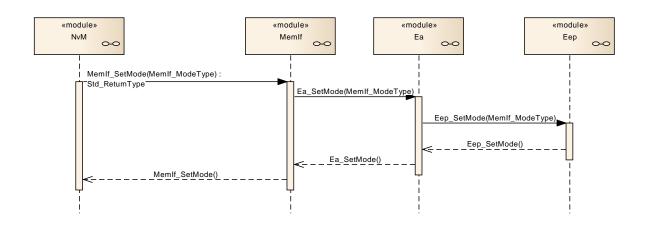


Figure 6: Sequence diagram of the "Ea_SetMode" service



9.3 Ea_Write

The following figure shows as an example the call sequence for the Ea_Write service. This sequence diagram also applies to the other asynchronous services of this module.

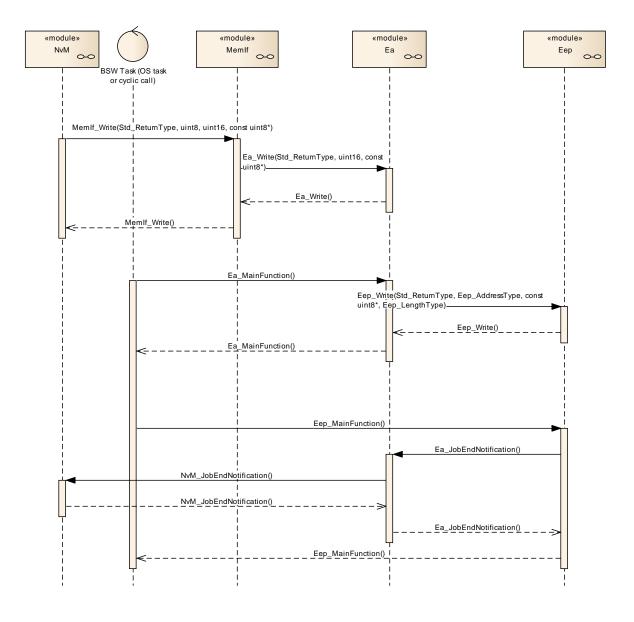


Figure 7: Sequence diagram "Ea_Write"



9.4 Ea_Cancel

The following figure shows as an example the call sequence for a canceled Ea_Write service. This sequence diagram shows that Ea_Cancel is asynchronous w.r.t. the underlying hardware while itself being synchronous.



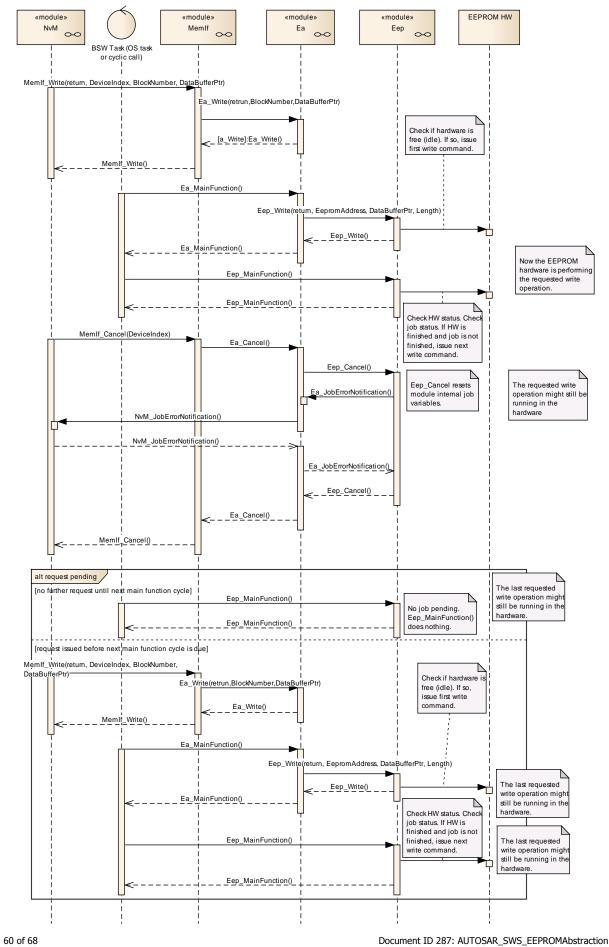




Figure 8: Sequence diagram "Ea_Cancel"



10 Configuration specification

10.1 Containers and configuration parameters

The following chapters summarize all configuration parameters. The detailed meanings of the parameters describe Chapters 7 and Chapter 8.

10.1.1 Variants

[SWS_Ea_00163] [The EA module shall support (only) the following configuration variants:

• VARIANT-PRE-COMPILE

Only parameters with "Pre-compile time" configuration are allowed in this variant. ()

10.1.2 Ea

SWS Item	ECUC_Ea_00133 :	
Module Name	Ea	
Module Description	Configuration of the Ea (EEPROM Abstraction) module. The module shall abstract from the device specific addressing scheme and segmentation and provide the upper layers with a virtual addressing scheme and segmentation as well as a 'virtually' unlimited number of erase cycles.	

Included Containers			
Container Name	Multiplicity	ty Scope / Dependency	
EaBlockConfiguration		Configuration of block specific parameters for the EEPROM abstraction module.	
EaGeneral	1	General configuration of the EEPROM abstraction module. This container lists block independent configuration parameters.	
EaPublishedInformation	1	Additional published parameters not covered by CommonPublishedInformation container. Note that these parameters do not have any configuration class setting, since they are published information.	



10.1.3 EaGeneral

SWS Item	ECUC_Ea_00039 :	
Container Name	EaGeneral{EA_ModuleConfiguration}	
	General configuration of the EEPROM abstraction module. This container lists block independent configuration parameters.	
Configuration Parameters		

SWS Item	ECUC_Ea_00120 :		
Name	EaDevErrorDetect {EA_DEV_ERROR_DETECT}		
Description	Pre-processor switch to enable and disable development error detection. true: Development error detection enabled. false: Development error detection disabled.		
Multiplicity	1		
Туре	EcucBooleanParamDef		
Default value			
ConfigurationClass	Pre-compile time	Х	All Variants
	Link time		
	Post-build time		
Scope / Dependency	scope: local		

SWS Item	ECUC_Ea_00118 : (Obsolete)		
Name	Ealndex		
Description	This parameter is obsolete and will be removed in future. Specifies the InstanceId of this module instance. If only one instance is present it shall have the Id 0. Tags: atp.Status=obsolete atp.StatusRevisionBegin=4.1.1		
Multiplicity	01		
Туре	EcucIntegerParamDef		
Range	0254		
Default value			
ConfigurationClass	Pre-compile time X All Variants		
	Link time		
	Post-build time		
Scope / Dependency	scope: local		

SWS Item	ECUC_Ea_00132 :			
Name	EaMainFunctionPeriod {E	EaMainFunctionPeriod {EA_MAIN_FUNCTION_PERIOD}		
Description	The period between succ	essive ca	alls to the main function in seconds.	
Multiplicity	1	1		
Туре	EcucFloatParamDef			
Range	1E-7 INF			
Default value				
ConfigurationClass	Pre-compile time	Х	All Variants	
	Link time			
	Post-build time	Post-build time		
Scope / Dependency	scope: ECU			

SWS Item	ECUC_Ea_00121 :
Name	EaNvmJobEndNotification {EA_NVM_JOB_END_NOTIFICATION}
	Mapped to the job end notification routine provided by the upper layer module (NvM_JobEndNotification).
Multiplicity	01
Туре	EcucFunctionNameDef
Default value	



maxLength		
minLength		
regularExpression		
ConfigurationClass	Pre-compile time	X All Variants
	Link time	
	Post-build time	
Scope / Dependency	scope: local	

SWS Item	ECUC_Ea_00122 :			
Name	EaNvmJobErrorNotification	on {EA_N	VM_JOB_ERROR_NOTIFICATION}	
Description	Mapped to the job error notification routine provided by the upper layer module (NvM_JobErrorNotification).			
Multiplicity	01	01		
Туре	EcucFunctionNameDef			
Default value				
maxLength				
minLength				
regularExpression				
ConfigurationClass	Pre-compile time X All Variants			
	Link time	Link time		
	Post-build time			
Scope / Dependency	scope: local			

SWS Item	ECUC_Ea_00123 :			
Name	EaPollingMode {EA_POL	EaPollingMode {EA_POLLING_MODE}		
Description	Pre-processor switch to enable and disable the polling mode for this module. true: Polling mode enabled, callback functions (provided to EEP module) disabled. false: Polling mode disabled, callback functions (provided to EEP module) enabled.			
Multiplicity	1	1		
Туре	EcucBooleanParamDef			
Default value				
ConfigurationClass	Pre-compile time	Pre-compile time X All Variants		
_	Link time	Link time		
	Post-build time	Post-build time		
Scope / Dependency	scope: local	-		

SWS Item	ECUC_Ea_00001 :			
Name	EaSetModeSupported {EA_SET_MODE_SUPPORTED}			
Description	Compile switch to enable / d	Compile switch to enable / disable the function Ea_SetMode.		
Multiplicity	1			
Туре	EcucBooleanParamDef			
Default value	true	true		
ConfigurationClass	Pre-compile time	Pre-compile time X All Variants		
	Link time			
	Post-build time			
Scope / Dependency	scope: local			

SWS Item	ECUC_Ea_00124 :
Name	EaVersionInfoApi {EA_VERSION_INFO_API}
-	Pre-processor switch to enable / disable the API to read out the modules version information. true: Version info API enabled. false: Version info API disabled.
Multiplicity	1



Туре	EcucBooleanParamDef			
Default value				
ConfigurationClass	Pre-compile time X All Variants			
	Link time			
	Post-build time			
Scope / Dependency	scope: local			

SWS Item	ECUC_Ea_00125 :			
Name	EaVirtualPageSize {EA_VIRTUAL_PAGE_SIZE}			
Description	The size in bytes to whic	The size in bytes to which logical blocks shall be aligned.		
Multiplicity	1	1		
Туре	EcucIntegerParamDef			
Range	0 65535	0 65535		
Default value				
ConfigurationClass	Pre-compile time	Х	All Variants	
	Link time	Link time		
	Post-build time			
Scope / Dependency	scope: local			

No Included Containers

10.1.4 EaBlockConfiguration

SWS Item	ECUC_Ea_00040 :
Container Name	EaBlockConfiguration{EA_BlockConfiguration}
Description	Configuration of block specific parameters for the EEPROM abstraction module.
Configuration Parameters	

SWS Item	ECUC_Ea_00130 :			
Name	EaBlockNumber {EA_BLOCK_NUMBER}			
Description	Block identifier (handle). 0x0000 and 0xFFFF shall no Range:	0x0000 and 0xFFFF shall not be used for block numbers (see EA006).		
	min = 2^NVM_DATASET_SELECTION_BITS max = 0xFFFF -2^NVM_DATASET_SELECTION_BITS Note: Depending on the number of bits set aside for dataset selection			
	several other block numbers shall also be left out to ease implementation.			
Multiplicity	1			
Туре	EcucIntegerParamDef (Sym	bolic I	Name generated for this parameter)	
Range	1 65534	1 65534		
Default value				
ConfigurationClass	Pre-compile time	Pre-compile time X All Variants		
-	Link time			
	Post-build time			
Scope / Dependency	scope: ECU			

SWS Item	ECUC_Ea_00128 :	
Name	EaBlockSize {EA_BLOCK_SIZE}	
Description	Size of a logical block in bytes.	
Multiplicity	1	
Туре	EcucIntegerParamDef	
Range	1 65535	
Default value		



ConfigurationClass	Pre-compile time	Х	All Variants
	Link time		
	Post-build time		
Scope / Dependency	scope: ECU		

SWS Item	ECUC_Ea_00131 :				
Name	EalmmediateData {EA_IM	EalmmediateData {EA_IMMEDIATE_DATA}			
Description	Marker for high priority data. true: Block contains immediate data. false: Block does not contain immediate data.				
Multiplicity	1	1			
Туре	EcucBooleanParamDef	EcucBooleanParamDef			
Default value					
ConfigurationClass	Pre-compile time	Pre-compile time X All Variants			
	Link time	Link time			
	Post-build time				
Scope / Dependency	scope: ECU				

SWS Item	ECUC_Ea_00119 :		
Name	EaNumberOfWriteCycles {EA_NUMBER_OF_WRITE_CYCLES}		
Description	Number of write cycles required for this block.		
Multiplicity	1		
Туре	EcucIntegerParamDef		
Range	0 4294967295		
Default value			
ConfigurationClass	Pre-compile time	Х	All Variants
	Link time		
	Post-build time		
Scope / Dependency	scope: local		

SWS Item	ECUC_Ea_00129 :		
Name	EaDeviceIndex {EA_DEVIC	E_IND	DEX}
Description	Device index (handle).		
	Range: 0 254 (0xFF reser	ved fo	r broadcast call to GetStatus function).
Multiplicity	1		
Туре	Symbolic name reference to [EepGeneral]		
ConfigurationClass	Pre-compile time	Х	All Variants
	Link time		
	Post-build time		
Scope / Dependency	scope: local dependency: This information is needed by the NVRAM manager respectively the Memory Abstraction Interface to address a certain logical block. It is listed in this specification to give a complete overview over all block related configuration parameters.		

No Included Containers

10.2 Published Information

SWS Item	ECUC_Ea_00043 :	
Container Name	EaPublishedInformation	
Description	Additional published parameters not covered by	
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	CommonPublishedInformation container.
	Note that these parameters do not have any configuration class setting,
	since they are published information.
Or afference tion Drug markens	

Configuration Parameters

SWS Item	ECUC_Ea_00126 :	
Name	EaBlockOverhead {EA_BLOCK_OVERHEAD}	
Description	Management overhead per logical block in bytes. Note: If the management overhead depends on the block size or block location a formula has to be provided that allows the configurator to calculate the management overhead correctly.	
Multiplicity	1	
Туре	EcucIntegerParamDef	
Range	0 65535	
Default value		
ConfigurationClass	Published Information X All Variants	
Scope / Dependency	scope: local	

SWS Item	ECUC_Ea_00070 : (Obsolete)	
Name	EaMaximumBlockingTime {EA_MAXIMUM_BLOCKING_TIME}	
Description	This parameter is obsolete and will be removed in future. The maximum time the EA module's API routines shall be blocked (delayed) by internal operations. (EA070) Note: Internal operations in that case means operations that are not explicitly invoked from the upper layer module but need to be handled for proper operation of this module or the underlying memory driver. Tags: atp.Status=obsolete atp.StatusRevisionBegin=4.1.1	
Multiplicity	01	
Туре	EcucFloatParamDef	
Range	0 INF	
Default value		
ConfigurationClass	Published Information X All Variants	
Scope / Dependency	scope: local	

SWS Item	ECUC_Ea_00127 :	
Name	EaPageOverhead {EA_PAGE_OVERHEAD}	
Description	Management overhead per page in bytes. Note: If the management overhead depends on the block size or block location a formula has to be provided that allows the configurator to calculate the management overhead correctly.	
Multiplicity	1	
Туре	EcucIntegerParamDef	
Range	0 65535	
Default value		
ConfigurationClass	Published Information X All Variants	
Scope / Dependency	scope: local	

No Included Containers



11 Not applicable requirements

[SWS_Ea_00999] [These requirements are not applicable to this specification.] (SRS_BSW_00416, SRS_BSW_00168, SRS_BSW_00423, SRS_BSW_00424, SRS BSW 00425. SRS BSW 00426. SRS BSW 00427. SRS BSW 00428. SRS BSW 00429, BSW00431, SRS BSW 00432, SRS BSW 00433, BSW00434, SRS BSW 00336, SRS BSW 00339, BSW00421, SRS BSW 00422, BSW00420, SRS_BSW_00417, SRS_BSW_00161, SRS_BSW_00162, BSW00324, SRS_BSW_00005, SRS_BSW_00415, SRS_BSW_00164, SRS_BSW_00326, SRS BSW 00342. SRS BSW 00160. SRS BSW 00007. SRS BSW 00300. SRS_BSW_00347, SRS_BSW_00305, SRS_BSW_00307, SRS_BSW_00314, SRS BSW 00348, SRS BSW 00353, SRS BSW 00361, SRS BSW 00302, SRS BSW 00328, SRS BSW 00312, SRS BSW 00006, SRS BSW 00304, SRS BSW 00355, SRS BSW 00378, SRS BSW 00306, SRS BSW 00308, SRS BSW 00309, SRS BSW 00371, SRS BSW 00330, SRS BSW 00009, SRS_BSW_00401, SRS_BSW_00172, SRS_BSW_00010, SRS_BSW_00333, SRS BSW 00321, SRS BSW 00341, SRS BSW 00334, SRS SPAL 12263, SRS SPAL 12267, SRS SPAL 12125, SRS SPAL 12163, BSW12058, BSW12059, BSW12060, SRS_SPAL_12461, SRS_SPAL_12462, SRS_SPAL_12463, BSW12062, SRS_SPAL_12068, SRS_SPAL_12069, SRS_SPAL_00157, BSW12155, SRS_SPAL_12063, SRS_SPAL_12129, SRS_SPAL_12064, SRS_SPAL_12067, SRS_SPAL_12077, SRS_SPAL_12078, SRS SPAL 12092, SRS SPAL 12265, BSW12081, SRS MemHwAb 14018)