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## 1 Scope of this Document

This document specifies requirements on the module ADC Driver.

### Constraints

First scope for specification of requirements on basic software modules are systems which are not safety relevant. For this reason safety requirements are assigned to medium priority.

## 2 How to Read this Document

Each requirement has its unique identifier starting with the prefix “BSW” (for “Basic Software”). For any review annotations, remarks or questions, please refer to this unique ID rather than chapter or page numbers!

### 2.1 Conventions Used

In requirements, the following specific semantics are used (taken from Request for Comment RFC 2119 from the Internet Engineering Task Force IETF)

The key words "MUST", "MUST NOT", "REQUIRED", "SHALL", "SHALL NOT", "SHOULD", "SHOULD NOT", "RECOMMENDED", "MAY", and "OPTIONAL" in this document are to be interpreted as described in RFC 2119. Note that the requirement level of the document in which they are used modifies the force of these words.

- **MUST:** This word, or the terms "REQUIRED" or "SHALL", mean that the definition is an absolute requirement of the specification.
- **MUST NOT:** This phrase, or the phrase "SHALL NOT", means that the definition is an absolute prohibition of the specification.
- **SHOULD:** This word, or the adjective "RECOMMENDED", mean that there may exist valid reasons in particular circumstances to ignore a particular item, but the full implications must be understood and carefully weighed before choosing a different course.
- **SHOULD NOT:** This phrase, or the phrase "NOT RECOMMENDED" mean that there may exist valid reasons in particular circumstances when the particular behavior is acceptable or even useful, but the full implications should be understood and the case carefully weighed before implementing any behavior described with this label.
- **MAY:** This word, or the adjective "OPTIONAL", means that an item is truly optional. One vendor may choose to include the item because a particular marketplace requires it or because the vendor feels that it enhances the product while another vendor may omit the same item. An implementation, which does not include a particular option, **MUST** be prepared to interoperate with another implementation, which does include the option, though perhaps with reduced functionality. In the same vein an implementation, which does include a particular option, **MUST** be prepared to interoperate with another implementation, which does not include the option (except, of course, for the feature the option provides.)

### 2.2 Requirements Structure

Each module specific chapter contains a short functional description of the Basic Software Module. Requirements of the same kind within each chapter are grouped under the following headlines (where applicable):

Functional Requirements:

- Configuration (which elements of the module need to be configurable).
- Initialization.
- Normal Operation.
- Shutdown Operation.
- Fault Operation.
- ...

**Non-Functional Requirements:**

- Timing Requirements.
- Resource Usage.
- Usability.
- Output for other WPs (e.g. Description Templates, Tooling,...).
- ...



### 3 Acronyms and Abbreviations

Acronyms and abbreviations that have a local scope are not contained in the AUTOSAR glossary. These must appear in a local glossary.

<b>Acronym:</b>	<b>Description:</b>
CS	Chip select
DIO	Digital Input Output
ECU	Electric Control Unit
EOL	End Of Line Often used in the term 'EOL Programming' or 'EOL Configuration'
HIS	Herstellerinitiative Software
ICU	Input Capture Unit
MAL	Old name of Microcontroller Abstraction Layer (replaced by MCAL because 'MAL' is a French term meaning 'bad')
MCAL	Microcontroller Abstraction Layer
MCU	Microcontroller Unit
MMU	Memory Management Unit
Master	A device controlling other devices (slaves, see below)
Slave	A device being completely controlled by a master device
NMI	Non maskable interrupt
OS	Operating System
PLL	Phase Locked Loop
PWM	Pulse Width Modulation
RX	Reception (in the context of bus communication)
SPAL	The name of this working group
SFR	Special Function Register
RTE	Runtime environment
WP	Work Package
ADC	Analogue Digital Converter
FFT	Fast Fourier Transformer

<b>Abbreviation:</b>	<b>Description:</b>
STD	Standard
REQ	Requirement
UNINIT	Uninitialized (= not initialized)

As this is a document from professionals for professionals, all other terms are expected to be known.

## 4 Requirement Specification

### 4.1 ADC Driver - General

#### 4.1.1 Functional Overview

The ADC driver initializes and controls the internal Analogue Digital Converter unit(s) of the microcontroller. It provides services to start and stop a conversion respectively to enable and disable the trigger source of a conversion. Furthermore, it provides services to enable and disable a notification mechanism and routines to query the status and result of a conversion.

The ADC Driver shall work on so called ADC Channels. An ADC channel combines an analogue input pin, the needed ADC circuitry itself and a conversion result register into an entity that can be individually controlled and accessed via the ADC Driver.

All used terms are specified in the following chapter.

#### 4.1.2 Terms and Expressions

The following expressions are used within the ADC driver:

<b>Expression</b>	<b>Explanation</b>
HW Unit	Represents a microcontroller input electronic device that includes all parts necessary to perform an "analogue to digital conversion".
ADC channel	Represents a logical ADC entity bound to one port pin. Multiple ADC entities can be mapped to the same port pin.
ADC channel group	A group of ADC channels linked to the same ADC hardware unit (e.g. one Sample&Hold and one A/D converter). The conversion of the whole group is triggered by one trigger source.
ADC result buffer	The user of the ADC Driver has to provide a buffer for every group. This buffer can hold multiple samples of the same channel group if streaming access mode is selected. If single access mode is selected one sample of each group channel is held in the buffer.
Trigger Source	Source event that starts a single conversion or a continuous series of conversions.
Conversion Mode	<u>One-Shot:</u> The conversion of an ADC channel group is performed once after a trigger and the result is written to the assigned buffer. A trigger can be a software API call or a hardware event. <u>Continuous:</u> The conversions of an ADC channel group are performed continuously after a software API call (start). The conversions themselves are running automatically (hardware/interrupt controlled). The Continuous conversions can be stopped by a software API call (stop).
Sampling Time	Time during which the analogue value is sampled (e.g. loading the capacitor, ...)
Conversion Time	Time during which the sampled analogue value is converted into digital representation.
Acquisition Time	Sample Time + Conversion Time.

## 4.2 ADC Driver

### 4.2.1 Functional Requirements

#### 4.2.1.1 Configuration

##### 4.2.1.1.1 [BSW12307] ADC Channel Configuration

<b>Initiator:</b>	WP4.2.2.1.12
<b>Date:</b>	27.09.2005
<b>Short Description:</b>	ADC channel configuration.
<b>Type:</b>	Changed: 29.06.2006 Merged with requirement BSW12306 ADC Global Configuration. 29.09.2006 Reformulated "Channel capability" in a detailed list of parameters.
<b>Importance:</b>	High
<b>Description:</b>	The ADC Driver shall support the following basic static configurations per channel (if supported by HW): <ul style="list-style-type: none"> <li>• Symbolic name for channel.</li> <li>• Sampling time.</li> <li>• Conversion time.</li> <li>• Resolution in bits.</li> <li>• Reference voltage source.</li> <li>• Clock source with optional prescaler settings.</li> <li>• Other MCU dependent parameters.</li> </ul> <p>Note: If one or more of these configuration parameters can only be assigned to the whole module, it's on the configuration tool to optimize data representation.</p>
<b>Rationale:</b>	To allow different usage for each channel.
<b>Use Case:</b>	--
<b>Dependencies:</b>	[BSW12001] Configuration of port pin properties (see PORT Driver SRS): the association with port pins is done in the overall port configuration.
<b>Conflicts:</b>	--
<b>Supporting Material:</b>	--

#### 4.2.1.1.2 [BSW12447] ADC Channel Group Configuration

<b>Initiator:</b>	WP4.2.2.1.12
<b>Date:</b>	29.06.2006
<b>Short Description:</b>	ADC channel group configuration.
<b>Type:</b>	Changed: 29.06.06 Reformulated, split BSW12447 into 3 distinct requirements. 29.06.06 Merged with requirement BSW12360 ADC Channel Group Configuration.
<b>Importance:</b>	High
<b>Description:</b>	The ADC Driver shall allow to group ADC channels that belong to the same ADC HW unit into so called ADC channel groups (or groups for short). An ADC Channel Group shall consist of at least one ADC Channel. All channels of a group share the same group configuration. The ADC Driver shall support the following basic static configurations per channel group (if supported by HW): <ul style="list-style-type: none"> <li>• Symbolic name for group.</li> <li>• Group notification on/off.</li> <li>• Callback notification function.</li> <li>• List of channels configured for use.</li> <li>• Group trigger source.</li> <li>• Group conversion mode.</li> </ul>
<b>Rationale:</b>	Allow sampling of multiple channels using only one control thread.
<b>Use Case:</b>	Grouping of several channels where the conversion results shall be consistent.
<b>Dependencies:</b>	[BSW12001] Configuration of port properties (see PORT Driver SRS): the association with port pins is done in the overall port configuration. [BSW12307] ADC Channel Configuration.
<b>Conflicts:</b>	--
<b>Supporting Material:</b>	--

#### 4.2.1.1.3 [BSW12817] Configuration of Group Trigger Sources

<b>Initiator:</b>	WP4.2.2.1.12
<b>Date:</b>	29.06.2006
<b>Short Description:</b>	Configuration of group trigger sources.
<b>Type:</b>	Changed: 29.07.06 Reformulated, split BSW12447 into 3 distinct requirements.
<b>Importance:</b>	High
<b>Description:</b>	The ADC Driver shall allow for each ADC channel group the static configuration of exactly one trigger source. Possible trigger sources are: <ul style="list-style-type: none"> <li>• Hardware event (e.g. on an IO pin or a timer interrupt generated MCU internal), if supported by HW.</li> <li>• SW API call.</li> </ul>
<b>Rationale:</b>	To make sure that one group is only controlled from one source (avoid inconsistencies, concurrency issues and so forth).
<b>Use Case:</b>	A group of ADC channels configured to be converted as reaction to an external interrupt on an IO pin.
<b>Dependencies:</b>	[BSW12447] ADC channel group configuration.
<b>Conflicts:</b>	--
<b>Supporting Material:</b>	--

#### 4.2.1.1.4 [BSW12818] Assignment of an ADC Channel to Multiple ADC Channel Groups

<b>Initiator:</b>	WP4.2.2.1.12
<b>Date:</b>	29.06.2006
<b>Short Description:</b>	Assignment of an ADC Channel to multiple ADC channel groups.
<b>Type:</b>	Changed: 29.07.06 Reformulated, split BSW12447 into 3 distinct requirements.
<b>Importance:</b>	High
<b>Description:</b>	The ADC Driver shall allow assigning one ADC channel to more than one ADC Channel Group.
<b>Rationale:</b>	This allows for different trigger sources for an ADC Channel if it is configured for different groups.
<b>Use Case:</b>	Same ADC channel usually converted cyclically (per HW event, in the background) can be converted once per SW API call.
<b>Dependencies:</b>	--
<b>Conflicts:</b>	--
<b>Supporting Material:</b>	Note: The ADC Driver shall not handle any problems arising from concurrent operation of the affected groups. The user of the ADC Driver has to ensure that there are no such concurrent operations of the affected groups.

#### 4.2.1.1.5 [BSW12821] Buffer Configuration for ADC Conversion Results

<b>Initiator:</b>	WP4.2.2.1.12
<b>Date:</b>	09.08.2006
<b>Short Description:</b>	Buffer configuration for ADC results
<b>Type:</b>	New: 09.08.2006 Substitutes requirement BSW12286. Changed: 17.11.2006 Adapted short description. 17.09.2007 BSW12821 now valid for all channel groups independent of the mode selected.
<b>Importance:</b>	High
<b>Description:</b>	For all channel groups, the ADC driver shall provide at configuration time the possibility to define a buffer for storing samples. As the result, the following parameters shall be added to the ADC channel group parameters: <ul style="list-style-type: none"> <li>• Pointer to data buffer (destination for conversion results).</li> <li>• Number of ADC values.</li> </ul>
<b>Rationale:</b>	Conversion results for all ADC channel groups are stored in external buffers.
<b>Use Case:</b>	Cyclic AD conversions as input for electric motor controller.
<b>Dependencies:</b>	
<b>Conflicts:</b>	--
<b>Supporting Material:</b>	--

#### 4.2.1.1.6 [BSW12820] ADC Priority for Channel Groups

<b>Initiator:</b>	WP4.2.2.1.12
<b>Date:</b>	06.07.2006
<b>Short Description:</b>	ADC priority for channel groups.
<b>Type:</b>	New Changed: 17.09.2007 Queuing for sequential requests fro the same group removed.
<b>Importance:</b>	High

<b>Description:</b>	The ADC driver shall allow the configuration of a priority level for each channel group. This implies a prioritization mechanism, implemented in SW (or where available supported by the HW), which offers following features: <ul style="list-style-type: none"> <li>• Queuing of requests for different groups.</li> <li>• Priority handling (aborting/suspending and restarting/resuming conversions).</li> <li>• Higher priority groups can abort/suspend lower priority groups.</li> <li>• Priorities 0..255, lowest priority 0.</li> </ul>
<b>Rationale:</b>	Give the opportunity to a group to be immediately converted (on demand) by interrupting an already ongoing conversion. Due to the possibility of nested request for conversion it is necessary to define a priority to solve the sequence in which the driver will manage them.
<b>Use Case:</b>	Triggered conversions are more and more used in Powertrain controllers (one example is the measurement of injector bank currents/voltages which need to be done synchronously with the fuel pulses – while there are auto-scan conversions ongoing in background).
<b>Dependencies:</b>	[BSW12447] ADC channel group configuration. [BSW12818] Assignment of an ADC Channel to multiple ADC channel groups.
<b>Conflicts:</b>	--
<b>Supporting Material:</b>	--

#### 4.2.1.1.7 [BSW12280] ADC Channel Group Results Access Mode

<b>Initiator:</b>	BMW
<b>Date:</b>	27.09.2005
<b>Short Description:</b>	ADC channel group results access mode.
<b>Type:</b>	Changed: 17.11.2006 Transformed into configuration requirement. 17.09.2007 Single value results access mode removed; ADC result buffer access possible for all ADC channel groups; access to last group conversion results added
<b>Importance:</b>	High
<b>Description:</b>	The ADC Driver shall allow the following result access modes for each ADC Channel Group: <ul style="list-style-type: none"> <li>• Result access to the ADC result buffer. The ADC result buffer contains in streaming access mode the streaming results and in single access mode the results of the last group conversion.</li> <li>• If statically configured, a result access to the results of the last group conversion shall be supported, also in streaming access mode. The group channel results are stored in the buffer in ascending order, in contrast to the buffer layout of the ADC result buffer in streaming access mode.</li> </ul>
<b>Rationale:</b>	Every channel value shall be accessible until the next scan of the channel group is completed.
<b>Use Case:</b>	--
<b>Dependencies:</b>	[BSW12447] ADC channel group configuration.
<b>Conflicts:</b>	--
<b>Supporting Material:</b>	BMW Specification MCAL V1.0a, MAL14.2.0.

#### 4.2.1.2 Normal Operation

##### 4.2.1.2.1 [BSW12283] Mask out Information Bits

<b>Initiator:</b>	BMW
<b>Date:</b>	25.09.2004
<b>Short Description:</b>	Mask out information bits.
<b>Type:</b>	Changed. 08.11.2006 Reformulated, split BSW12283 into 2 distinct requirements.
<b>Importance:</b>	High
<b>Description:</b>	The ADC driver shall mask out information bits from the conversion result not belonging to the ADC value.
<b>Rationale:</b>	Information bits are microcontroller specific.
<b>Use Case:</b>	Some $\mu$ Cs are saving information bits in the result register (e.g. the channel number). These bits shall be masked out.
<b>Dependencies:</b>	--
<b>Conflicts:</b>	--
<b>Supporting Material:</b>	BMW Specification MCAL V1.0a, MAL14.4.1.

##### 4.2.1.2.2 [BSW12824] Right-aligned Results

<b>Initiator:</b>	BMW
<b>Date:</b>	08.11.2006
<b>Short Description:</b>	Right-aligned results.
<b>Type:</b>	Changed 08.11.2006 Reformulated, split BSW12283 into 2 distinct requirements.
<b>Importance:</b>	High
<b>Description:</b>	The result shall be right-aligned.
<b>Rationale:</b>	Information bits are microcontroller specific.
<b>Use Case:</b>	--
<b>Dependencies:</b>	--
<b>Conflicts:</b>	--
<b>Supporting Material:</b>	--

##### 4.2.1.2.3 [BSW12819] ADC Channel Group Read Service

<b>Initiator:</b>	WP4.2.2.1.12
<b>Date:</b>	06.07.2006
<b>Short Description:</b>	ADC channel group read service
<b>Type:</b>	New: 06.07.2006 substitute requirement BSW12285 ADC channel read service.
<b>Importance:</b>	High
<b>Description:</b>	The ADC Driver shall provide a synchronous service for reading the last valid conversion results of the selected channel group (passed as parameter).
<b>Rationale:</b>	Provide access to the most recent converted values of a channel group.
<b>Use Case:</b>	This is the standard functionality for reading ADC values.
<b>Dependencies:</b>	--
<b>Conflicts:</b>	--
<b>Supporting Material:</b>	BMW Specification MCAL V1.0a.



#### 4.2.1.2.4 [BSW12822] ADC Uniform Result Structure

<b>Initiator:</b>	WP4.2.2.1.12
<b>Date:</b>	09.08.2006
<b>Short Description:</b>	ADC uniform result structure.
<b>Type:</b>	Changed: 17.11.2006 Removed user case. 17.09.2007 Tailoring on largest channel in any group.
<b>Importance:</b>	High
<b>Description:</b>	The structure containing the results of a channel group conversion shall be generated by the configuration tool with a uniform dimension, tailored on the largest (in number of bits) channel belonging to any group.
<b>Rationale:</b>	Channels within one group may have different sized result values (e.g. 8 bit, 10 bit, 12 bit, ...).
<b>Use Case:</b>	--
<b>Dependencies:</b>	[BSW12447] ADC channel group configuration.
<b>Conflicts:</b>	--
<b>Supporting Material:</b>	--

#### 4.2.1.2.5 [BSW12317] ADC Channel Group Notification Function

<b>Initiator:</b>	CAS
<b>Date:</b>	27.09.2005
<b>Short Description:</b>	ADC channel group notification function.
<b>Type:</b>	Changed
<b>Importance:</b>	High
<b>Description:</b>	The ADC Driver shall provide notification functions to inform the caller about the end of a conversion for a Channel Group.
<b>Rationale:</b>	Allow for non blocking access to the ADC.
<b>Use Case:</b>	--
<b>Dependencies:</b>	--
<b>Conflicts:</b>	--
<b>Supporting Material:</b>	--

#### 4.2.1.2.6 [BSW12291] ADC Channel Group Status Service

<b>Initiator:</b>	BMW, CAS
<b>Date:</b>	27.09.2005
<b>Short Description:</b>	ADC channel group status service.
<b>Type:</b>	Changed
<b>Importance:</b>	High
<b>Description:</b>	The ADC Driver shall provide a service for querying the status of an ADC Channel Group. The ADC channel group status shall be one out of following four: <ul style="list-style-type: none"> <li>• Idle.</li> <li>• Busy (conversion ongoing).</li> <li>• Completed.</li> <li>• Stream Completed.</li> </ul>
<b>Rationale:</b>	Before reading a value it might be necessary to query the status of the ADC.
<b>Use Case:</b>	The status can be used for access synchronization.
<b>Dependencies:</b>	--
<b>Conflicts:</b>	--



<b>Supporting Material:</b>	BMW Specification MCAL V1.0a.
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#### 4.2.1.2.7 [BSW12318] Enable / Disable Notification Functions

<b>Initiator:</b>	CAS
<b>Date:</b>	27.09.2005
<b>Short Description:</b>	Enable / disable notification functions.
<b>Type:</b>	Changed
<b>Importance:</b>	High
<b>Description:</b>	The ADC driver shall provide a service to enable and disable each notification function separately.
<b>Rationale:</b>	Prevents from invoking unwanted notifications (interrupts) and allows to select the exact point in the execution flow of the software when the first or next notification may arise.
<b>Use Case:</b>	--
<b>Dependencies:</b>	--
<b>Conflicts:</b>	--
<b>Supporting Material:</b>	--

#### 4.2.1.2.8 [BSW12364] Start and Stop Conversion of an ADC Channel Group

<b>Initiator:</b>	CAS
<b>Date:</b>	18.10.2004
<b>Short Description:</b>	Start and stop conversion of an ADC channel group.
<b>Type:</b>	Changed: Merged with BSW12365. 17.11.2006 Removed user case.
<b>Importance:</b>	High
<b>Description:</b>	The ADC driver shall provide services to start and stop the conversion of an ADC Channel Group for all conversion modes.
<b>Rationale:</b>	Allow for software control of the start time of conversion of a group of channels.
<b>Use Case:</b>	--
<b>Dependencies:</b>	--
<b>Conflicts:</b>	--
<b>Supporting Material:</b>	--

#### 4.2.1.2.9 [BSW12292] Handling of Signed Values

<b>Initiator:</b>	BMW
<b>Date:</b>	25.09.2004
<b>Short Description:</b>	Handling of signed values.
<b>Type:</b>	New
<b>Importance:</b>	High
<b>Description:</b>	If the ADC provides signed values, the ADC driver shall put the sign bit into the MSB of the return value.
<b>Rationale:</b>	Allow mapping of ADC return values to standard AUTOSAR integer data types.
<b>Use Case:</b>	Some microcontrollers provide signed ADC values. Example: ADC register with 12 bit, sign bit in bit 11 (in a microcontroller with a "Big Endian" byte ordering).

<b>Dependencies:</b>	--
<b>Conflicts:</b>	--
<b>Supporting Material:</b>	BMW Specification MCAL V1.0a, MAL14.4.2.

#### 4.2.1.2.10 [BSW12288] ADC Stream Buffer Handling

<b>Initiator:</b>	BMW
<b>Date:</b>	25.09.2004
<b>Short Description:</b>	ADC stream buffer handling.
<b>Type:</b>	Changed: 06.07.2006 Reformulated the description. 17.11.2006 Reformulated the description.
<b>Importance:</b>	High
<b>Description:</b>	Based on the channel group configuration the ADC driver shall be able to handle the buffers of stream jobs in two different way: <ul style="list-style-type: none"> <li>As "linear buffers", i.e. the ADC driver stops the conversion as soon as the stream buffer is full (number of samples reached).</li> <li>As "circular buffers", i.e. the ADC driver continue the conversion even if the stream buffer is full (number of samples reached) by wrapping around the buffer itself.</li> </ul>
<b>Rationale:</b>	Allow cyclic streams of ADC conversion that run automatically.
<b>Use Case:</b>	Electric motor control.
<b>Dependencies:</b>	[BSW12280] ADC channel group results access mode.
<b>Conflicts:</b>	--
<b>Supporting Material:</b>	BMW Specification MCAL V1.0a.

#### 4.2.1.2.11 [BSW12802] Identify Most Recent Sample and Number of Available Samples

<b>Initiator:</b>	MM
<b>Date:</b>	20.05.2005
<b>Short Description:</b>	Identify most recent sample and number of available samples.
<b>Type:</b>	New
<b>Importance:</b>	High
<b>Description:</b>	The ADC driver shall provide (for streaming access mode) a service to identify most recent sample and number of available samples of a channel group. Passed parameters shall be: <ul style="list-style-type: none"> <li>ADC channel group.</li> </ul> Returned objects shall be: <ul style="list-style-type: none"> <li>Number of samples.</li> <li>Pointer to last sample.</li> </ul>
<b>Rationale:</b>	To identify which samples can be used.
<b>Use Case:</b>	Digital knock sensor acquisition.
<b>Dependencies:</b>	--
<b>Conflicts:</b>	--
<b>Supporting Material:</b>	--

#### 4.2.1.2.12 [BSW12823] Enable / Disable Hardware Triggers

<b>Initiator:</b>	WP4.2.2.1.12
<b>Date:</b>	21.08.2006
<b>Short Description:</b>	Enable / disable hardware triggers.
<b>Type:</b>	New
<b>Importance:</b>	High
<b>Description:</b>	The ADC driver shall provide services to enable and disable HW triggers for each channel group.
<b>Rationale:</b>	Give the possibility to ignore upcoming hardware triggers at run-time.
<b>Use Case:</b>	--
<b>Dependencies:</b>	[BSW12447] ADC channel group configuration. [BSW12817] Configuration of Group Trigger Sources.
<b>Conflicts:</b>	--
<b>Supporting Material:</b>	--

#### 4.2.1.2.13 [BSW12825] Structure of Result Buffer for Stream Conversion Mode

<b>Initiator:</b>	WP4.2.2.1.12
<b>Date:</b>	17.11.2006
<b>Short Description:</b>	Structure of result buffer for stream conversion mode.
<b>Type:</b>	New
<b>Importance:</b>	High
<b>Description:</b>	The results of the conversion of a channel group configured in streaming access mode shall be returned into a buffer of $n*m$ elements (integers), where $n$ is the number of channels belonging to the group, $m$ the number of samples acquired per channel. In this way the first $m$ elements belong to the first channel in the group, the second $m$ elements to the second channel and so on.
<b>Rationale:</b>	Support channel group streaming conversion mode.
<b>Use Case:</b>	--
<b>Dependencies:</b>	[BSW12280] ADC channel group results access mode.
<b>Conflicts:</b>	--
<b>Supporting Material:</b>	--

## 5 References

### 5.1 Deliverables of AUTOSAR

- [1] List of Basic Software Modules  
[https://svn2.autosar.org/repos2/22\\_Releases/AUTOSAR\\_Basic\\_Software\\_List.pdf](https://svn2.autosar.org/repos2/22_Releases/AUTOSAR_Basic_Software_List.pdf)
- [2] Layered Software Architecture  
[https://svn2.autosar.org/repos2/22\\_Releases/AUTOSAR\\_LayeredSoftwareArchitecture.pdf](https://svn2.autosar.org/repos2/22_Releases/AUTOSAR_LayeredSoftwareArchitecture.pdf)
- [3] General Requirements on Basic Software Modules  
[https://svn2.autosar.org/repos2/22\\_Releases/AUTOSAR\\_SRS\\_General.pdf](https://svn2.autosar.org/repos2/22_Releases/AUTOSAR_SRS_General.pdf)
- [4] General Requirements on SPAL  
[https://svn2.autosar.org/repos2/22\\_Releases/AUTOSAR\\_SRS\\_SPAL\\_General.pdf](https://svn2.autosar.org/repos2/22_Releases/AUTOSAR_SRS_SPAL_General.pdf)

### 5.2 Related standards and norms

- [5] HIS API I/O Driver Specification V2.1.3  
<http://www.automotive-his.de>