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$\triangle$						
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	4.1.1	Administration	Published parameter     EaMaximumBlockingTime deprecated			
			Configuration parameter EaIndex deprecated			
			Introduced parameter checks and corresponding DET errors			
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			Module short name changed			
			Check fpr NULL pointer added			
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			Description of return values clarified			
			Configuration variants clarified			
		AUTOSAR	Multiplicity of notification routines corected			
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			File include structure changed			
			Legal disclaimer revised			
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			EA_MAXIMUM_BLOCKING_TIME as published parameter			
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			Document meta information extended			
			Small layout adaptations made			





			File include structure updated
			API of initialization function adapted
			Range of EA block numbers adapted
2007-01-24	2.1.15	AUTOSAR Administration	Legal disclaimer revised
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			"Advice for users" revised
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2006-05-16	2.0	AUTOSAR Administration	Initial release



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# 1 Introduction and functional overview

This specification describes the functionality, API and configuration of the EEPROM Abstraction Layer (see Figure 1.1).

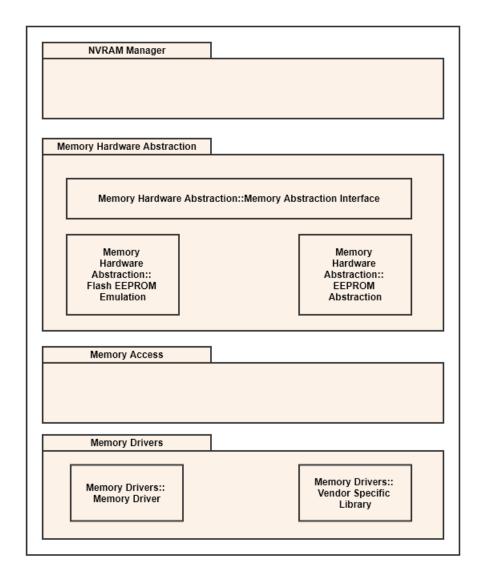


Figure 1.1: Module overview of memory hardware abstraction layer

The EEPROM Abstraction (EA) abstracts from the device specific addressing scheme and segmentation and provides the upper layers with a virtual addressing scheme and segmentation as well as a "virtually" unlimited number of erase cycles.



# 2 Acronyms and Abbreviations

Acronyms and abbreviations which have a local scope and therefore are not contained in the AUTOSAR glossary must appear in a local glossary.

Abbreviation / Acronym:	Description:
Address Area	Contiguous memory area in the logical address space
	Typically multiple physical memory sectors are combined to one
	logical address area.
EA	EEPROM Abstraction
EEPROM	Electrically Erasable and Programmable ROM (Read Only Memory)
FEE	Flash EEPROM Emulation
LSB	Least significant bit / byte (depending on context). Here it's bit.
Mem	Memory Driver
MemAcc	Memory Access
MemIf	Memory Abstraction Interface
MSB	Most significant bit / byte (depending on context). Here it's bit.
NvM	NVRAM Manager
NVRAM	Non-volatile RAM (Random Access Memory)
NVRAM block	Management unit as seen by the NVRAM Manager
(Logical) block	Smallest writable / erasable unit as seen by the modules user. Consists of one or more virtual pages.
Virtual page	May consist of one or several physical pages to ease handling of logical blocks and address calculation.
Internal residue	Unused space at the end of the last virtual page if the configured block size isn't an integer multiple of the virtual page size (see Figure 7.1).
Virtual address	Consisting of 16 bit block number and 16 bit offset inside the logical block.
Physical address	Address information in device specific format (depending on the underlying EEPROM driver and device) that is used to access a logical block.
Dataset	Concept of the NVRAM manager: A user addressable array of blocks of the same size.
	E.g. could be used to provide different configuration settings for the CAN driver (CAN IDs, filter settings,) to an ECU which has otherwise identical application software (e.g. door module).
Redundant copy	Concept of the NVRAM manager: Storing the same information twice to enhance reliability of data storage.

Table 2.1: Acronyms and abbreviations used in the scope of this Document



# 3 Related documentation

# 3.1 Input documents & related standards and norms

- [1] General Specification of Basic Software Modules AUTOSAR\_CP\_SWS\_BSWGeneral
- [2] Requirements on Memory Hardware Abstraction Layer AUTOSAR CP RS MemoryHWAbstractionLayer

# 3.2 Related specification

AUTOSAR provides a General Specification on Basic Software modules [1], which is also valid for EEEPROM Abstraction.

Thus, the specification SWS BSW General shall be considered as additional and required specification for EEEPROM Abstraction.



# 4 Constraints and assumptions

# 4.1 Limitations

No limitations.

# 4.2 Applicability to car domains

No restrictions.



# 5 Dependencies to other modules

This module depends on the capabilities of the underlying EEPROM driver as well as the configuration of the NVRAM manager.



# 6 Requirements Tracing

The following tables reference the requirements specified in [1] and [2], and links to the fulfillment of these. Please note that if column "Satisfied by" is empty for a specific requirement this means that this requirement is not fulfilled by this document.

Requirement	Description	Satisfied by
[RS_BRF_01048]	AUTOSAR module design shall support modules to cooperate in a multitasking environment	[SWS_Ea_00026] [SWS_Ea_00056] [SWS_Ea_00072] [SWS_Ea_00089] [SWS_Ea_00090] [SWS_Ea_00174]
[RS_BRF_01056]	AUTOSAR BSW modules shall provide standardized interfaces	[SWS_Ea_00097] [SWS_Ea_00098]
[RS_BRF_01064]	AUTOSAR BSW shall provide callback functions in order to access upper layer modules	[SWS_Ea_00051] [SWS_Ea_00054] [SWS_Ea_00055] [SWS_Ea_00094] [SWS_Ea_00141] [SWS_Ea_00142] [SWS_Ea_00143] [SWS_Ea_00144] [SWS_Ea_00145] [SWS_Ea_00146] [SWS_Ea_00153]
[RS_BRF_01812]	AUTOSAR non-volatile memory functionality shall support the prioritization and asynchronous execution of jobs	[SWS_Ea_00195]
[SRS_BSW_00101]	The Basic Software Module shall be able to initialize variables and hardware in a separate initialization function	[SWS_Ea_00017] [SWS_Ea_00084]
[SRS_BSW_00323]	All AUTOSAR Basic Software Modules shall check passed API parameters for validity	[SWS_Ea_00065] [SWS_Ea_00135] [SWS_Ea_00147] [SWS_Ea_00148] [SWS_Ea_00149] [SWS_Ea_00152] [SWS_Ea_00158] [SWS_Ea_00159] [SWS_Ea_00161] [SWS_Ea_00162] [SWS_Ea_00164] [SWS_Ea_00167] [SWS_Ea_00168] [SWS_Ea_00169] [SWS_Ea_00170] [SWS_Ea_00172] [SWS_Ea_00173] [SWS_Ea_00175] [SWS_Ea_00176]
[SRS_BSW_00373]	The main processing function of each AUTOSAR Basic Software Module shall be named according the defined convention	[SWS_Ea_00096]
[SRS_BSW_00385]	List possible error notifications	[SWS_Ea_00099] [SWS_Ea_00100]
[SRS_BSW_00392]	Parameters shall have a type	[SWS_Ea_00083] [SWS_Ea_00117]
[SRS_BSW_00406]	API handling in uninitialized state	[SWS_Ea_00035] [SWS_Ea_00128] [SWS_Ea_00130] [SWS_Ea_00131] [SWS_Ea_00132] [SWS_Ea_00134] [SWS_Ea_00136] [SWS_Ea_00171] [SWS_Ea_00178]
[SRS_BSW_00407]	Each BSW module shall provide a function to read out the version information of a dedicated module implementation	[SWS_Ea_00092]
[SRS_BSW_00414]	Init functions shall have a pointer to a configuration structure as single parameter	[SWS_Ea_00190] [SWS_Ea_00191]
[SRS_MemHwAb 14001]	The FEE and EA modules shall allow the configuration of the alignment of the start and end addresses of logical blocks	[SWS_Ea_00005] [SWS_Ea_00068] [SWS_Ea_00075] [SWS_Ea_00137]





Requirement	Description	Satisfied by
[SRS_MemHwAb 14002]	The FEE and EA modules shall allow the configuration of a required number of write cycles for each logical block	[SWS_Ea_00080]
[SRS_MemHwAb 14005]	The FEE and EA modules shall provide upper layer modules with a virtual 32bit address space	[SWS_Ea_00066] [SWS_Ea_00075]
[SRS_MemHwAb 14006]	The start address for a block erase or write operation shall always be aligned to the virtual 64K boundary	[SWS_Ea_00024]
[SRS_MemHwAb 14007]	The start address and length for reading a block shall not be limited to a certain alignment	[SWS_Ea_00021]
[SRS_MemHwAb 14009]	The FEE and EA modules shall provide a conversion between the logical linear addresses and the physical memory addresses	[SWS_Ea_00007] [SWS_Ea_00021] [SWS_Ea_00024] [SWS_Ea_00036] [SWS_Ea_00063]
[SRS_MemHwAb 14010]	The FEE and EA modules shall provide a write service that operates only on complete configured logical blocks	[SWS_Ea_00087] [SWS_Ea_00151] [SWS_Ea_00159] [SWS_Ea_00181]
[SRS_MemHwAb 14012]	Spreading of write access	[SWS_Ea_00079]
[SRS_MemHwAb 14013]	Writing of immediate data shall not be delayed by internal management operations nor by erasing the memory area to be written to	[SWS_Ea_00025]
[SRS_MemHwAb 14014]	The FEE and EA modules shall detect possible data inconsistencies due to aborted / interrupted write operations	[SWS_Ea_00046] [SWS_Ea_00047] [SWS_Ea_00188] [SWS_Ea_00189]
[SRS_MemHwAb 14015]	The FEE and EA modules shall report possible data inconsistencies	[SWS_Ea_00104]
[SRS_MemHwAb 14016]	The FEE and EA modules shall not return inconsistent data to the caller	[SWS_Ea_00104]
[SRS_MemHwAb 14026]	The block numbers 0x0000 and 0x FFFF shall not be used	[SWS_Ea_00006]
[SRS_MemHwAb 14028]	The FEE and EA modules shall provide a service to invalidate a logical block	[SWS_Ea_00037] [SWS_Ea_00074] [SWS_Ea_00091] [SWS_Ea_00194]
[SRS_MemHwAb 14029]	The FEE and EA modules shall provide a read service that allows reading all or part of a logical block	[SWS_Ea_00022] [SWS_Ea_00086] [SWS_Ea_00158] [SWS_Ea_00179]
[SRS_MemHwAb 14031]	The FEE and EA modules shall provide a service that allows canceling an ongoing asynchronous operation	[SWS_Ea_00077] [SWS_Ea_00078] [SWS_Ea_00088] [SWS_Ea_00160]
[SRS_MemHwAb 14032]	The FEE and EA modules shall provide an erase service that operates only on complete logical blocks containing immediate data	[SWS_Ea_00063] [SWS_Ea_00064] [SWS_Ea_00065] [SWS_Ea_00093] [SWS_Ea_00104]

**Table 6.1: Requirements Tracing** 



# 7 Functional specification

#### 7.1 General behavior

#### [SWS Ea 00137]

Upstream requirements: SRS\_MemHwAb\_14001

The EEPROM Abstraction (EA) shall only accept one job at a time, i.e. the module shall not provide a queue for pending jobs (that's the job of the NVRAM Manager).

Note: Since the NvM is the only caller for this module and in order to keep this module reasonably small, the modules functions shall not check, whether the module is currently busy or not. It is the responsibility of the NvM to serialize the pending jobs and only start a new job after the previous one has been finished or canceled.

#### 7.1.1 Addressing scheme and segmentation

The EEPROM Abstraction (EA) provides upper layers with a 32bit virtual linear address space and uniform segmentation scheme. This virtual 32bit addresses consists of

- a 16bit block number allowing a (theoretical) number of 65536 logical blocks
- a 16bit block offset allowing a (theoretical) block size of 64Kbyte per block

The 16bit block number represents a configurable (virtual) paging mechanism. The values for this address alignment can be derived from that of the underlying EEPROM driver and device. This virtual paging is configurable via the parameter EA\_VIRTUAL\_PAGE\_SIZE.

#### [SWS Ea 00075]

Upstream requirements: SRS\_MemHwAb\_14001, SRS\_MemHwAb\_14005

The configuration of the Ea module shall be such that the virtual page size (defined in EA\_VIRTUAL\_PAGE\_SIZE) is an integer multiple of the physical page size, i.e. it is not allowed to configure a smaller virtual page than the actual physical page size.

#### Example:

The size of a virtual page is configured to be eight bytes, thus the address alignment is eight bytes. The logical block with block number 1 is placed at physical address x. The logical block with the block number 2 then would be placed at x+8, block number 3 would be placed at x+16.

Note: This specification requirement allows the physical start address of a logical block to be calculated rather than making a lookup table necessary for the address mapping.



#### [SWS Ea 00005]

Upstream requirements: SRS\_MemHwAb\_14001

[Each configured logical block shall take up an integer multiple of the configured virtual page size EaVirtualPageSize.]

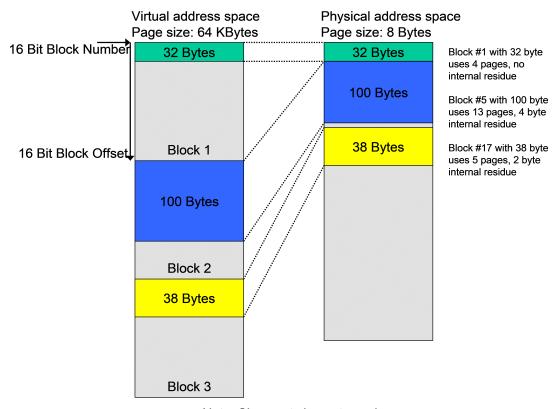
Example: If the virtual page size is configured to be eight bytes, logical blocks can be of size 8, 16, 24, 32, ... bytes but not e.g. 10, 20, 50, ... bytes.

### [SWS Ea 00068]

Upstream requirements: SRS\_MemHwAb\_14001

[Logical blocks must not overlap each other and must not be contained within one another.]

Example: The address alignment / virtual paging is configured to be eight bytes by setting the parameter EA\_VIRTUAL\_PAGE\_SIZE accordingly. The logical block number 1 is configured to have a size of 32 bytes (see Figure 7.1). This logical block would use exactly 4 virtual pages. The next logical block thus would get the block number 5, since block numbers 2, 3 and 4 are "blocked" by the first logical block. This second block is configured to have a size of 100 bytes, taking up 13 virtual pages and leaving 4 bytes of the last page unused. The next available logical block number thus would be 17.



Note: Sizes not shown to scale

Figure 7.1: Virtual vs. physical memory layout



#### [SWS Ea 00006]

Upstream requirements: SRS\_MemHwAb\_14026

[The block numbers 0x0000 and 0xFFFF shall not be configurable for a logical block EaBlockNumber.]

#### 7.1.2 Address calculation

#### [SWS Ea 00007]

Upstream requirements: SRS\_MemHwAb\_14009

[Depending on the implementation of the EA module and the exact address format used, the functions of the EA module shall combine the 16bit block number and 16bit block offset to derive the physical EEPROM address needed for the underlying EEP-ROM driver.]

Note: The exact address format needed by the underlying EEPROM driver and therefore the mechanism how to derive the physical EEPROM address from the given 16bit block number and 16bit block offset depends on the EEPROM device and the implementation of the EEPROM device driver and can therefore not be specified in this document.

#### [SWS Ea 00066]

Upstream requirements: SRS MemHwAb 14005

[Only those bits of the 16bit block number, that do not denote a specific dataset or redundant copy shall be used for address calculation.]

Note: Since this information is needed by the NVRAM manager, the number of bits to encode this can be configured for the NVRAM manager with the parameter NVM\_DATASET\_SELECTION\_BITS.

Example: Dataset information is configured to be encoded in the four LSB's of the 16bit block number (allowing for a maximum of 16 datasets per NVRAM block and a total of 4094 NVRAM blocks). An implementer decides to store all datasets of a logical block directly adjacent and using the length of the block and a pointer to access each dataset. To calculate the start address of the block (the address of the first dataset) she/he uses only the 12 MSB's, to access a specific dataset she/he adds the size of the block multiplied by the dataset index (the four LSB's) to this start address (Figure 7.2).



NVM\_DATASET\_SELECTION\_BITS configured to be four (bits), leaving twelve bit for the block number. Each logical block thus can be subdivided in up to 16 datasets.

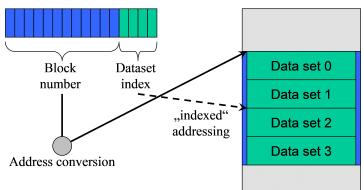


Figure 7.2: Block number and dataset index

#### 7.1.3 Limitation of erase / write cycles

#### [SWS Ea 00079]

Upstream requirements: SRS MemHwAb 14012

The configuration of the Ea module shall define the expected number of erase/write cycles for each logical block in the configuration parameter EaNumberOfWriteCycles.

#### [SWS Ea 00080]

Upstream requirements: SRS\_MemHwAb\_14002

[If the underlying EEPROM device or device driver does not provide at least the configured number of erase/write cycles per physical memory cell (given in the parameter EepAllowedWriteCycles), the EA module shall provide mechanisms to spread the erase/ write access such that the physical device is not overstressed. This shall also apply to all management data used internally by the EA module.

Example: The logical block number 1 is configured for an expected 500.000 write cycles, the underlying EEPROM device and device driver are only specified for 100.000 erase cycles. In this case the EA module has to provide (at least) five separate memory areas and alternate the access between those areas internally, so that each physical memory location is only erased for a maximum of the specified 100.000 cycles.

#### 7.1.4 Handling of "immediate" data

Blocks, containing immediate data, have to be written instantaneously, i.e. such blocks shall be writable without the need, to first erase the corresponding memory area (e.g.



by using pre-erased memory). An ongoing lower priority read / erase / write or compare job shall be canceled by the NVRAM manager before immediate data is written.

Note: A running operation on the hardware (e.g. writing one page or erasing one sector) can usually not be aborted once it has been started. The maximum time of the longest hardware operation thus has to be accepted as delay even for immediate data.

Example: Three blocks with 10 bytes each have been configured for immediate data. The EA module / configuration tool reserves these 30 bytes (plus the implementation specific overhead per block / page if needed) for use by this immediate data only. That is this memory area shall not be used for storage of other data blocks.

Now, the NVRAM manager has requested the EA module to write a data block of 100 bytes. While this block is being written a situation occurs that one (or several) of the immediate data blocks need to be written. Therefore the NVRAM manager cancels the ongoing write request and subsequently issues the write request for the (first) block containing immediate data. The cancelation of the ongoing write request is performed synchronously by the EA module and the underlying EEPROM driver that is the write request for the immediate data can be started without any further delay. However, before the first bytes of immediate data can be written, the EA module respectively the underlying EEPROM driver have to wait for the end of an ongoing hardware access from the previous write request (e.g. writing of a page, erasing of a sector, transfer via SPI, ...).

#### 7.1.5 Managing block consistency information

# [SWS\_Ea\_00046]

Upstream requirements: SRS\_MemHwAb\_14014

The Ea module shall manage for each block the information, whether this block is "correct" from the point of view of the EA module or not. This consistency information shall only concern the internal handling of the block, not the block's contents.

#### [SWS Ea 00047]

Upstream requirements: SRS MemHwAb 14014

[When a block write operation is started the EA module shall mark the corresponding block as inconsistent <sup>1</sup>. Upon the successful end of the block write operation, the EA module shall mark the block as consistent (again).]

Note: This internal management information should not be mixed up with the validity information of a block which can be manipulated by using the Ea\_InvalidateBlock ser-

<sup>&</sup>lt;sup>1</sup>This does not necessarily mean a write operation on the physical device. If there are other means to detect the consistency of a logical block, changing the management information stored with the block shall be avoided.



vice, i.e. the EA module shall be able to distinguish between an inconsistent block and a block that has been deliberately invalidated by the upper layer.

# 7.1.6 Buffer Alignment

**[SWS\_Ea\_00197]** [The Ea shall align internal buffers to the EaBufferAlignmentValue Ref. |

[SWS\_Ea\_00198] [The Ea shall align read request to the EaMinimumReadPageSize.]

# 7.2 Error Classification

# 7.2.1 Development Errors

# [SWS\_Ea\_91001] Definiton of development errors in module Ea [

Type of error	Related error code	Error value
API service called while module is not (yet) initialized	EA_E_UNINIT	0x01
API service called with invalid block number	EA_E_INVALID_BLOCK_NO	0x02
API service called with invalid block offset	EA_E_INVALID_BLOCK_OFS	0x03
API service called with invalid pointer argument	EA_E_PARAM_POINTER	0x04
API service called with invalid block length information	EA_E_INVALID_BLOCK_LEN	0x05

# 7.2.2 Runtime Errors

# [SWS\_Ea\_91002] Definiton of runtime errors in module Ea [

Type of error	Related error code	Error value
API service called while module is busy	EA_E_BUSY	0x06
Ea_Cancel called while no job was pending	EA_E_INVALID_CANCEL	0x08



# 7.2.3 Production Errors

There are no production errors.

# 7.2.4 Extended Production Errors

There are no extended production errors.



# 8 API specification

# 8.1 Imported types

# [SWS\_Ea\_00083] Definition of imported datatypes of module Ea

Upstream requirements: SRS\_BSW\_00392

Γ

Module	Header File	Imported Type
MemAcc	MemAcc_GeneralTypes.h	MemAcc_AddressArealdType
	MemAcc_GeneralTypes.h	MemAcc_AddressType
	MemAcc_GeneralTypes.h	MemAcc_DataType
	MemAcc_GeneralTypes.h	MemAcc_JobResultType
	MemAcc_GeneralTypes.h	MemAcc_LengthType
MemIf	Memlf.h	MemIf_JobResultType
	Memlf.h	MemIf_StatusType
Std	Std_Types.h	Std_ReturnType
	Std_Types.h	Std_VersionInfoType

١

# [SWS\_Ea\_00117]

Upstream requirements: SRS\_BSW\_00392

[The types mentioned in SWS\_Ea\_00083 shall not be changed or extended for a specific EA module or hardware platform.]

# 8.2 Type definitions

# [SWS\_Ea\_00190] Definition of datatype Ea\_ConfigType

Upstream requirements: SRS\_BSW\_00414

Γ

Name	Ea_ConfigType		
Kind	Structure	Structure	
Elements	implementation specific		
	Type –		
	Comment	-	
Description	Configuration data structure of the Ea module.		
Available via	Ea.h		

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#### 8.3 Function definitions

#### 8.3.1 Ea Init

#### [SWS Ea 00084] Definition of API function Ea Init

Upstream requirements: SRS BSW 00101

Γ

Service Name	Ea_Init	Ea_Init	
Syntax	<pre>void Ea_Init (     const Ea_ConfigTy )</pre>	<pre>void Ea_Init (    const Ea_ConfigType* ConfigPtr )</pre>	
Service ID [hex]	0x00	0x00	
Sync/Async	Synchronous	Synchronous	
Reentrancy	Non Reentrant	Non Reentrant	
Parameters (in)	ConfigPtr	ConfigPtr Pointer to the selected configuration set.	
Parameters (inout)	None	None	
Parameters (out)	None	None	
Return value	None	None	
Description	Initializes the EEPROM a	Initializes the EEPROM abstraction module.	
Available via	Ea.h		

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#### [SWS Ea 00191]

Upstream requirements: SRS\_BSW\_00414

[The configuration pointer ConfigPtr shall always have a NULL PTR value.]

Note: the Configuration pointer ConfigPtr is currently not used and shall therefore be set NULL\_PTR value.

### [SWS\_Ea\_00017]

Upstream requirements: SRS\_BSW\_00101

[The function Ea\_Init shall set the module state from MEMIF\_UNINIT to MEMIF\_BUSY\_INTERNAL once it starts the module's initialization.]

#### [SWS Ea 00128]

Upstream requirements: SRS\_BSW\_00406

[If initialization is finished within Ea\_Init, the function Ea\_Init shall set the module state from MEMIF\_BUSY\_INTERNAL to MEMIF\_IDLE once initialization has been successfully finished.]



Note: The Ea module's environment shall not call the function Ea\_Init during a running operation of the EA module.

### 8.3.2 Ea Read

#### [SWS Ea 00086] Definition of API function Ea Read

Upstream requirements: SRS\_MemHwAb\_14029

Γ

Service Name	Ea_Read		
Syntax	<pre>Std_ReturnType Ea_Read (    uint16 BlockNumber,    uint16 BlockOffset,    uint8* DataBufferPtr,    uint16 Length )</pre>		
Service ID [hex]	0x02		
Sync/Async	Asynchronous	Asynchronous	
Reentrancy	Non Reentrant	Non Reentrant	
Parameters (in)	BlockNumber	Number of logical block, also denoting start address of that block in EEPROM.	
	BlockOffset	BlockOffset Read address offset inside the block	
	Length	Length Number of bytes to read	
Parameters (inout)	None	None	
Parameters (out)	DataBufferPtr	DataBufferPtr Pointer to data buffer	
Return value	Std_ReturnType	E_OK: The requested job has been accepted by the module.  E_NOT_OK: The requested job has not been accepted by the EA module.	
Description	Reads Length bytes of blo	Reads Length bytes of block Blocknumber at offset BlockOffset into the buffer DataBufferPtr.	
Available via	Ea.h		

#### [SWS Ea 00021]

Upstream requirements: SRS\_MemHwAb\_14007, SRS\_MemHwAb\_14009

[The function Ea\_Read shall take the block number and offset and calculate the corresponding memory read address.]

Note: The address offset and length parameter can take any value within the given types range, this allows reading of an arbitrary number of bytes from an arbitrary address inside a logical block.

#### [SWS\_Ea\_00072]

Upstream requirements: RS\_BRF\_01048

The EA module shall execute the read operation asynchronously within the EA module's main function.



#### [SWS Ea 00022]

Upstream requirements: SRS\_MemHwAb\_14029

[If the current module status is MEMIF\_IDLE or if the current module status is MEMIF\_BUSY INTERNAL, the function Ea\_Read shall accept the read request, copy the given / computed parameters to module internal variables, initiate a read job, set the EA module status to MEMIF\_BUSY, set the job result to MEMIF\_JOB\_PENDING and return with E OK.|

#### [SWS Ea 00179]

Upstream requirements: SRS\_MemHwAb\_14029

[If the current module status is MEMIF\_UNINIT or MEMIF\_BUSY, the function Ea\_Read shall reject the job request and return with E\_NOT\_OK.]

#### [SWS\_Ea\_00130]

Upstream requirements: SRS\_BSW\_00406

[If development error detection for the module EA is enabled: the function Ea\_Read shall check if the module state is MEMIF\_UNINIT. If this is the case, the function Ea\_Read shall reject the read request, raise the development error EA\_E\_UNINIT and return with E\_NOT\_OK.|

#### [SWS\_Ea\_00167]

Upstream requirements: SRS\_BSW\_00323

[The function Ea\_Read shall check if the module state is MEMIF\_BUSY. If this is the case, the function Ea\_Read shall reject the read request, raise the runtime error EA\_ E BUSY and return with E NOT OK.|

#### [SWS Ea 00147]

Upstream requirements: SRS\_BSW\_00323

[If development error detection is enabled for the module: the function Ea\_Read shall check whether the given block number is valid (i.e. inside the configured range). If this is not the case, the function Ea\_Read shall reject the read request, raise the development error EA\_E\_INVALID\_BLOCK\_NO and return E\_NOT\_OK.]

#### [SWS Ea 00168]

Upstream requirements: SRS BSW 00323

[If development error detection is enabled for the module: the function Ea\_Read shall check that the given block offset is valid (i.e. that it is less than the block length configured for this block). If this is not the case, the function Ea\_Read shall reject the read request, raise the development error EA\_E\_INVALID\_BLOCK\_OFS and return with E\_NOT\_OK.]



#### [SWS Ea 00169]

Upstream requirements: SRS\_BSW\_00323

[If development error detection is enabled for the module: the function Ea\_Read shall check that the given length information is valid, i.e. that the requested length information plus the block offset do not exceed the block end address (block start address plus configured block length). If this is not the case, the function Ea\_Read shall reject the read request, raise the development error EA\_E\_INVALID\_BLOCK\_LEN and return with E\_NOT\_OK.|

#### [SWS Ea 00170]

Upstream requirements: SRS\_BSW\_00323

[If development error detection is enabled for the module: the function Ea\_Read shall check that the given data pointer is valid (i.e. that it is not NULL). If this is not the case, the function Ea\_Read shall reject the read request, raise the development error EA\_ E\_PARAM\_POINTER and return with E\_NOT\_OK.]

### [SWS\_Ea\_00158]

Upstream requirements: SRS\_MemHwAb\_14029, SRS\_BSW\_00323

[If a read request is rejected by the function Ea\_Read, i.e. requirements SWS\_Ea\_00130, SWS\_Ea\_00147, SWS\_Ea\_00167, SWS\_Ea\_00168, SWS\_Ea\_00169, SWS\_Ea\_00170 or SWS\_Ea\_00179 apply, the function Ea\_Read shall not change the current module status or job result.]

#### 8.3.3 Ea Write

# [SWS\_Ea\_00087] Definition of API function Ea\_Write

Upstream requirements: SRS\_MemHwAb\_14010

Γ

Service Name	Ea_Write	Ea_Write	
Syntax	Std_ReturnType Ea_Write (    uint16 BlockNumber,    const uint8* DataBufferPtr )		
Service ID [hex]	0x03		
Sync/Async	Asynchronous		
Reentrancy	Non Reentrant		
Parameters (in)	BlockNumber	Number of logical block, also denoting start address of that block in EEPROM.	
	DataBufferPtr Pointer to data buffer		
Parameters (inout)	None		





Parameters (out)	None	
Return value	Std_ReturnType	E_OK: The requested job has been accepted by the module. E_NOT_OK: The requested job has not been accepted by the EA module.
Description	Writes the contents of the DataBufferPtr to the block BlockNumber.	
Available via	Ea.h	

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#### [SWS Ea 00024]

Upstream requirements: SRS\_MemHwAb\_14006, SRS\_MemHwAb\_14009

The function Ea\_Write shall take the block number and calculate the corresponding memory write address. The block offset shall be fixed to zero for this address calculation.

#### [SWS Ea 00151]

Upstream requirements: SRS\_MemHwAb\_14010

The function Ea\_Write shall set the length parameter for the write job to the length configured for this logical block.

#### [SWS\_Ea\_00025]

Upstream requirements: SRS\_MemHwAb\_14013

[If the current module status is MEMIF\_IDLE or if the current module status is MEMIF\_BUSY INTERNAL, the function Ea\_Write shall accept the write request, copy the given / computed parameters to module internal variables, initiate a write job, set the EA module status to MEMIF\_BUSY, set the job result to MEMIF\_JOB\_PENDING and return with E\_OK.]

#### [SWS Ea 00181]

Upstream requirements: SRS\_MemHwAb\_14010

[If the current module status is MEMIF\_UNINIT or MEMIF\_BUSY, the function Ea\_ Write shall reject the job request and return with E\_NOT\_OK.]

#### [SWS Ea 00026]

Upstream requirements: RS\_BRF\_01048

The EA module shall execute the write job of the function Ea\_Write asynchronously within the EA module's main function.



### [SWS Ea 00131]

Upstream requirements: SRS\_BSW\_00406

[If development error detection for the module EA is enabled: the function Ea\_Write shall check if the module state is MEMIF\_UNINIT. If this is the case, the function Ea\_Write shall reject the write request, raise the development error EA\_E\_UNINIT and return with E\_NOT\_OK.]

#### [SWS Ea 00171]

Upstream requirements: SRS\_BSW\_00406

The function Ea\_Write shall check if the module state is MEMIF\_BUSY. If this is the case, the function Ea\_Write shall reject the write request, raise the runtime error EA\_ E\_BUSY and return with E\_NOT\_OK.

#### [SWS Ea 00148]

Upstream requirements: SRS\_BSW\_00323

[If development error detection for the module EA is enabled: the function Ea\_Write shall check whether the given block number is valid (i.e. inside the configured range). If this is not the case, the function Ea\_Write shall reject the write request, raise the development error EA\_E\_INVALID\_BLOCK\_NO and return with E\_NOT\_OK.|

#### [SWS Ea 00172]

Upstream requirements: SRS\_BSW\_00323

[If development error detection is enabled for the module: the function Ea\_Write shall check that the given data pointer is valid (i.e. that it is not NULL). If this is not the case, the function Ea\_Write shall reject the write request, raise the development error EA\_E\_PARAM\_POINTER and return with E\_NOT\_OK.]

#### [SWS Ea 00159]

Upstream requirements: SRS\_MemHwAb\_14010, SRS\_BSW\_00323

[If a write request is rejected by the function Ea\_Write, i.e. requirements SWS\_Ea\_00131, SWS\_Ea\_00171, SWS\_Ea\_00148, SWS\_Ea\_00172 or SWS\_Ea\_00181 apply, the function Ea\_Write shall not change the current module status or job result.



#### 8.3.4 Ea Cancel

### [SWS Ea 00088] Definition of API function Ea Cancel

Upstream requirements: SRS MemHwAb 14031

Γ

Service Name	Ea_Cancel
Syntax	void Ea_Cancel (
	void
	)
Service ID [hex]	0x04
Sync/Async	Asynchronous
Reentrancy	Non Reentrant
Parameters (in)	None
Parameters (inout)	None
Parameters (out)	None
Return value	None
Description	Cancels the ongoing asynchronous operation.
Available via	Ea.h

# [SWS\_Ea\_00132]

Upstream requirements: SRS\_BSW\_00406

[If development error detection for the module EA is enabled: the function Ea\_Cancel shall check if the module state is MEMIF\_UNINIT. If this is the case, the function Ea\_Cancel shall raise the development error EA\_E\_UNINIT and return to the caller without changing any internal variables.]

### [SWS\_Ea\_00077]

Upstream requirements: SRS\_MemHwAb\_14031

[If the current module status is MEMIF\_BUSY (i.e. the request to cancel a pending job is accepted by the function Ea\_Cancel), the function Ea\_Cancel shall call the cancel function of the underlying EEPROM driver.

#### [SWS\_Ea\_00078]

Upstream requirements: SRS MemHwAb 14031

[If the current module status is MEMIF\_BUSY (i.e. the request to cancel a pending job is accepted by the function Ea\_Cancel), the function Ea\_Cancel shall reset the EA module's internal variables to make the module ready for a new job request. I.e. the function Ea\_Cancel shall set the job result to MEMIF\_JOB\_CANCELED and the module status to MEMIF\_IDLE.]



#### [SWS Ea 00160]

Upstream requirements: SRS\_MemHwAb\_14031

[If the current module status is not MEMIF\_BUSY (i.e. the request to cancel a pending job is rejected by the function Ea\_Cancel), the function Ea\_Cancel shall not change the current module status or job result.

### [SWS Ea 00173]

Upstream requirements: SRS\_BSW\_00323

[If the current module status is not MEMIF\_BUSY (i.e. there is no job to cancel and therefore the request to cancel a pending job is rejected by the function Ea\_Cancel), the function Ea\_Cancel shall raise the runtime error EA\_E\_INVALID\_CANCEL.

#### 8.3.5 Ea GetStatus

### [SWS\_Ea\_00089] Definition of API function Ea\_GetStatus

Upstream requirements: RS\_BRF\_01048

Γ

Service Name	Ea_GetStatus	
Syntax	<pre>MemIf_StatusType Ea_(    void )</pre>	GetStatus (
Service ID [hex]	0x05	
Sync/Async	Synchronous	
Reentrancy	Non Reentrant	
Parameters (in)	None	
Parameters (inout)	None	
Parameters (out)	None	
Return value	MemIf_StatusType	MEMIF_UNINIT: The EA module has not been initialized (yet).  MEMIF_IDLE: The EA module is currently idle.  MEMIF_BUSY: The EA module is currently busy.  MEMIF_BUSY_INTERNAL: The EA module is currently busy with internal management operations.
Description	Service to return the Status	
Available via	Ea.h	

**[SWS\_Ea\_00034]** [The function Ea\_GetStatus shall return MEMIF\_UNINIT if the module has not (yet) been initialized.

**[SWS\_Ea\_00156]** [The function Ea\_GetStatus shall return MEMIF\_IDLE if the module is neither processing a request from the upper layer nor is it doing an internal management operation.]



**[SWS\_Ea\_00157]** [The function Ea\_GetStatus shall return MEMIF\_BUSY if it is currently processing a request from the upper layer.]

**[SWS\_Ea\_00073]** [The function Ea\_GetStatus shall return MEMIF\_BUSY\_INTER-NAL, if an internal management operation is currently ongoing.]

Note: Internal management operation may e.g. be a re-organization of the used EEP-ROM memory (garbage collection). This may imply that the underlying device driver is - at least temporarily - busy.

#### 8.3.6 Ea GetJobResult

# [SWS\_Ea\_00090] Definition of API function Ea\_GetJobResult

Upstream requirements: RS\_BRF\_01048

Γ

Service Name	Ea_GetJobResult	
Syntax	<pre>MemIf_JobResultType Ea_GetJobResult (    void )</pre>	
Service ID [hex]	0x06	
Sync/Async	Synchronous	
Reentrancy	Non Reentrant	
Parameters (in)	None	
Parameters (inout)	None	
Parameters (out)	None	
Return value	MemIf_JobResultType	MEMIF_JOB_OK: The last job has been finished successfully.  MEMIF_JOB_PENDING: The last job is waiting for execution or currently being executed.  MEMIF_JOB_CANCELED: The last job has been canceled (which means it failed).  MEMIF_JOB_FAILED: The last job was not finished successfully (it failed).  MEMIF_BLOCK_INCONSISTENT: The requested block is inconsistent, it may contain corrupted data.  MEMIF_BLOCK_INVALID: The requested block has been invalidated, the requested operation can not be performed.
Description	Service to return the JobResult.	
Available via	Ea.h	

#### 

#### [SWS\_Ea\_00134]

Upstream requirements: SRS\_BSW\_00406

[If development error detection for the module EA is enabled: the function Ea\_Get JobResult shall check if the module state is MEMIF\_UNINIT. If this is the case, the



function Ea\_GetJobResult shall raise the development error EA\_E\_UNINIT and return with MEMIF\_JOB\_FAILED.|

# [SWS Ea 00035]

Upstream requirements: SRS BSW 00406

The function Ea\_GetJobResult shall return the status of the last job requested by the NVRAM manager.

### [SWS Ea 00174]

Upstream requirements: RS\_BRF\_01048

[Only those jobs which have been requested directly by the upper layer shall have influence on the job result returned by the function Ea\_GetJobResult. I.e. jobs which are issued by the EA module itself in the course of internal management operations shall not alter the job result.]

Note: To facilitate this, the EA module may have to implement a second set of local variables to store the data for internal jobs.

Note: Internal management operations (e.g. "garbage collection") will only be invoked in the context of jobs requested from the NvM. Whether they have to be done before or after the requested job is the decision of the modules implementor and shall not be detailed in this specification.

### 8.3.7 Ea\_InvalidateBlock

#### [SWS Ea 00091] Definition of API function Ea InvalidateBlock

Upstream requirements: SRS MemHwAb 14028

Γ

Service Name	Ea_InvalidateBlock	
Syntax	<pre>Std_ReturnType Ea_InvalidateBlock (   uint16 BlockNumber )</pre>	
Service ID [hex]	0x07	
Sync/Async	Asynchronous	
Reentrancy	Non Reentrant	
Parameters (in)	BlockNumber	Number of logical block, also denoting start address of that block in EEPROM.
Parameters (inout)	None	
Parameters (out)	None	





Return value	Std_ReturnType	E_OK: The requested job has been accepted by the module.  E_NOT_OK - only if DET is enabled: The requested job has not been accepted by the EA module.
Description	Invalidates the block BlockNumber.	
Available via	Ea.h	

#### [SWS Ea 00036]

Upstream requirements: SRS\_MemHwAb\_14009

The function Ea\_InvalidateBlock shall take the block number and calculate the corresponding memory block address.

#### [SWS Ea 00037]

Upstream requirements: SRS\_MemHwAb\_14028

[Depending on implementation, the function Ea\_InvalidateBlock shall invalidate the block <BlockNumber> by either calling the erase function of the underlying device driver or changing some module internal management information accordingly.

Note: How exactly the requested block is invalidated depends on the module's implementation and will not be further detailed in this specification. The internal management information has to be stored in NV memory since it has to be resistant against resets. What this information is and how it is stored is not further detailed by this specification.

#### [SWS Ea 00135]

Upstream requirements: SRS BSW 00323

[If development error detection for the module Ea is enabled: the function Ea\_InvalidateBlock shall check if the module state is MEMIF\_UNINIT. If this is the case, the function Ea\_InvalidateBlock shall reject the invalidation request, raise the development error EA\_E\_UNINIT and return with E\_NOT\_OK.]

#### [SWS Ea 00175]

Upstream requirements: SRS\_BSW\_00323

[The function Ea\_InvalidateBlock shall check if the module state is MEMIF\_BUSY. If this is the case, the function Ea\_InvalidateBlock shall reject the invalidation request, raise the runtime error EA E BUSY and return with E NOT OK.]



### [SWS Ea 00194]

Upstream requirements: SRS\_MemHwAb\_14028

The function Ea\_InvalidateBlock shall check if the module state is MEMIF\_IDLE or MEMIF\_BUSY\_INTERNAL. If this is the case the module shall accept the invalidation request, set the Ea module status to MEMIF\_BUSY, set the job result to MEMIF\_JOB\_PENDING and return E OK to the caller.

#### [SWS Ea 00195]

Upstream requirements: RS\_BRF\_01812

The Ea module shall execute the block invalidation request asynchronously within the Ea module's main function.

### [SWS Ea 00149]

Upstream requirements: SRS\_BSW\_00323

[If development error detection for the module EA is enabled: the function Ea\_InvalidateBlock shall check whether the given block number is valid (i.e. it has been configured). If this is not the case, the function Ea\_InvalidateBlock shall reject the request, raise the development error EA\_E\_INVALID\_BLOCK\_NO and return with E\_NOT\_OK.

### [SWS\_Ea\_00161]

Upstream requirements: SRS\_BSW\_00323

[If an invalidation request is rejected by the function Ea\_InvalidateBlock, i.e. requirements SWS\_Ea\_00135, SWS\_Ea\_00149 or SWS\_Ea\_00175 apply, the function Ea\_InvalidateBlock shall not change the current module status or job result.

#### 8.3.8 Ea GetVersionInfo

#### [SWS Ea 00092] Definition of API function Ea GetVersionInfo

Upstream requirements: SRS\_BSW\_00407

Γ

Service Name	Ea_GetVersionInfo	
Syntax	void Ea_GetVersionInfo ( Std_VersionInfoType* VersionInfoPtr )	
Service ID [hex]	0x08	
Sync/Async	Synchronous	
Reentrancy	Reentrant	





Parameters (in)	None		
Parameters (inout)	None		
Parameters (out)	VersionInfoPtr	Pointer to standard version information structure.	
Return value	None		
Description	Service to get the version information of this module.		
Available via	Ea.h		

# [SWS Ea 00164]

Upstream requirements: SRS\_BSW\_00323

[If development error detection for the module EA is enabled: the function EA\_Get VersionInfo shall check that the given data pointer is valid (i.e. that it is not NULL). If this is not the case, the function Ea\_GetVersionInfo shall raise the development error EA\_E\_PARAM\_POINTER.|

# 8.3.9 Ea\_EraseImmediateBlock

# [SWS\_Ea\_00093] Definition of API function Ea\_EraseImmediateBlock

Upstream requirements: SRS MemHwAb 14032

Service Name	Ea_EraseImmediateBlock		
Syntax	Std_ReturnType Ea_EraseImmediateBlock (    uint16 BlockNumber )		
Service ID [hex]	0x09		
Sync/Async	Asynchronous		
Reentrancy	Non Reentrant		
Parameters (in)	BlockNumber	Number of logical block, also denoting start address of that block in EEPROM.	
Parameters (inout)	None		
Parameters (out)	None		
Return value	Std_ReturnType	E_OK: The requested job has been accepted by the module.  E_NOT_OK - only if DET is enabled: The requested job has not been accepted by the EA module.	
Description	Erases the block BlockNumber.		
Available via	Ea.h		

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Note: The function Ea\_EraseImmediateBlock shall only be called by e.g. diagnostic or similar system services to pre-erase the area for immediate data if necessary.



#### [SWS Ea 00063]

Upstream requirements: SRS\_MemHwAb\_14009, SRS\_MemHwAb\_14032

[The function Ea\_EraseImmediateBlock shall take the block number and calculate the corresponding memory block address. The block offset shall be fixed to zero for this address calculation.]

### [SWS Ea 00064]

Upstream requirements: SRS\_MemHwAb\_14032

[The function Ea\_EraseImmediateBlock shall ensure that the EA module can write immediate data. Whether this involves physically erasing a memory area and therefore calling the erase function of the underlying driver depends on the implementation.]

### [SWS Ea 00136]

Upstream requirements: SRS\_BSW\_00406

[If development error detection for the module EA is enabled: the function Ea\_Erase ImmediateBlock shall check if the module state is MEMIF\_UNINIT. If this is the case, the function Ea\_EraseImmediateBlock shall reject the erase request, raise the development error EA\_E\_UNINIT and return with E\_NOT\_OK.]

#### [SWS Ea 00176]

Upstream requirements: SRS BSW 00323

The function Ea\_EraseImmediateBlock shall check if the module state is MEMIF\_BUSY. If this is the case, the function shall reject the erase request, raise the runtime error EA\_E\_BUSY and return with E\_NOT\_OK.

#### [SWS Ea 00152]

Upstream requirements: SRS\_BSW\_00323

[If development error detection for the module EA is enabled: the function Ea\_Erase ImmediateBlock shall check whether the given block number is valid (i.e. it has been configured). If this is not the case, the function Ea\_EraseImmediateBlock shall reject the erase request, raise the development error EA\_E\_INVALID\_BLOCK\_NO and return with E\_NOT\_OK.]

#### [SWS Ea 00065]

Upstream requirements: SRS BSW 00323, SRS MemHwAb 14032

[If development error detection for the EA module is enabled, the function Ea\_Erase ImmediateBlock shall check whether the addressed logical block is configured as containing immediate data (configuration parameter EaImmediateData == TRUE). If not, the function Ea\_EraseImmediateBlock shall reject the erase request, raise the deleopment error EA\_E\_INVALID\_BLOCK\_NO and return with E\_NOT\_OK.]



### [SWS Ea 00162]

Upstream requirements: SRS\_BSW\_00323

[If an erase request for an immediate block is rejected by the function Ea\_EraseImmediateBlock, i.e. requirements SWS\_Ea\_00136, SWS\_Ea\_00176, SWS\_Ea\_00152 or SWS\_Ea\_00065 apply, the function Ea\_EraseImmediateBlock shall not change the current module status or job result.]

### 8.4 Callback notifications

This chaper lists all functions provided by the Ea module to lower layer modules.

Note: Depending on the implementation of the modules making up the NV memory stack, callback routines provided by the EA module may be called on interrupt level. The implementation of the EA module therefore has to make sure that the runtime of those routines is reasonably short, i.e. since callbacks may be propagated upward through several software layers. Whether callback routines are allowable / feasible on interrupt level depends on the project specific needs (reaction time) and limitations (runtime in interrupt context). Therefore system design has to make sure that the configuration of the involved modules meets those requirements.

### 8.4.1 Ea JobEndNotification

#### [SWS Ea 00094] Definition of callback function Ea JobEndNotification

Upstream requirements: RS BRF 01064

Γ

Service Name	Ea_JobEndNotification		
Syntax	<pre>void Ea_JobEndNotification (    void )</pre>		
Service ID [hex]	0x10		
Sync/Async	Synchronous		
Reentrancy	Non Reentrant		
Parameters (in)	None		
Parameters (inout)	None		
Parameters (out)	None		
Return value	None		
Description	Service to report to this module the successful end of an asynchronous operation.		
Available via	Ea.h		



The underlying EEPROM driver shall call the function Ea\_JobEndNotification to report the successful end of an asynchronous operation.

### [SWS Ea 00153]

Upstream requirements: RS BRF 01064

[If the job result is currently MEMIF\_JOB\_PENDING, the function Ea\_JobEndNotification shall set the job result to MEMIF\_JOB\_OK, else it shall leave the job result untouched.]

### [SWS Ea 00051]

Upstream requirements: RS\_BRF\_01064

The function Ea\_JobEndNotification shall perform any necessary block management operations and shall call the corresponding callback routine of the upper layer module (Ea\_NvMJobEndNotification).

**[SWS\_Ea\_00200]** [The function Ea\_JobEndNotification shall perform any necessary block management and error handling operations and shall call the corresponding call-back routine of the upper layer module (Ea\_NvMJobErrorNotification).

Note: The function Ea JobEndNotification shall be callable on interrupt level.

#### 8.5 Scheduled functions

These functions are directly called by the Basic Software Scheduler. The following functions shall have no return value and no parameter. All functions shall be non reentrant.

#### 8.5.1 Ea MainFunction

### [SWS Ea 00096] Definition of scheduled function Ea MainFunction

Upstream requirements: SRS\_BSW\_00373

Γ

Service Name	Ea_MainFunction
Syntax	void Ea_MainFunction ( void )
Service ID [hex]	0x12
Description	Service to handle the requested jobs and the internal management operations.





Available via	SchM Ea.h
7114114111111	

Note: The cycle time for the function Ea\_MainFunction should be the same as that configured for the underlying EEPROM driver.

### [SWS\_Ea\_00178]

Upstream requirements: SRS\_BSW\_00406

[If the module initialization (started in the function Ea\_Init) is completed in the module's main function, the function Ea\_MainFunction shall set the module status from MEMIF\_BUSY\_INTERNAL to MEMIF\_IDLE once initialization of the module has been successfully finished.]

### [SWS Ea 00056]

Upstream requirements: RS\_BRF\_01048

The function Ea\_MainFunction shall asynchronously handle the read / write / erase / invalidate jobs requested by the upper layer and internal management operations.

#### [SWS Ea 00074]

Upstream requirements: SRS MemHwAb 14028

[The function Ea\_MainFunction shall check, whether the block requested for reading has been invalidated by the upper layer module. If so, the function Ea\_MainFunction shall set the job result to MEMIF\_BLOCK\_INVALID and call the job error notification function if configured.

#### [SWS Ea 00104]

Upstream requirements: SRS\_MemHwAb\_14032, SRS\_MemHwAb\_14015, SRS\_MemHwAb\_14016

The function Ea\_MainFunction shall check the consistency of the logical block being read before notifying the caller. If an inconsistency of the block is detected (see SWS\_Ea\_00046 and SWS\_Ea\_00047) or if the requested block can't be found, the function Ea\_MainFunction shall set the job result to MEMIF\_BLOCK\_INCONSISTENT and call the error notification routine of the upper layer if configured.

Note: In this case the upper layer shall not use the contents of the data buffer.



### [SWS Ea 00188]

Upstream requirements: SRS\_MemHwAb\_14014

[If an internal management operation has been suspended because of a job request from the upper layer, the function Ea\_MainFunction shall resume this internal management operation once the job requested by the upper layer has been finished.]

### [SWS Ea 00189]

Upstream requirements: SRS\_MemHwAb\_14014

[If an internal management operation has been aborted because of a job request from the upper layer, the function Ea\_MainFunction shall restart this internal management operation once the job requested by the upper layer has been finished.

### 8.6 Expected interfaces

In this chapter all interfaces required from other modules are listed.

### 8.6.1 Mandatory Interfaces

This chapter defines all interfaces which are required to fulfill the core functionality of the module.

### [SWS\_Ea\_00097] Definition of mandatory interfaces required by module Ea

Upstream requirements: RS\_BRF\_01056

Γ

API Function	Header File	Description
Det_ReportRuntimeError	Det.h	Service to report runtime errors. If a callout has been configured then this callout shall be called.
Eep_GetStatus	Eep.h	Returns the EEPROM status.
MemAcc_Cancel	MemAcc.h	Triggers a cancel operation of the pending job for the address area referenced by the addressAreald. Cancelling affects only jobs in pending state. For any other states, the request will be ignored.
MemAcc_Erase	MemAcc.h	Triggers an erase job of the given area.
		Triggers an erase job of the given area defined by targetAddress and length. The result of this service can be retrieved using the Mem_GetJobResult API. If the erase operation was successful, the result of the job is MEM_JOB_OK. If the erase operation failed, e.g. due to a hardware issue, the result of the job is MEM_JOB_FAILED.
MemAcc_GetJobResult	MemAcc.h	Returns the consolidated job result of the address area referenced by addressAreald.





API Function	Header File	Description
MemAcc_Read	MemAcc.h	Triggers a read job to copy data from the source address into the referenced destination data buffer. The result of this service can be retrieved using the MemAcc_GetJobResult API. If the read operation was successful, the result of the job is MEMACC_OK. If the read operation failed, the result of the job is either MEMACC_FAILED in case of a general error or MEMACC_ECC_CORRECTED/MEMACC_ECC_UNCORRECTED in case of a correctable/uncorrectable ECC error.
MemAcc_Write	MemAcc.h	Triggers a write job to store the passed data to the provided address area with given address and length. The result of this service can be retrieved using the MemAcc_GetJobResult API. If the write operation was successful, the job result is MEMACC_OK. If there was an issue writing the data, the result is MEMACC_FAILED.

I

### 8.6.2 Optional Interfaces

This chapter defines all interfaces which are required to fulfill an optional functionality of the module.

### [SWS Ea 00098] Definition of optional interfaces requested by module Ea

Upstream requirements: RS\_BRF\_01056

ſ

API Function	Header File	Description
Det_ReportError	Det.h	Service to report development errors.

### 8.6.3 Configurable interfaces

In this chapter all interfaces are listed where the target function could be configured. The target function is usually a callback function. The names of this kind of interfaces are not fixed because they are configurable.

Note: Depending on the implementation of the modules making up the NV memory stack, callback routines invoked by the EA module may be called on interrupt level. The implementor of the module providing these routines therefore has to make sure that their runtime is reasonably short, i.e. since callbacks may be propagated upward through several software layers. Whether callback routines are allowable / feasible on interrupt level depends on the project specific needs (reaction time) and limitations



(runtime in interrupt context). Therefore system design has to make sure that the configuration of the involved modules meets those requirements.

### [SWS\_Ea\_00099] Definition of configurable interface NvM\_JobEndNotification

Upstream requirements: SRS BSW 00385

Service Name	NvM_JobEndNotification	
Syntax	<pre>void NvM_JobEndNotification (    void )</pre>	
Sync/Async	Synchronous	
Reentrancy	Non Reentrant	
Parameters (in)	None	
Parameters (inout)	None	
Parameters (out)	None	
Return value	None	
Description	Function to be used by the underlying memory abstraction to signal end of job without error.	
Available via	NvM_Memlf.h	

### [SWS Ea 00054]

Upstream requirements: RS\_BRF\_01064

The Ea module shall call the function defined in the configuration parameter EaNv MJobEndNotification upon successful end of an asynchronous read operation after performing all necessary internal management operations. Successful end of an asynchronous read operation implies the read job is finished and the result is OK.

#### [SWS Ea 00141]

Upstream requirements: RS BRF 01064

[The Ea module shall call the function defined in the configuration parameter EaNv MJobEndNotification upon successful end of an asynchronous write operation after performing all necessary internal management operations. Successful end of an asynchronous write operation implies the write job is finished, the result is OK and the block has been marked as valid.

### [SWS Ea 00142]

Upstream requirements: RS\_BRF\_01064

[The Ea module shall call the function defined in the configuration parameter EaNv MJobEndNotification upon successful end of an asynchronous erase operation after performing all necessary internal management operations. Successful end of an asynchronous erase operation implies the erase job for immediate data is finished and the result is OK (see SWS Ea 00064).



### [SWS Ea 00143]

Upstream requirements: RS\_BRF\_01064

[The Ea module shall call the function defined in the configuration parameter EaNv MJobEndNotification upon successful end of an asynchronous block invalidation operation after performing all necessary internal management operations. Successful end of an asynchronous block invalidation operation implies the block invalidation job is finished and the result is OK (i.e. the block has been marked as invalid).]

### [SWS\_Ea\_00100] Definition of configurable interface NvM\_JobErrorNotification

Upstream requirements: SRS\_BSW\_00385

Γ

Service Name	NvM_JobErrorNotification	
Syntax	<pre>void NvM_JobErrorNotification (   void )</pre>	
Sync/Async	Synchronous	
Reentrancy	Non Reentrant	
Parameters (in)	None	
Parameters (inout)	None	
Parameters (out)	None	
Return value	None	
Description	Function to be used by the underlying memory abstraction to signal end of job with error.	
Available via	NvM_Memlf.h	

### [SWS\_Ea\_00055]

Upstream requirements: RS\_BRF\_01064

[The Ea module shall call the function defined in the configuration parameter EaNv MJobErrorNotification upon failure of an asynchronous read operation after performing all necessary internal management and error handling operations. Failure of an asynchronous read operation implies the read job is finished and has failed (i.e. block invalid or inconsistent).

#### [SWS Ea 00144]

Upstream requirements: RS BRF 01064

[The Ea module shall call the function defined in the configuration parameter EaNv MJobErrorNotification upon failure of an asynchronous write operation after performing all necessary internal management and error handling operations. Failure of an asynchronous write operation implies the write job is finished and has failed and block has been marked as inconsistent.



### [SWS\_Ea\_00145]

Upstream requirements: RS\_BRF\_01064

[The Ea module shall call the function defined in the configuration parameter EaNv MJobErrorNotification upon failure of an asynchronous erase operation after performing all necessary internal management and error handling operations. Failure of an asynchronous erase operation implies the erase job for immediate data is finished and has failed (see SWS Ea 00064).

### [SWS Ea 00146]

Upstream requirements: RS\_BRF\_01064

[The Ea module shall call the function defined in the configuration parameter EaNv MJobErrorNotification upon failure of an asynchronous block invalidation operation after performing all necessary internal management and error handling operations. Failure of an asynchronous block invalidation operation implies the block invalidation job is finished and has failed.



# 9 Sequence diagrams

Note: For a vendor specific library the following sequence diagrams are valid only insofar as they show the relation to the calling modules (Ecu\_StateManager resp. memory abstraction interface). The calling relations from a memory abstraction module to an underlying driver are not relevant / binding for a vendor specific library.

### 9.1 Ea Init

The following figure shows the call sequence for the Ea\_Init routine. It is different from that of all other services of this module as it is not called by the NVRAM manager and not called via the memory abstraction interface.

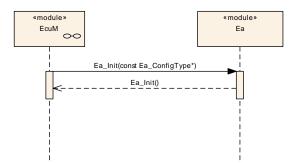


Figure 9.1: Sequence diagram of "Ea\_Init" service

# 9.2 Ea\_Write

The following figure shows as an example the call sequence for the Ea\_Write service. This sequence diagram also applies to the other asynchronous services of this module.



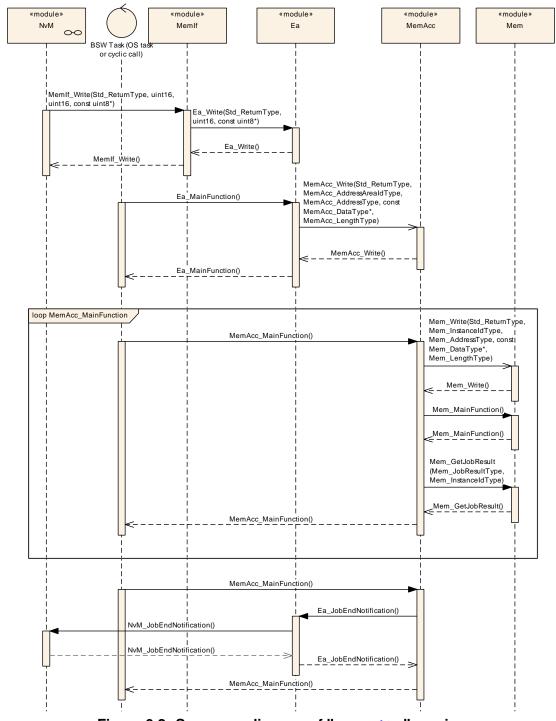


Figure 9.2: Sequence diagram of "Ea\_Write" service

# 9.3 Ea\_Cancel

The following figure shows as an example the call sequence for a canceled Ea\_Write service. This sequence diagram shows that Ea\_Cancel is asynchronous w.r.t. the underlying hardware while itself being synchronous.



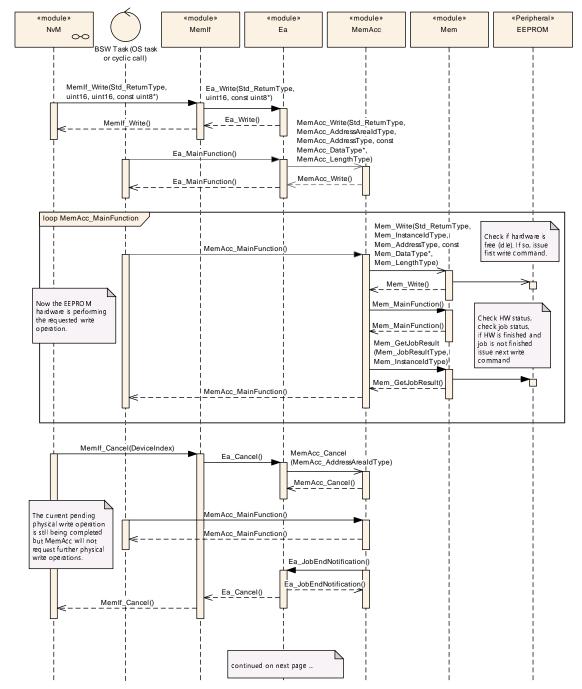


Figure 9.3: Sequence diagram of "Ea\_Cancel" service (Part 1)



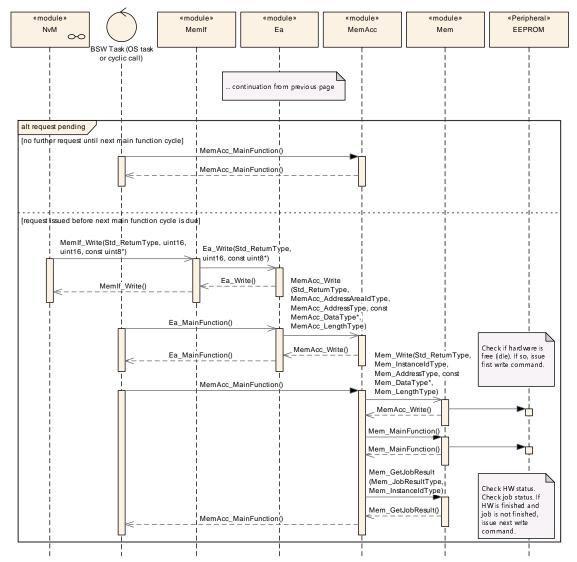


Figure 9.4: Sequence diagram of "Ea\_Cancel" service (Part 2)



# 10 Configuration specification

# 10.1 Containers and configuration parameters

The following chapters summarize all configuration parameters.

### 10.1.1 Ea

### [ECUC\_Ea\_00133] Definition of EcucModuleDef Ea [

Module Name	Ea
Description	Configuration of the Ea (EEPROM Abstraction) module. The module shall abstract from the device specific addressing scheme and segmentation and provide the upper layers with a virtual addressing scheme and segmentation as well as a 'virtually' unlimited number of erase cycles.
Post-Build Variant Support	false
Supported Config Variants	VARIANT-PRE-COMPILE

Included Containers			
Container Name	Multiplicity	Scope / Dependency	
EaBlockConfiguration	1*	Configuration of block specific parameters for the EEPROM abstraction module.	
EaGeneral	1	General configuration of the EEPROM abstraction module. This container lists block independent configuration parameters.	
EaPublishedInformation	1	Additional published parameters not covered by Common PublishedInformation container.	
		Note that these parameters do not have any configuration class setting, since they are published information.	



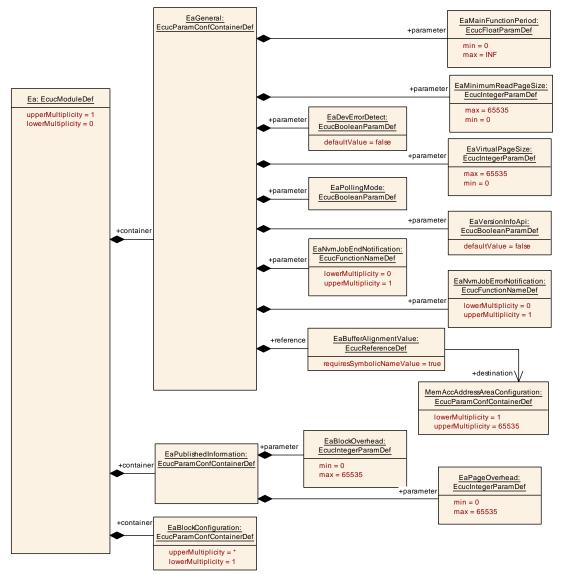


Figure 10.1: Ea Configuration Layout

#### 10.1.2 EaGeneral

### [ECUC\_Ea\_00039] Definition of EcucParamConfContainerDef EaGeneral [

Container Name	EaGeneral	
Parent Container	Ea	
Description	General configuration of the EEPROM abstraction module. This container lists block independent configuration parameters.	
Configuration Parameters		



Included Parameters			
Parameter Name	Multiplicity	ECUC ID	
EaDevErrorDetect	1	[ECUC_Ea_00120]	
EaMainFunctionPeriod	1	[ECUC_Ea_00132]	
EaMinimumReadPageSize	1	[ECUC_Ea_00135]	
EaNvmJobEndNotification	01	[ECUC_Ea_00121]	
EaNvmJobErrorNotification	01	[ECUC_Ea_00122]	
EaPollingMode	1	[ECUC_Ea_00123]	
EaVersionInfoApi	1	[ECUC_Ea_00124]	
EaVirtualPageSize	1	[ECUC_Ea_00125]	
EaBufferAlignmentValue	1	[ECUC_Ea_00136]	

No Included Containers

1

## [ECUC\_Ea\_00120] Definition of EcucBooleanParamDef EaDevErrorDetect [

Parameter Name	EaDevErrorDetect		
Parent Container	EaGeneral		
Description	Switches the development error detection and notification on or off.		
	• true: detection and notification is enabled.		
	false: detection and notification is disabled.		
Multiplicity	1		
Туре	EcucBooleanParamDef		
Default value	false		
Post-Build Variant Value	false		
Value Configuration Class	Pre-compile time	X	All Variants
	Link time –		
	Post-build time –		
Scope / Dependency	scope: local		

-

# [ECUC\_Ea\_00132] Definition of EcucFloatParamDef EaMainFunctionPeriod [

Parameter Name	EaMainFunctionPeriod			
Parent Container	EaGeneral	EaGeneral		
Description	The period between successive calls to the main function in seconds.			
Multiplicity	1			
Туре	EcucFloatParamDef			
Range	]0 INF[			
Default value	-			
Post-Build Variant Value	false			
Value Configuration Class	Pre-compile time X All Variants			
	Link time -			
	Post-build time	Post-build time –		





Scope / Dependency	scope: ECU
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## [ECUC\_Ea\_00135] Definition of EcucIntegerParamDef EaMinimumReadPageSize

Status: DRAFT

Γ

Parameter Name	EaMinimumReadPageSize	EaMinimumReadPageSize		
Parent Container	EaGeneral			
Description	Minimum Page size will be a multiple of the minimum page size. Ea shall align read requests to this size.			
	Tags: atp.Status=draft			
Multiplicity	1			
Туре	EcucIntegerParamDef			
Range	0 65535	0 65535		
Default value	-	•		
Post-Build Variant Value	false			
Value Configuration Class	Pre-compile time	Х	All Variants	
	Link time –			
	Post-build time –			
Scope / Dependency	scope: local			

# [ECUC\_Ea\_00121] Definition of EcucFunctionNameDef EaNvmJobEndNotification $\lceil$

Parameter Name	EaNvmJobEndNotification	EaNvmJobEndNotification		
Parent Container	EaGeneral			
Description	Mapped to the job end notification routine provided by the upper layer module (NvM_ JobEndNotification).			
Multiplicity	01			
Туре	EcucFunctionNameDef			
Default value	-			
Regular Expression	-			
Post-Build Variant Multiplicity	false			
Post-Build Variant Value	false			
Multiplicity Configuration Class	Pre-compile time	X	All Variants	
	Link time	_		
	Post-build time	_		
Value Configuration Class	Pre-compile time	X	All Variants	
	Link time -			
	Post-build time –			
Scope / Dependency	scope: local			



# [ECUC\_Ea\_00122] Definition of EcucFunctionNameDef EaNvmJobErrorNotification $\lceil$

Parameter Name	EaNvmJobErrorNotification		
Parent Container	EaGeneral		
Description	Mapped to the job error notification routine provided by the upper layer module (NvM_ JobErrorNotification).		
Multiplicity	01		
Туре	EcucFunctionNameDef		
Default value	-		
Regular Expression	-		
Post-Build Variant Multiplicity	false		
Post-Build Variant Value	false		
Multiplicity Configuration Class	Pre-compile time	X	All Variants
	Link time	_	
	Post-build time	_	
Value Configuration Class	Pre-compile time X All Variants		
	Link time –		
	Post-build time –		
Scope / Dependency	scope: local		

1

# [ECUC\_Ea\_00123] Definition of EcucBooleanParamDef EaPollingMode $\lceil$

Parameter Name	EaPollingMode		
Parent Container	EaGeneral		
Description	Pre-processor switch to enable and disable the polling mode for this module.		
	true: Polling mode enabled, callback functions (provided to EEP module) disabled.		
	false: Polling mode disabled, callback functions (provided to EEP module) enabled.		
Multiplicity	1		
Туре	EcucBooleanParamDef		
Default value	-		
Post-Build Variant Value	false		
Value Configuration Class	Pre-compile time	X	All Variants
	Link time	_	
	Post-build time –		
Scope / Dependency	scope: local		



## [ECUC\_Ea\_00124] Definition of EcucBooleanParamDef EaVersionInfoApi

Parameter Name	EaVersionInfoApi			
Parent Container	EaGeneral	EaGeneral		
Description	Pre-processor switch to enable / disable the API to read out the modules version information.			
	true: Version info API enabled. false: Version info API disabled.			
Multiplicity	1			
Туре	EcucBooleanParamDef			
Default value	false			
Post-Build Variant Value	false			
Value Configuration Class	Pre-compile time	Х	All Variants	
	Link time –			
	Post-build time –			
Scope / Dependency	scope: local			

## [ECUC\_Ea\_00125] Definition of EcucIntegerParamDef EaVirtualPageSize [

Parameter Name	EaVirtualPageSize		
Parent Container	EaGeneral		
Description	The size in bytes to which logical blocks shall be aligned.		
Multiplicity	1		
Туре	EcucIntegerParamDef		
Range	0 65535		
Default value	-		
Post-Build Variant Value	false		
Value Configuration Class	Pre-compile time	X	All Variants
	Link time –		
	Post-build time –		
Scope / Dependency	scope: local		

### [ECUC\_Ea\_00136] Definition of EcucReferenceDef EaBufferAlignmentValue

Status: DRAFT

Γ

Parameter Name	EaBufferAlignmentValue
Parent Container	EaGeneral
Description	Parameter determines the alignment of the start address that Ea buffers need to have.
	Value shall be inherited from MemAccBufferAlignmentValue.
	Tags: atp.Status=draft
Multiplicity	1
Туре	Symbolic name reference to MemAccAddressAreaConfiguration





Post-Build Variant Value	false		
Value Configuration Class	Pre-compile time X All Variants		
	Link time –		
	Post-build time	_	
Scope / Dependency	scope: local		-

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### 10.1.3 EaBlockConfiguration

# [ECUC\_Ea\_00040] Definition of EcucParamConfContainerDef EaBlockConfiguration $\ \lceil$

Container Name	EaBlockConfiguration
Parent Container	Ea
Description	Configuration of block specific parameters for the EEPROM abstraction module.
Configuration Parameters	

Included Parameters			
Parameter Name	Multiplicity	ECUC ID	
EaBlockNumber	1	[ECUC_Ea_00130]	
EaBlockSize	1	[ECUC_Ea_00128]	
EalmmediateData	1	[ECUC_Ea_00131]	
EaNumberOfWriteCycles	1	[ECUC_Ea_00119]	
EaDeviceIndex	01	[ECUC_Ea_00129]	
EaMemAccAddressArea	01	[ECUC_Ea_00134]	

1

# [ECUC\_Ea\_00130] Definition of EcucIntegerParamDef EaBlockNumber $\lceil$

Parameter Name	EaBlockNumber
Parent Container	EaBlockConfiguration
Description	Block identifier (handle).
	0x0000 and 0xFFFF shall not be used for block numbers (see SWS_Ea_00006).
	Range: min = 2^NVM_DATASET_SELECTION_BITS max = 0xFFFF -2^NVM_DATASET_SELECTION_BITS
	Note: Depending on the number of bits set aside for dataset selection several other block numbers shall also be left out to ease implementation.
Multiplicity	1
Туре	EcucIntegerParamDef (Symbolic Name generated for this parameter)





Range	1 65534		
Default value	-		
Post-Build Variant Value	false		
Value Configuration Class	Pre-compile time	X	All Variants
	Link time	_	
	Post-build time	_	
Scope / Dependency	scope: ECU		

-

# [ECUC\_Ea\_00128] Definition of EcucIntegerParamDef EaBlockSize [

Parameter Name	EaBlockSize			
Parent Container	EaBlockConfiguration			
Description	Size of a logical block in bytes.			
Multiplicity	1			
Туре	EcucIntegerParamDef	EcucIntegerParamDef		
Range	1 65535	1 65535		
Default value	-			
Post-Build Variant Value	false			
Value Configuration Class	Pre-compile time	Pre-compile time X All Variants		
	Link time	_		
	Post-build time	_		
Scope / Dependency	scope: ECU			

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# [ECUC\_Ea\_00131] Definition of EcucBooleanParamDef EalmmediateData $\lceil$

Parameter Name	EalmmediateData			
Parent Container	EaBlockConfiguration			
Description	Marker for high priority data.	Marker for high priority data.		
	true: Block contains immediate data	ı. false: E	llock does not contain immediate data.	
Multiplicity	1	1		
Туре	EcucBooleanParamDef			
Default value	-			
Post-Build Variant Value	false			
Value Configuration Class	Pre-compile time X All Variants			
	Link time –			
	Post-build time	_		
Scope / Dependency	scope: ECU		_	



# [ECUC\_Ea\_00119] Definition of EcucIntegerParamDef EaNumberOfWriteCycles

Parameter Name	EaNumberOfWriteCycles			
Parent Container	EaBlockConfiguration	EaBlockConfiguration		
Description	Number of write cycles required for	this block		
Multiplicity	1			
Туре	EcucIntegerParamDef	EcucIntegerParamDef		
Range	0 4294967295			
Default value	-			
Post-Build Variant Value	false			
Value Configuration Class	Pre-compile time X All Variants			
	Link time –			
	Post-build time –			
Scope / Dependency	scope: local			

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### [ECUC\_Ea\_00129] Definition of EcucReferenceDef EaDeviceIndex

Status: OBSOLETE

Γ

Parameter Name	EaDeviceIndex			
Parent Container	EaBlockConfiguration			
Description	Reference to the device this block is	s stored i	n.	
	This reference is mutually exclusive	to EaMe	mAccAddressArea.	
	Tags: atp.Status=obsolete			
Multiplicity	01			
Туре	Symbolic name reference to EepGe	eneral		
Post-Build Variant Multiplicity	false			
Post-Build Variant Value	false			
Multiplicity Configuration Class	Pre-compile time X All Variants			
	Link time –			
	Post-build time –			
Value Configuration Class	Pre-compile time X All Variants			
	Link time –			
	Post-build time –			
Scope / Dependency	scope: local			
	dependency: This information is needed by the NVRAM manager respectively the Memory Abstraction Interface to address a certain logical block. It is listed in this specification to give a complete overview over all block related configuration parameters. This reference is mutually exclusive to EaMemAccAddressArea.			



### [ECUC\_Ea\_00134] Definition of EcucReferenceDef EaMemAccAddressArea

Status: DRAFT

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Parameter Name	EaMemAccAddressArea		
Parent Container	EaBlockConfiguration		
Description	Reference to the MemAccAddressA	AreaCon	figuration.
	This reference is mutually exclusive	to EaD	eviceIndex.
	Tags: atp.Status=draft		
Multiplicity	01		
Туре	Symbolic name reference to MemAccAddressAreaConfiguration		
Post-Build Variant Multiplicity	false		
Post-Build Variant Value	false		
Multiplicity Configuration Class	Pre-compile time X All Variants		
	Link time –		
	Post-build time	-	
Value Configuration Class	Pre-compile time X All Variants		
	Link time –		
	Post-build time –		
Scope / Dependency	scope: local		
	dependency: This reference is mutually exclusive to EaDeviceIndex.		

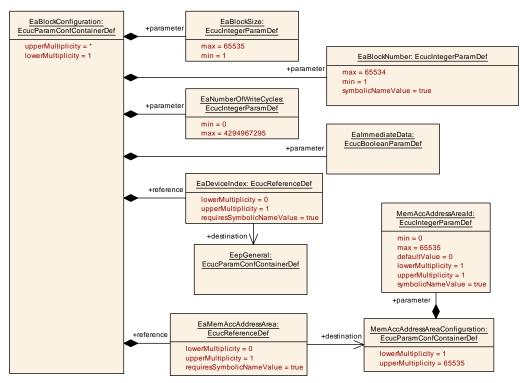


Figure 10.2: Ea Block Configuration Layout



### 10.2 Published Information

### 10.2.1 EaPublishedInformation

# [ECUC\_Ea\_00043] Definition of EcucParamConfContainerDef EaPublishedInformation $\lceil$

Container Name	EaPublishedInformation
Parent Container	Ea
Description	Additional published parameters not covered by CommonPublishedInformation container.
	Note that these parameters do not have any configuration class setting, since they are published information.
Configuration Parameters	

Included Parameters			
Parameter Name	Multiplicity	ECUC ID	
EaBlockOverhead	1	[ECUC_Ea_00126]	
EaPageOverhead	1	[ECUC_Ea_00127]	

No Included Containers	
No Included Containers	

## [ECUC\_Ea\_00126] Definition of EcucIntegerParamDef EaBlockOverhead $\lceil$

Parameter Name	EaBlockOverhead			
Parent Container	EaPublishedInformation			
Description	Management overhead per logical block in bytes.			
	Note: If the management overhead depends on the block size or block location a formula has to be provided that allows the configurator to calculate the management overhead correctly.			
Multiplicity	1			
Туре	EcucIntegerParamDef			
Range	0 65535			
Default value	-			
Post-Build Variant Value	false			
Value Configuration Class	Published Information	Х	All Variants	
Scope / Dependency	scope: local			



## [ECUC\_Ea\_00127] Definition of EcucIntegerParamDef EaPageOverhead [

Parameter Name	EaPageOverhead			
Parent Container	EaPublishedInformation			
Description	Management overhead per page in bytes.			
	Note: If the management overhead depends on the block size or block location a formula has to be provided that allows the configurator to calculate the management overhead correctly.			
Multiplicity	1			
Туре	EcucIntegerParamDef			
Range	0 65535			
Default value	-			
Post-Build Variant Value	false			
Value Configuration Class	Published Information	Х	All Variants	
Scope / Dependency	scope: local			



# Not applicable requirements

### [SWS Ea NA 00999]

Upstream requirements: SRS\_BSW\_00416, SRS\_BSW\_00168, SRS\_BSW\_00423, SRS\_BSW\_-00424, SRS\_BSW\_00425, SRS\_BSW\_00426, SRS\_BSW\_00427, SRS\_BSW\_00428, SRS\_BSW\_00429, SRS\_BSW\_00432, SRS\_BSW\_-SRS BSW 00336, SRS BSW 00339, SRS BSW 00422, SRS BSW 00417, SRS BSW 00161, SRS BSW 00162, SRS BSW -SRS BSW 00415, SRS BSW 00164, SRS BSW 00342, 00005. SRS BSW 00160, SRS BSW 00007, SRS BSW 00300, SRS BSW -SRS BSW 00305, SRS BSW 00307, SRS BSW 00314, SRS\_BSW\_00348, SRS\_BSW\_00353, SRS\_BSW\_00302, SRS\_BSW\_-00328, SRS BSW 00312, SRS BSW 00006, SRS BSW 00304, SRS\_BSW\_00378, SRS\_BSW\_00306, SRS\_BSW\_00308, SRS\_BSW\_-00309, SRS\_BSW\_00330, SRS\_BSW\_00009, SRS\_BSW\_00401, SRS BSW 00172, SRS BSW 00010, SRS BSW 00333, SRS BSW -00321, SRS\_BSW\_00341, SRS\_SPAL\_12263, SRS\_SPAL\_12267, SRS SPAL 12125. SRS SPAL 12163. SRS SPAL 12461. SRS -SPAL 12462, SRS SPAL 12463, SRS SPAL 12068, SRS SPAL -12069, SRS SPAL 00157, SRS SPAL 12063, SRS SPAL 12129, SRS SPAL 12064, SRS\_SPAL\_12067, SRS\_SPAL\_12077, SRS\_-SPAL\_12078, SRS\_SPAL\_12092, SRS\_SPAL\_12265, SRS\_MemHwAb\_14018

These requirements are not applicable to this specification.



# B Change history of AUTOSAR traceable items

- B.1 Change History of this document according to AUTOSAR Release R24-11
- **B.1.1 Added Specification Items in R24-11**

none

**B.1.2 Changed Specification Items in R24-11** 

[ECUC\_Ea\_00129]

**B.1.3 Deleted Specification Items in R24-11** 

none