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- [2] Standardization Template  
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- [3] SW-C and System Modeling Guide  
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- [4] XML Specification of Application Interfaces  
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- [11] Secure Design Patterns  
<http://www.sei.cmu.edu/reports/09tr010.pdf>
- [12] Software Component Template  
AUTOSAR\_TPS\_SoftwareComponentTemplate
- [13] Layered Software Architecture  
AUTOSAR\_EXP\_LayeredSoftwareArchitecture

# 1 Introduction

## 1.1 Document conventions

Technical terms (Class Names) are typeset in mono spaced font, e.g. `FrameTriggering`.

When defining name patterns the syntax defined according to ANTLR is used [1]. The grammar for name patterns as defined in [2], [TPS\_STDT\_00055], is used. In the following we just list the most important placeholders that are used throughout the document:

**anyName** This represents a string which is valid `shortName` according to `Identifier`

**anyNamePart** This represents a string `(([a-zA-Z0-9]_[a-zA-Z0-9])*_?)` which is valid part of a `shortName`.

Hint: The place holder "anyNamePart" shall not be used at the beginning of a `shortName` pattern to avoid invalid `shortNames`.

**blueprintName** This represents the `shortName` / `shortLabel` / `symbol` of the applied blueprint

**componentName** This represents the `shortName` of the BSW module resp. ASW `SwComponentType` / ASW component prototype related to the derived object. "Related" mainly could be both, aggregating or referencing.

The placeholder `componentName` in particular supports multiple derivation of a `PortPrototypeBlueprint` in the context of different software component types resp. modules [TPS\_STDT\_00036].

**componentTypeName** This represents the `shortName` of the dedicated `SwComponentType`.

**componentPrototypeName** This represents the `shortName` of the dedicated `SwComponentPrototype`.

**index** This represents a numerical index applicable for example to arrays.

**keyword** This represents the `abbrName` of a keyword acting as a name part of the short name [TPS\_STDT\_00004].

For a complete description see [2], [TPS\_STDT\_00055]. Additionally we assume that the naming rules as defined in [3] are fulfilled. If applicable and available the keywords used in names are those standardized in [4].

Additionally we extend the grammar using the following place holders:

**anyLongName** This represents a string which is a valid `longName`.



Additionally we assume that [TR\_SWNR\_0064] is fulfilled. This means that the long name starts with a capital letter and that all words except articles (e.g. "a", "the"), prepositions (e.g. "at", "by", "to") and conjunctions (e.g. "and", "or") start with a capital letter as well.

**anyLongNamePart** This represents a string which is a valid part of a `longName`.

## 1.2 Requirements Tracing

Requirements against this document are stated in the requirements document [5].

The following table references the requirements specified in [5] and provides information about individual specification items that fulfill a given requirement.

Requirement	Description	Satisfied by
[RS_MAIN_00060]	AUTOSAR shall provide a standardized software interface for communication between Applications	[TR_AIDPC_00006] [TR_AIDPC_00007]
[RS_MAIN_00080]	AUTOSAR shall provide means to describe a component model for Application Software	[TR_AIDPC_00001] [TR_AIDPC_00002]
[RS_MAIN_00130]	AUTOSAR shall provide an abstraction from hardware	[TR_AIDPC_00001] [TR_AIDPC_00002]
[RS_MAIN_00140]	AUTOSAR shall provide network independent communication mechanisms for applications	[TR_AIDPC_00001] [TR_AIDPC_00002] [TR_AIDPC_00003]
[RS_MAIN_00150]	AUTOSAR shall support the deployment and reallocation of AUTOSAR Application Software	[TR_AIDPC_00001] [TR_AIDPC_00002]
[RS_MAIN_00400]	AUTOSAR shall provide a layered software architecture	[TR_AIDPC_00001] [TR_AIDPC_00002] [TR_AIDPC_00003] [TR_AIDPC_00004]
[RS_MAIN_00410]	AUTOSAR shall provide specifications for routines commonly used by Application Software to support sharing and optimization	[TR_AIDPC_00003]
[RS_MAIN_00500]	AUTOSAR shall provide naming conventions	[TR_AIDPC_00005]

## 2 About Patterns

This document gives an overview of the patterns defined in AUTOSAR for ease the usage of AUTOSAR architecture, AUTOSAR application interfaces and the AUTOSAR meta-model. The focus is on application software (ASW).

### 2.1 Types of Pattern

The following categories/classifications of patterns are distinguished:

**Architectural Pattern** An architectural pattern is a standard design in the field of software architecture. The concept of an architectural pattern has a broader scope than the concept of design pattern. The architectural patterns address various issues in software engineering, such as computer hardware performance limitations, high availability and minimization of a business risk [6].

**Design Pattern** In software engineering, a design pattern is a general reusable solution to a commonly occurring problem within a given context in software design. A design pattern is not a finished design that can be transformed directly into source or machine code. It is a description or template for how to solve a problem that can be used in many different situations. Patterns are formalized best practices that the programmer must implement themselves in the application [7].

**Solution Pattern** A solution pattern describes a generic solution for a specific problem like for example error handling or job scheduling [6].

An orthogonal classification of patterns is the following:

**Design Patterns** A design pattern in architecture and computer science is a formal way of documenting a solution to a design problem in a particular field of expertise [8].

**Anti-Patterns** In software engineering, an anti-pattern (or anti-pattern) is a pattern used in social or business operations or software engineering that may be commonly used but is ineffective and/or counterproductive in practice [9].

### 2.2 Describing Patterns

The description of the patterns in this document follow a predefined structure. This structure was created based on the contents of the documents [7], [10], [11], [1], and [2].

A pattern is described in a separate section and the header of the particular pattern contains the name of the pattern and the pattern identification (standardized name):  
{pattern name} ({pattern identification})

At the very beginning of the section describing a specific pattern the classification is given as shown below:

**Classification** {type of pattern} Pattern

The type of the pattern is one of the categories described in section 2.1.

Section	Mandatory	Instruction	Additional Information
Problem	Yes	The problem solved by the design pattern and its general rationale and purpose.	None
Also Known As	No	Other names for the pattern, if any are known.	None
Applicability	Yes	A general description of the characteristics a system must have for the pattern to be useful in the design or implementation of the program.	Indications: something you notice, hinting that this pattern may be applicable Contraindications: something that would indicate that this pattern would not be applicable
Solution	Yes	A textual or graphical description of the pattern. This provides a detailed specification of the structural aspects of the pattern, using appropriate notations.	Also think about <i>Overdose Effect</i> : what undesirable thing happens if you keep applying the suggested action over and over and over and over. Also think about <i>Side Effects</i> : new problems that you might expect to crop up upon applying the solution, or new issues that come to the fore.
Naming	No	Describes naming pattern that are usable or should be used in the context of the pattern.	Name pattern follow syntax defined according to ANTLR like it was decided to use in [2], e.g. in [TPS_STDT_00055].
Example	Yes	Example how to apply the pattern.	None
Sample Code and Model	No	Code or model providing an example of how to implement the pattern.	None
Known Uses	No	Examples of the use of the pattern, taken from existing systems or literature.	None
Related Patterns	No	Other patterns that have some relationship with the pattern; discussion of the differences between the pattern and similar patterns.	Other patterns that relate, either superordinate, subordinate, competitor, or neighboring patterns, with references to where they can be found.
Anti-Patterns	No	Anti-Patterns you should be aware of.	None
Reading	No	Further material worthwhile to know.	None

**Table 2.1: Pattern Description Template**

## 3 Sensor and Actuator Pattern

**Classification** Design Pattern

### 3.1 Motivation

The Sensor/Actuator Design Pattern describes how to handle sensors or actuators that are connected to an ECU in the context of an overall architecture.

The main intention of this pattern is standardizing application interfaces for SWC controlling sensors and actuators, it focuses on aspects of:

- Independence of application software from concrete sensors and actuators connected to a specific ECU.
- Reusable code between different sensors and actuators.
- Different code sharing cooperation models (software sharing), thus supporting different business models.
- Deployment of functionality to different ECUs.

For standardizing interfaces it is useful to have an architectural design overview of a sensor/actuator composition. Therefore it was decided to create an architectural design pattern first and define the interface inside next. In a first step a layer model containing the main interfaces between those layers is created. Then the most common functions within the layers are defined and described for a common understanding in a second step. In the third step it is planned to describe also the interfaces in these functions from step 2.

The pattern in general is a strong recommendation but is not mandatory to be followed. The interfaces which are standardized as a result from the pattern will be reserved exactly for the described usecase and shall not be used for other purpose even if the pattern is not followed.

### 3.2 Also Known As

This pattern is also known as *Device Abstraction*.

### 3.3 Applicability

**[TR\_AIDPC\_00001] Access to Hardware by PSnsrAct** [

The *Device Abstraction* is located above the RTE. It is a set of software components that abstracts from the sensors and actuators connected to a specific ECU. It uses

sensor actuator software components, the only components above RTE that are allowed to access the ECU abstraction interface.]([RS\\_MAIN\\_00080](#), [RS\\_MAIN\\_00130](#), [RS\\_MAIN\\_00140](#), [RS\\_MAIN\\_00150](#), [RS\\_MAIN\\_00400](#))

In case direct access to the Micro controller is required because specific interrupts and/or complex Micro controller peripherals to fulfill the special functional and timing requirements of the sensor evaluation or actuator control have to be implemented this pattern cannot be applied. Instead a complex driver implementation shall be used.

**[TR\_AIDPC\_00002] Collaboration supported by PSnsrAct** [The Sensor/Actuator Design Pattern supports software sharing (=collaboration between various partners) on different levels: Development partner one might deliver the sensors together with the basic electrical driver software (DrvrsnsrElec), development partner two might deliver the sensor device driver software (DevDrvrsnsr) and the third partner might develop the substitute models together with the virtual device drivers (DevSnsrVirt). There might be different suppliers for the same Sensor/Actuator or there might be sensors/actuators from different vendors used within one and the same system.]([RS\\_MAIN\\_00080](#), [RS\\_MAIN\\_00130](#), [RS\\_MAIN\\_00140](#), [RS\\_MAIN\\_00150](#), [RS\\_MAIN\\_00400](#))

In case software sharing shall not be supported it is also possible to just implement the interfaces of the composition of a single sensor or actuator but not following the internal three-level-architecture.

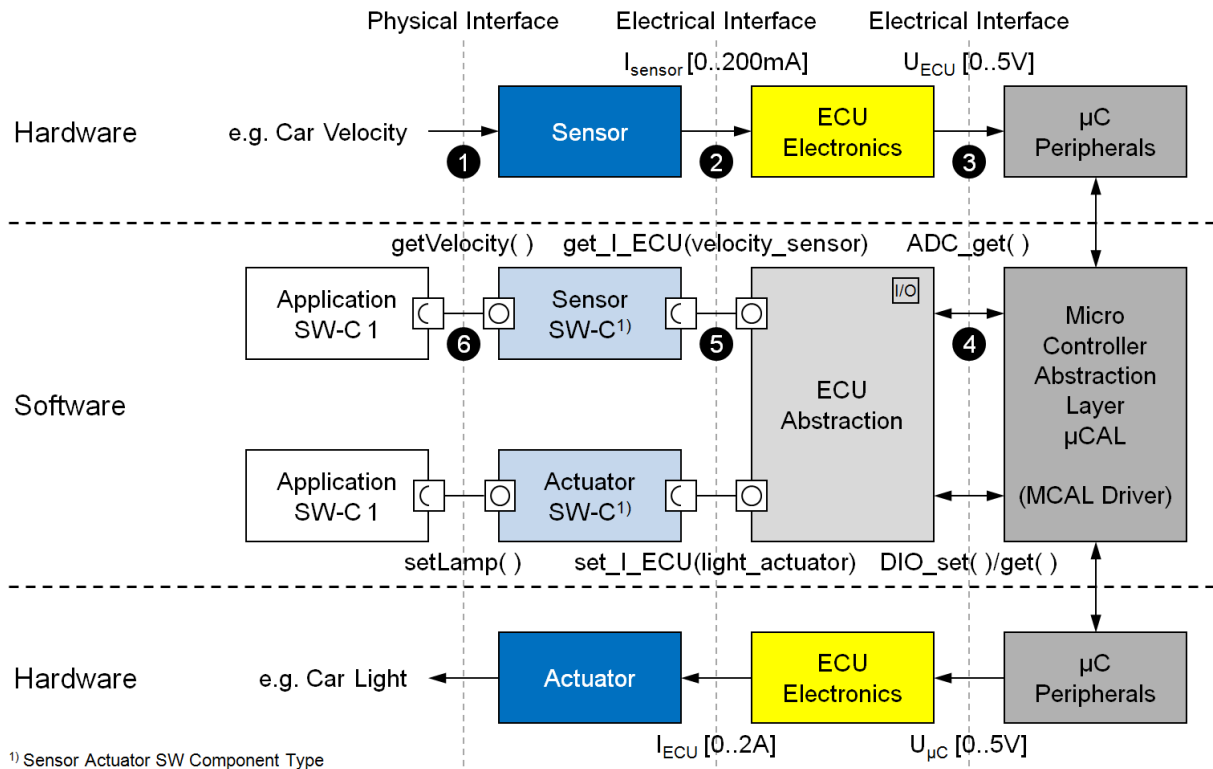
**[TR\_AIDPC\_00003] Deployment/Relocation supported by PSnsrAct** [The Sensor/Actuator-Pattern also supports different deployment scenarios to ECUs. One ECU might provide the measured value of a sensor whereas another ECU is implementing the model that calculates the estimated value that may substitute the measured sensor value.]([RS\\_MAIN\\_00140](#), [RS\\_MAIN\\_00400](#), [RS\\_MAIN\\_00410](#))

Note: In general a pattern is not applied without any changes but with extension by combining several patterns to one solution. For example:

- The composition pattern (splitting of component if they are getting too large and are not maintainable any longer) is combined with this pattern.
- The diagnosis pattern is combined with this pattern.

### 3.4 Solution

In Figure 3.1 that was taken from [12] an example of the signal flow for a lamp (actuator) and a velocity sensor is shown. This signal flow pattern is refined by this sensor/actuator pattern.



<sup>1)</sup> Sensor Actuator SW Component Type

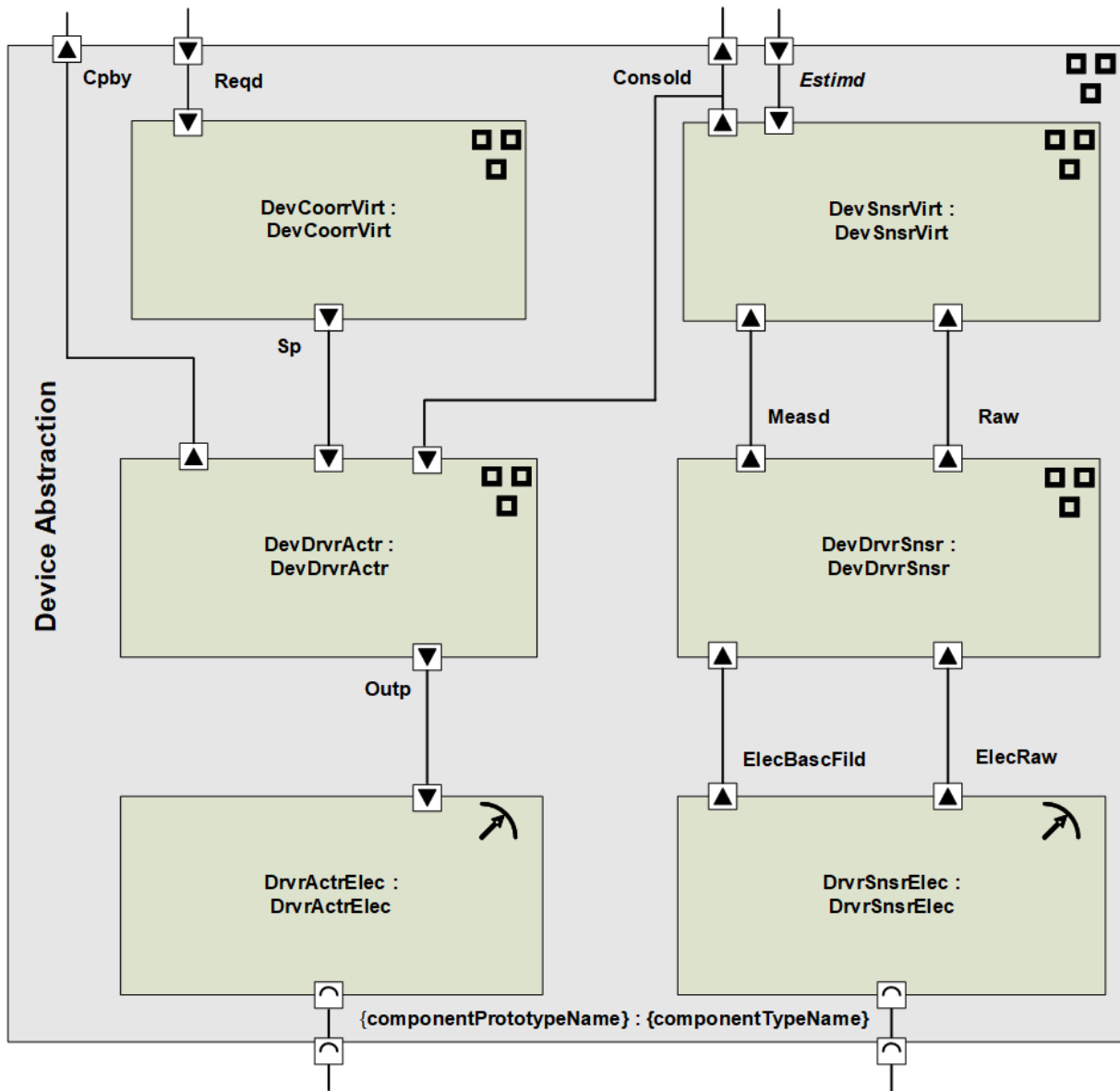
**Figure 3.1: Sensor Actuator Signal Flow [12]**

**[TR\_AIDPC\_00004] Layers of PSnsrAct** [The solution is proposing a three-level layering within a composition representing a sensor or actuator:

- electrical device driver layer,
- sensor/actuator device driver layer,
- virtual device driver layer.

]([RS\\_MAIN\\_00400](#))

In Figure 3.2 the overall structure of the pattern is shown. Recursive elements are optional. Closed loop controlled actuator and position feedback is included. The naming is simplified and will be explained in more detail later.



**Figure 3.2: Sensor Actuator Pattern for Closed Loop**

The application software can rely on the existence of the consolidated value. The consolidated value can be calculated from the

- estimated value,
- setpoint value,
- measured and/or raw value.

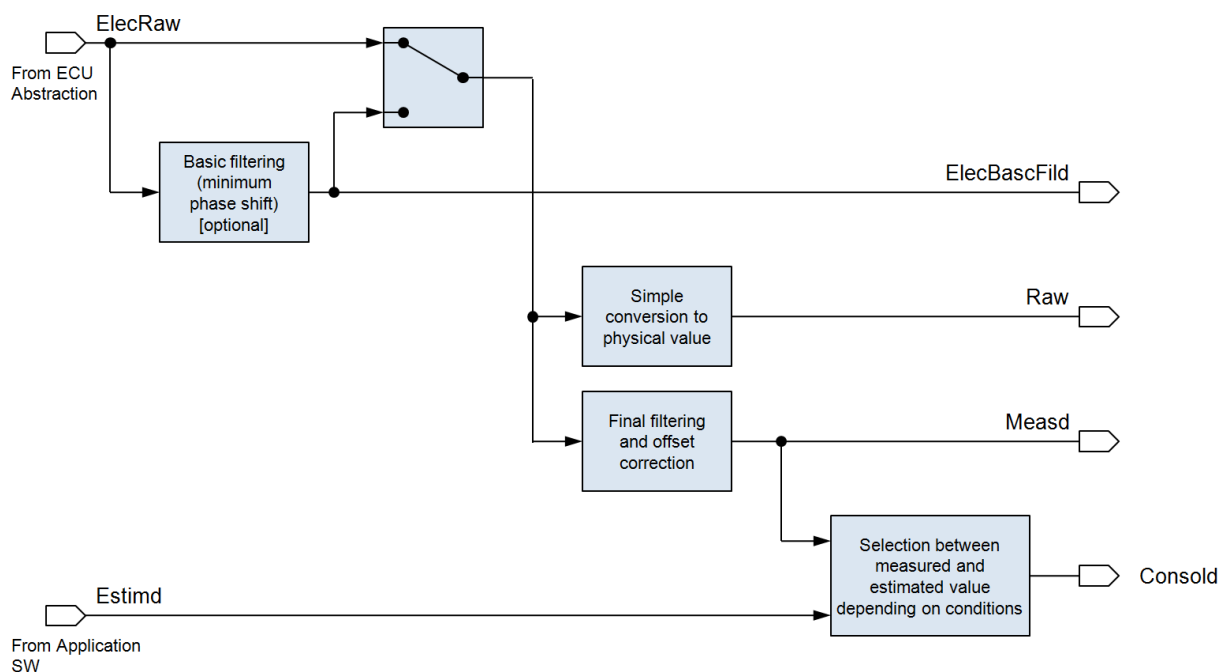
The calculation of the consolidated value via the setpoint or estimated value is used in case of actuators without feedback loop. In Figure 3.8 an example of an actuator without feedback loop calculating the consolidated value from the setpoint value is shown. Besides actuators with open loop control there are also smart actuators that can directly deal with the setpoint value itself. In this case the device driver actuator SW-C and the electrical driver actuator SW-C are only routing the setpoint value since



the controlling of the actuator and thus the calculating of the output value etc. is realized within the smart actuator itself. However, the two layers, electrical device layer and device driver layer, are additionally needed because of diagnosis etc.

The pattern can be tailored for a standard sensor. In this case the consolidated value (Consold) is provided and the estimated value (Estimd) is requested, see Figure 3.9.

The signal flow is shown in Figure 3.3: The electrical raw value is requested from the ECU Abstraction. After basic filtering the signal is converted to a physical value representing the measured value. If the measured value is not suitable for the application the estimated value might be chosen to be the consolidated value, i.e. the value that can be used by the rest of the application software. Some applications request to explicitly know about the physical raw value. This is why this signal is also made available.



**Figure 3.3: Signal Flow within Sensor and Actuator Pattern**

Please be aware: [SensorActuatorSwComponentTypes](#) are the only components that are allowed to access ECU Abstraction Software, namely [EcuAbstraction-SwComponentType](#). This is shown in Figure 3.4 taken from [13]. Access is denoted by "IO".

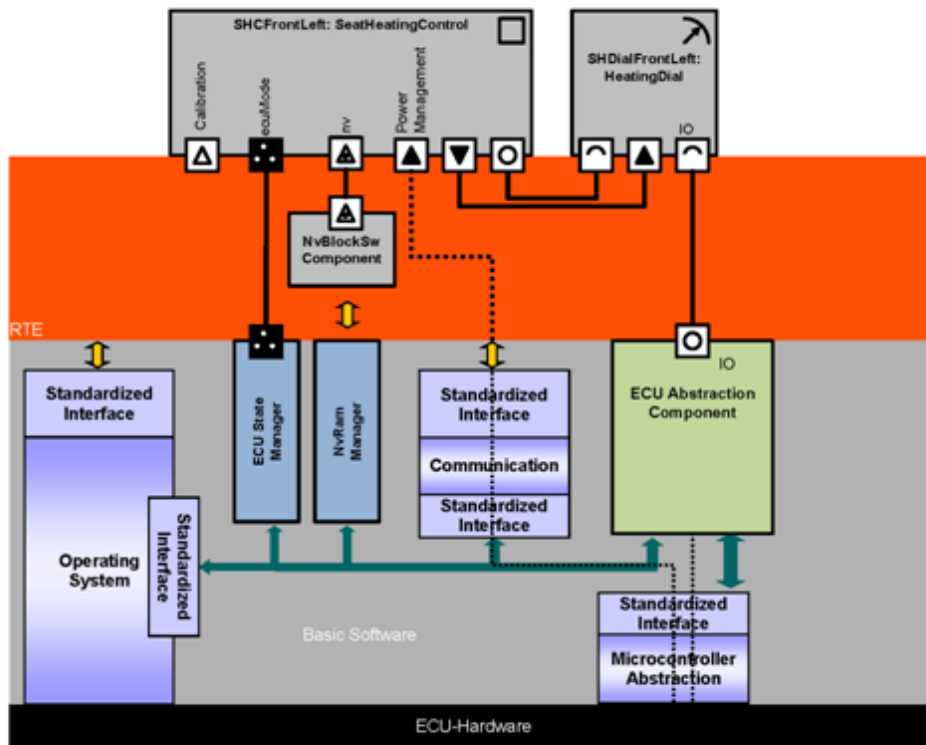


Figure 3.4: Access to ECU Abstraction

### 3.5 Naming

[TR\_AIDPC\_00005] **Naming within PSnsrAct** [In the following the semantic port prototype (blueprint) definition together with the name patterns are described.

The overall name pattern for port short names is described in grammar 3.1. In the following these port (prototype blueprint) names are also referred to as signal names. In Table 3.1 additionally the pattern for the corresponding long names is given.] ([RS\\_MAIN\\_00500](#))

Listing 3.1: Name Pattern for Ports in Device Abstraction

```

grammar PSnsrActrPortNames;

portName
    :   { 'sensorActuatorSignal' } ;

sensorActuatorSignal
    :   { anyName } { 'sensorActuatorSignalType' } ;

sensorActuatorSignalType
    :   ( ElecRaw | ElecBascFld | Raw | Measd | Consold | Estimd | Outp |
        Sp | Reqd ) ;
    
```

**anyName**

```
: ('keyword')* ;
```

In case of a generic long name {anyLongNamePart} or {anyLongName}, resp., is empty.

Generic Signal Name	Long Name Pattern of Concrete Sensor/Actuator Signal (EN)	Generic Long Name of Signal (EN)	AUTOSAR Definition
ElecRaw	Electrical Raw Value of {anyLongNamePart}	Electrical Raw Value	Electrical raw sensor value as provided by the ECU Abstraction. Typically this value is unfiltered. However, there are for example smart components doing some filtering themselves. This signal can only be represented in voltage, current, (period) time, binary value, frequency, dutycycle [12].
ElecBascFild	Electrical Basic Filtered Value of {anyLongNamePart}	Electrical Basic Filtered Value	Basic filtered electrical raw sensor value (e.g. maximum allowed phase shift is one scheduling raster or maximum 360 degree crankshaft rotation if exhaust gas pulsation dependent). Electrical representation of a technical signal [12]. This signal can only be represented in voltage, current, (period) time, binary value, frequency, dutycycle.
Raw	Raw Value of {anyLongNamePart}	Raw Value	Physical raw/base sensor value. Simple conversion of basic filtered electrical (ElecBascFild) to physical value.
Measd	{anyLongName} (Measured)	Measured Value	Final filtered and offset corrected physical sensor value. Physical sensor value/standard sensor value. The physical sensor value is the linearized/filtered physical raw/base sensor value including offset. At this step a (significant) phase-shift could be possible.
Consold	{anyLongName}	Value	Consolidated physical value, either a measured value (Measd) or a modeled value (Estimd). Final filtered and offset corrected consolidated actuator value/physical sensor value. Virtual physical sensor value/fused sensor value that comes as close as possible to the technical signal. In case of inability to provide a physical sensor value (e.g. failure, implausibility or other reasons) a substitute value/default value or a frozen value is provided.
Estimd	{anyLongName} (Estimated)	Estimated Value	Modelled value physical sensor value/standard sensor value. Can be used as a replacement for final filtered and offset corrected physical sensor value. The interface is optional.

Outp	Output of {any-LongNamePart}	Output Value	Final controller output (closed loop or open loop). It includes the necessary control actions to reach the requested setpoint in the given system conditions. For example for realizing the requested actuator position a precontrol impulse to overcome the static friction is needed. In case of a smart actuator the output value might add a dedicated initialization duty cycle to wakeup the actuator. Typically expressed as percentage.
Sp	Setpoint {anyLong-NamePart}	Setpoint Value	Final actuator setpoint. Typically expressed as percentage.
Reqd	Requested Setpoint {anyLong-NamePart}	Requested Setpoint	Final requested physical setpoint. Typically expressed as percentage but could also be expressed e.g. as factor.
Cpby	Capability {any-LongNamePart}	Capability	Provides the dynamic instant capability typically based on output limitation but could also contain the limitation on rate of change of the consolidated value. It is expressed as percentage.

**Table 3.1: Signal Names and Semantics**

Some examples of short and long names for sensor/actuator signals or ports, resp., are given in Table 3.2.

Short Name	Class	Long Name (EN)
TrboChrgrReqd	<a href="#">PortPrototype</a>	Requested Setpoint for Turbo Charger
Consold	<a href="#">PortPrototype</a>	Consolidated Value
TrboChrgrStg3AtBnk2	<a href="#">FlatInstanceDescriptor</a>	Value of Turbo Charger at Third Stage at Second Bank
TrboChrgr	<a href="#">PortPrototype</a>	Value of Turbo Charger

**Table 3.2: Port Names Examples**

In grammar 3.2 the pattern for component types and component prototypes for the atomic components within a composition representing a sensor or an actuator is described.

In some cases there might be parts of the implementation that can be reused for different sensors/actuators. Therefore the name pattern for the component type name is more generic and does not necessarily contain the Sensor/Actuator name. In other cases the Sensor/Actuator names are not sufficient to make the component type names unique so an additional identifier can be added to the component type name.

**Listing 3.2: Name Pattern for Atomic Software Component Types in Device Abstraction**

grammar [PSnsrActrAtomicSwcShortName](#);

[sensorActuatorComponentTypeName](#)  
 : [sensorActuatorComponentName](#) ;

```

sensorActuatorComponentPrototypeName
    :   sensorActuatorComponentName ;

sensorActuatorComponentName
    :   ( Drv{Device}Elec | DevDrv{Device} | Dev{Device}Virt | DevCoorrVirt
        ) ('anyNamePart' ) ;

Device
    :   ( Snsr | Actr ) ;

anyNamePart
    :   ('keyword')* ;
    
```

In grammar 3.3 the pattern is more refined but still conforming to grammar 3.2 because "For" is a standardized keyword. Note: the refined grammar is following [TR\_SWNR\_0034] that requests that field blocks are concatenated by adding an appropriate preposition.

### Listing 3.3: Refined Name Pattern for Atomic Software Component Types in Device Abstraction

```

grammar PSnsrActrAtomicSwcShortNameRefined;

sensorActuatorComponentTypeName
    :   sensorActuatorComponentName ;

sensorActuatorComponentPrototypeName
    :   sensorActuatorComponentName ;

sensorActuatorComponentName
    :   ( Drv{deviceType}Elec | DevDrv{deviceType} | Dev{deviceType}Virt |
        DevCoorrVirt) ({{device}}) ;

deviceType
    :   ( Snsr | Actr ) ;

device
    :   ( For{sensor}('anyNamePart') | For{actuator}('anyNamePart') ) ;

sensor
    :   'anyName' ;

actuator
    :   'anyName' ;

anyName
    :   ('keyword')* ;

anyNamePart
    :   ('keyword')* ;
    
```

In grammar 3.4 the pattern for the corresponding English long names of the components is described.

### Listing 3.4: Pattern for English Long Names Atomic Software Component Types in Device Abstraction

```

grammar PSnsrActrAtomicSwcLongName;

sensorActuatorComponentLongName
    :   sensorActuatorComponentName ;

sensorActuatorComponentLongName
    :   ('anyLongName') ( Electrical Sensor Driver | Sensor Device Driver |
        Virtual Device Driver | Electrical Actuator Driver | Actuator Device
        Driver | Virtual Device Coordinator) ('anyLongNamePart') ;

anyLongName
    :   ('keyword') * ;

anyLongNamePart
    :   ('keyword') * ;
    
```

In Table 3.3 the generic sensor and actuator component short and long names are shown as pairs.

Generic Short Name Pattern	Generic Long Name (EN)
DrvrSnsrElec	Electrical Sensor Driver
DevDrvrSnsr	Sensor Device Driver
DevSnsrVirt	Virtual Device Driver
DrvrActrElec	Electrical Actuator Driver
DevDrvrActr	Actuator Device Driver
DevCoorrVirt	Virtual Device Coordinator

**Table 3.3: Sensor and Actuator Component Name Patterns**

Short Name	Class	Long Name (EN)
DrvrActrElecForTle8209	<a href="#">SensorActuatorSwComponentType</a>	TLE8209: Electrical Sensor Driver
DrvrActrElecForTrboChrgr	<a href="#">SwComponentPrototype</a>	Turbo Charger: Electrical Sensor Driver
DevSnsrVirtForAnyTSnsr	<a href="#">ApplicationSwComponentType</a>	Virtual Device Driver for Any Temperature Sensor
DevSnsrVirtForTrboChrgr	<a href="#">SwComponentPrototype</a>	Turbo Charger: Virtual Device Driver
TrboChrgrAcmeT064	<a href="#">CompositionSwComponentType</a>	Turbo Charger: ACME T064
TrboChrgrStg3AtBnk2	<a href="#">SwComponentPrototype</a>	Turbo Charger at Third Stage at First Bank

**Table 3.4: Examples for Sensor and Actuator Names**

In grammar 3.5 a pattern is described how to refine 'anyNamePart' as defined in grammar 3.3 in case of a system with several banks and stages. In Table 3.5 corresponding name examples are shown using this grammar part.

### Listing 3.5: Name Pattern for Signals in Device Abstraction in Case of a System with Several Banks

```

grammar PSnsrActrStgBnkShortNames;
    
```

```
stageBank
    : (Stg{'indexStg'}(AtBnk{'indexBnk'})) ;
```

```
indexStg
    : ( 1st | 2nd | 3rd ) ;
```

```
indexBnk
    : ( 1st | 2nd | 3rd ) ;
```

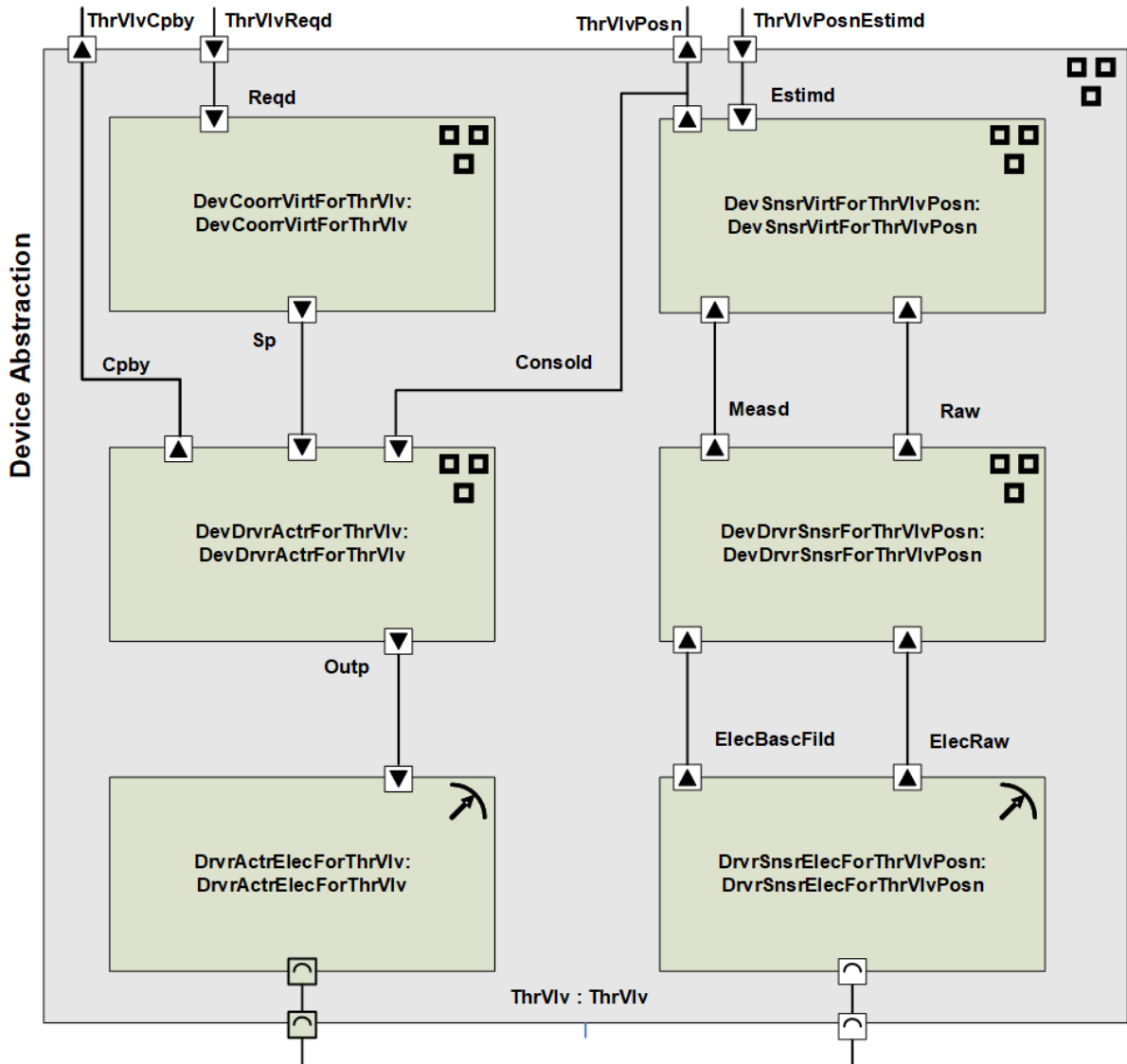
Short Name	Class	Long Name (EN)
TrboChrgrStg3rdAtBnk1st	<a href="#">PortPrototype</a>	Value of Turbo Charger at Third Stage at First Bank
TrboChrgrStg3rdAtBnk2nd	<a href="#">SwComponentPrototype</a>	Turbo Charger at Third Stage at Second Bank

**Table 3.5: Examples for Sensor and Actuator Names**

## 3.6 Example

### 3.6.1 Throttle Valve

Figure 3.5 shows an example device abstraction for a throttle valve.

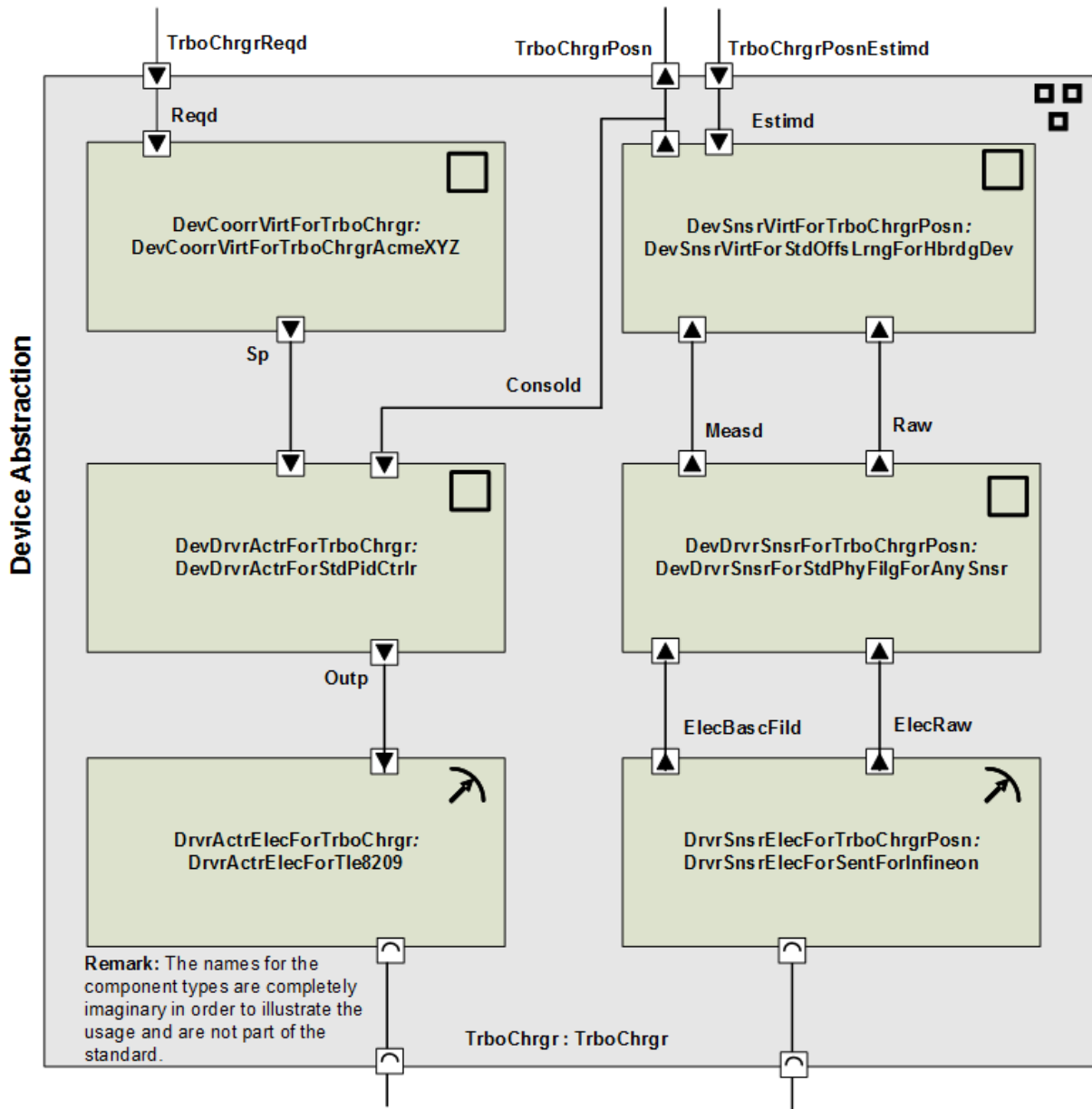


**Figure 3.5: Device Abstraction for a Throttle Valve**

### 3.6.2 Turbo Charger

In Figure 3.6 an example of a closed looped controlled device with position feedback — a turbo charger — is shown.



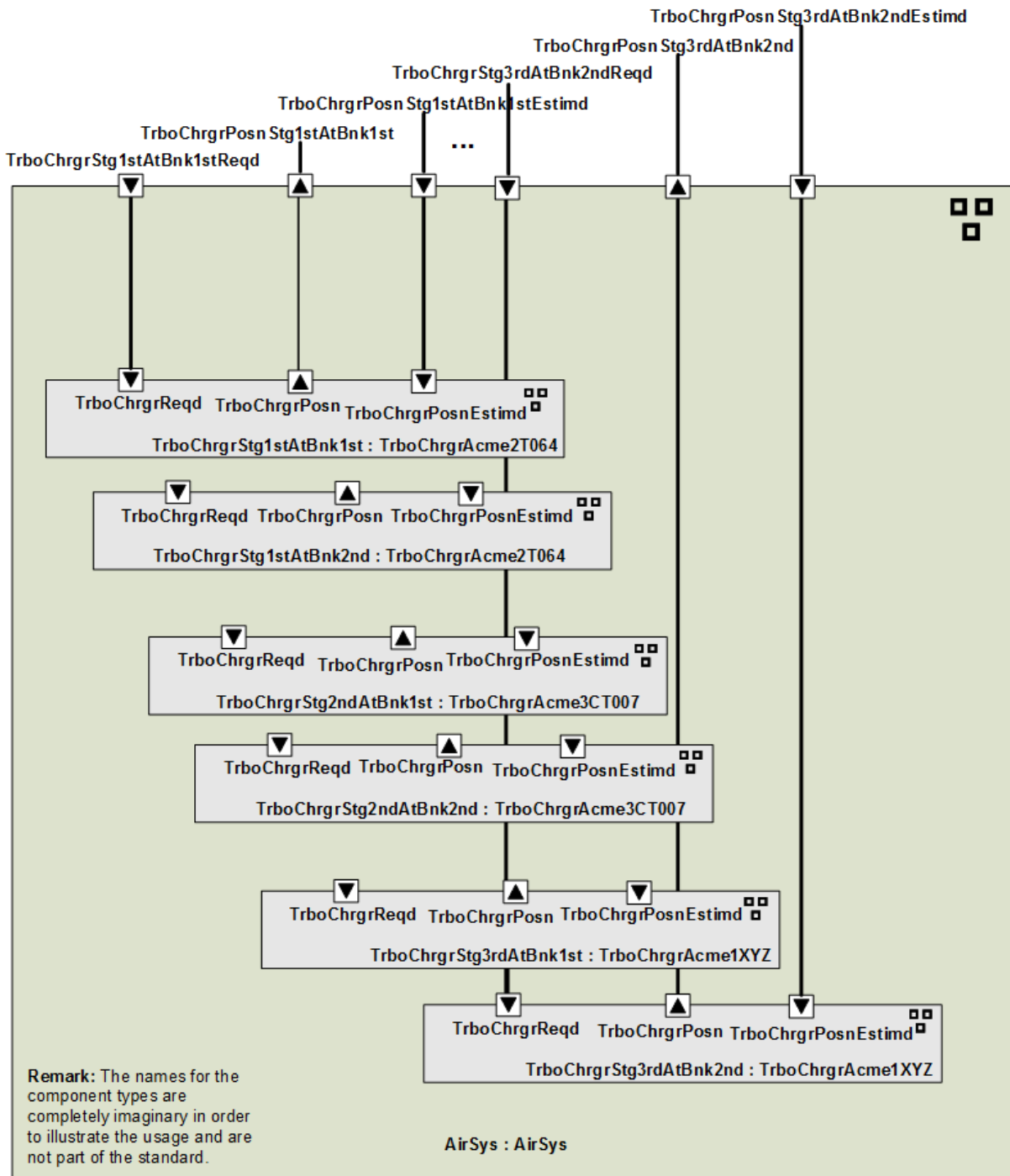


**Figure 3.6: Device Abstraction for a Turbo Charger**

Hint: In most cases it is not recommended to use company names in model names (like "AcmeXYZ" used in the Figures). Company names etc. are only used in the examples to show the difference between type and prototype and what is the reason for the difference. For general rules and recommendations how to deal with variants in models, as for example expressed by the company names in the examples, please refer to the modeling guides and templates.

### 3.6.3 Turbo Charger with Stages and Banks

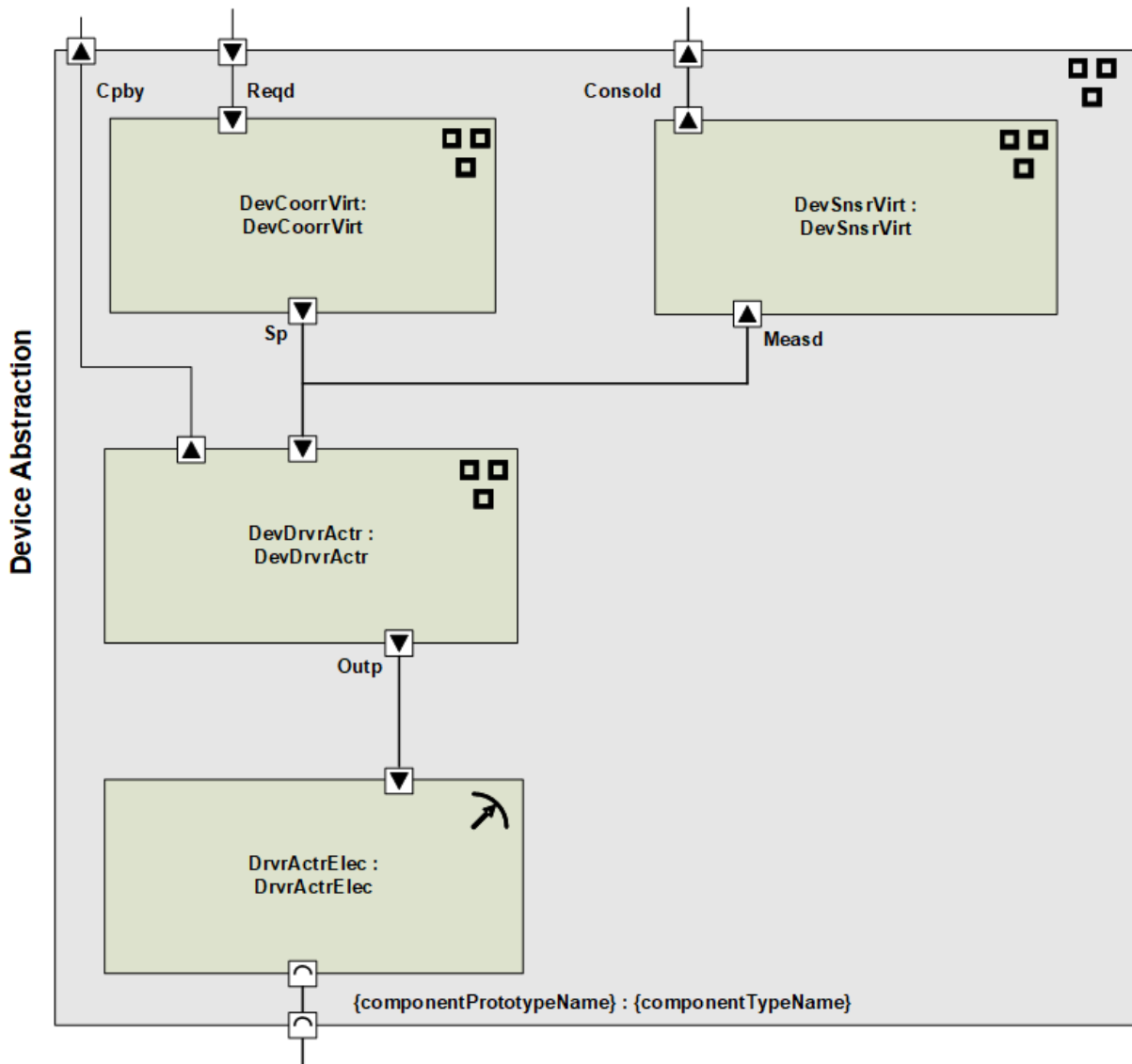
In Figure 3.7 a project system configuration for turbo charger with several stages and banks is shown.



**Figure 3.7: Device Abstraction for a Turbo Charger with Banks and Stages**

### 3.6.4 Actuator without Feedback Loop

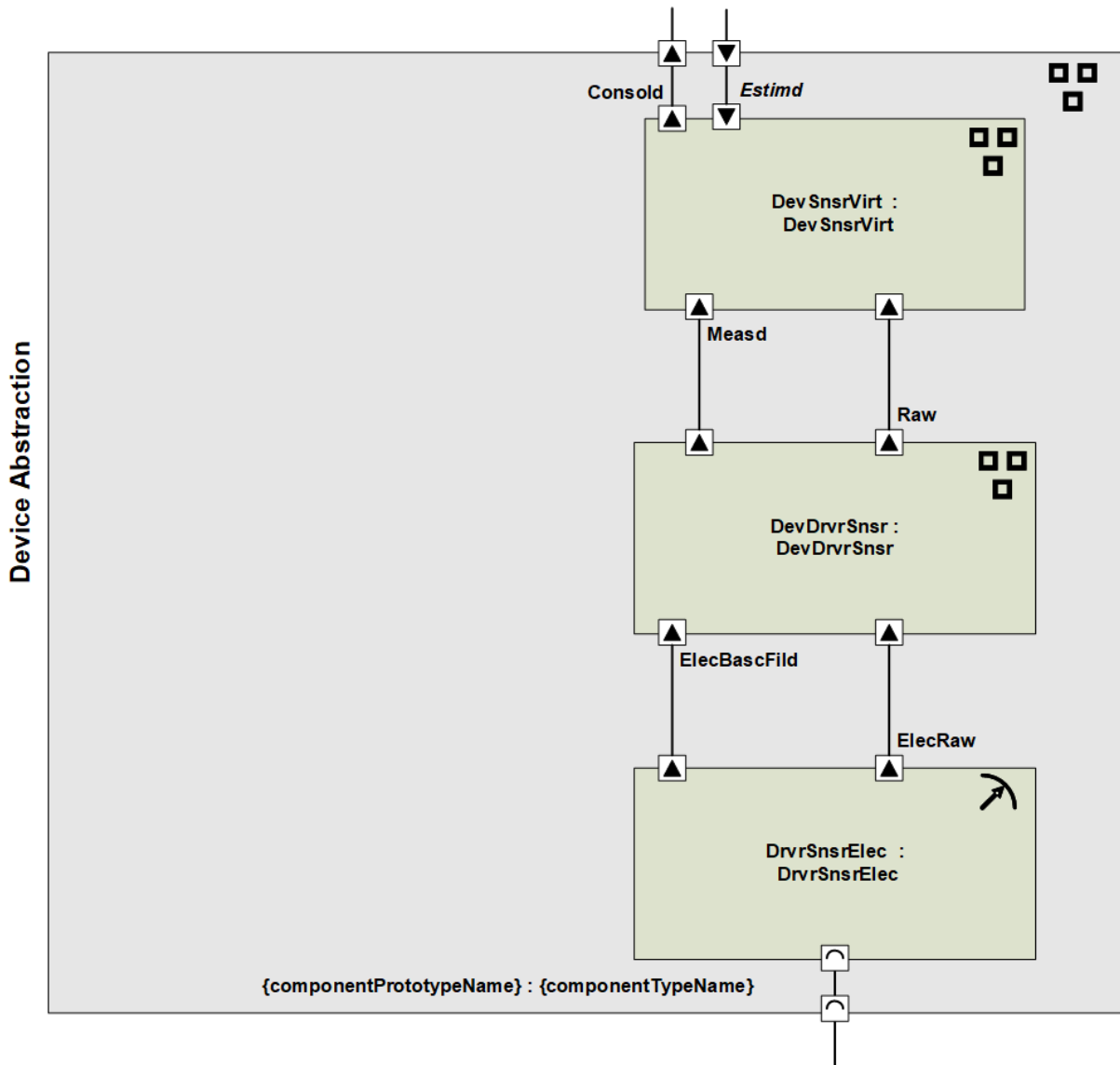
In Figure 3.8 an open loop controlled actuator is shown that calculates the consolidated value using the setpoint input as input. As described before there are alternatives how to calculate the consolidated value. No estimated value (*Estimd*) is used in this example.



**Figure 3.8: Example Actuator without Feedback Loop (Setpoint Alternative)**

### 3.6.5 Standard Sensor

In Figure 3.9 a design pattern of blueprint components for a standard sensor is shown.



**Figure 3.9: Device Abstraction for Standard Sensor**

### 3.6.6 Standard Sensor for Environment Temperature

In Figure 3.10 a standard sensor for environment temperature is shown.

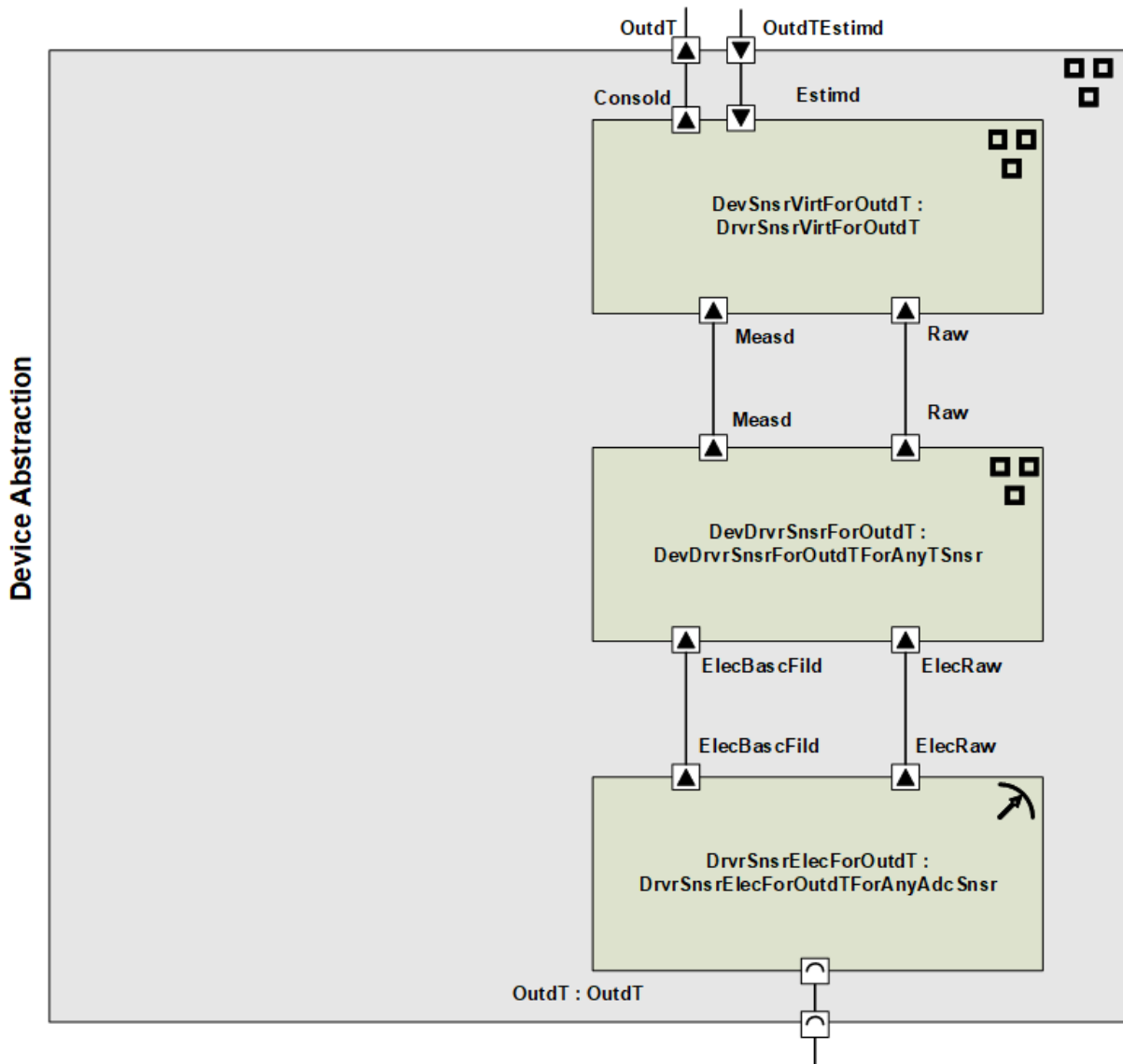
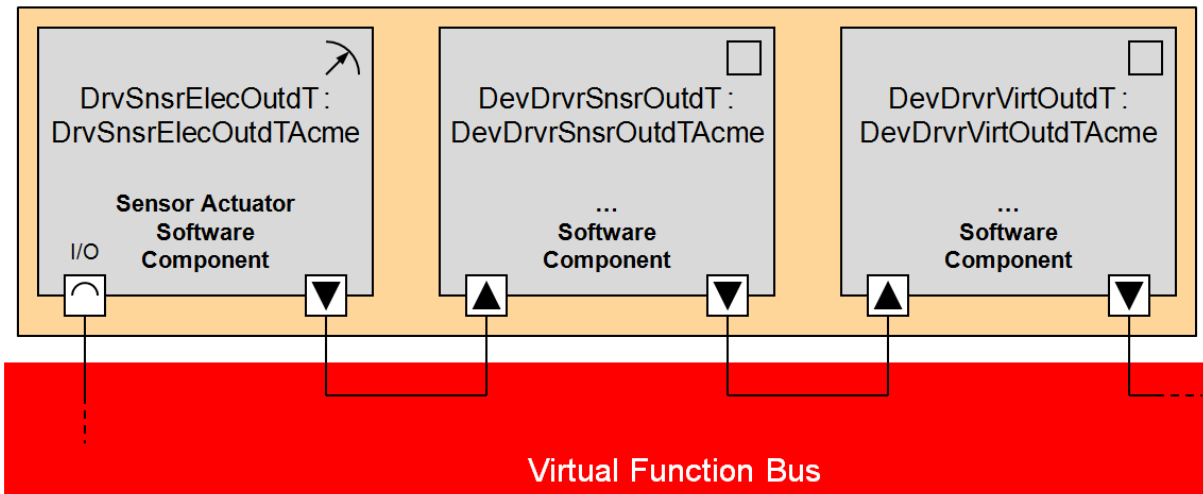


Figure 3.10: Device Abstraction for a Sensor measuring the Environment Temperature

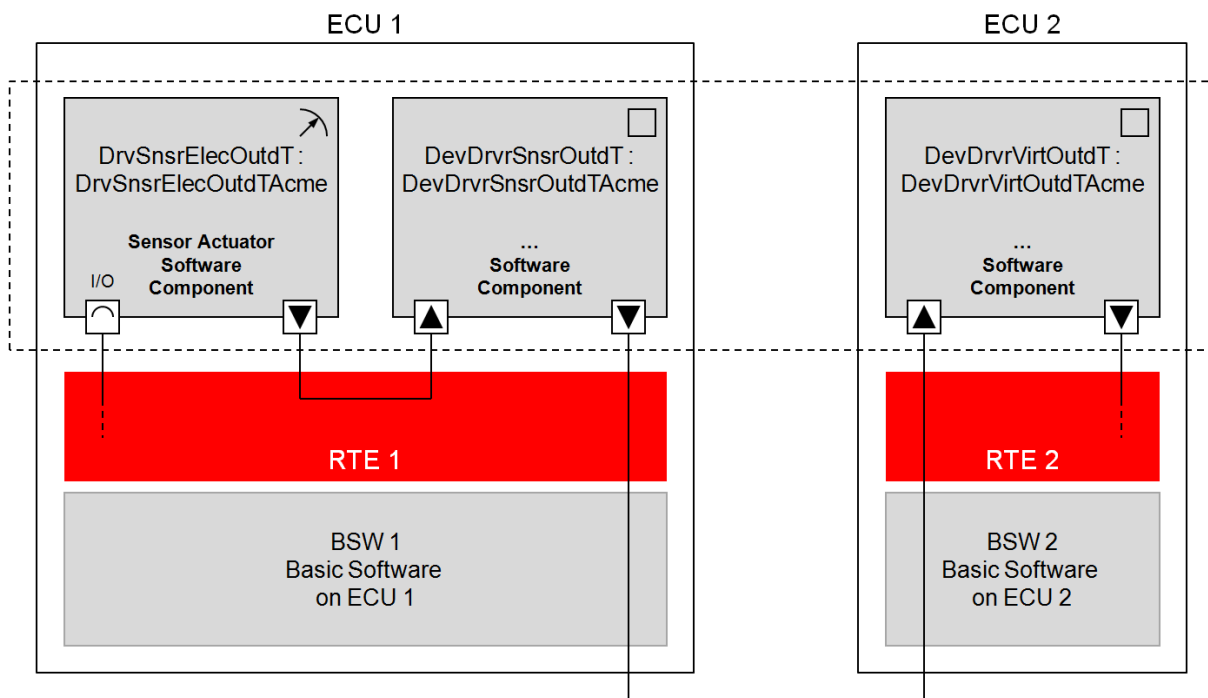
### 3.6.7 Distributing Device Abstraction

In Figure 3.12 the ECU view derived from the VFB view of a temperature sensor as shown in Figure 3.11 is shown. Finally it is shown that it is possible to also deploy the different SW-C to different ECUs. Of course timing constraints have to be considered before distributing components to different ECUs.

Device Abstraction



**Figure 3.11: VFB View of Temperature Sensor Example**



[---] Device Abstraction     RTE Runtime Environment

**Figure 3.12: ECU Views after Distribution of SW-Cs of Temperature Sensor to two ECUs**

### 3.7 Sample Code and Model

In Listing 3.6 a blueprint for the components used in the Sensor/Actuator pattern is provided. The blueprint code is not complete but just gives an idea how it is realized. The composition component is not shown.

Please note that the AUTOSAR meta model requests that a sensor actuator component type references a corresponding sensor or actuator, resp., using a [HwDescriptionEntity](#), [12]. In this case a [HwElement](#) is needed to be used. Since there is a standardized [HwCategory](#) for sensors and actuators also a [HwType](#) is defined that is referenced by the [HwElement](#).

**Listing 3.6: Sensor/Actuator Pattern**

```

<AR-PACKAGE>
  <SHORT-NAME>SwComponentTypes_Blueprint</SHORT-NAME>
  <CATEGORY>BLUEPRINT</CATEGORY>
  <REFERENCE-BASES>
    <REFERENCE-BASE>
      <SHORT-LABEL NAME-PATTERN="{anyName}">HwDescriptionEntity</SHORT-LABEL>
      <IS-DEFAULT>>false</IS-DEFAULT>
      <IS-GLOBAL>>false</IS-GLOBAL>
      <BASE-IS-THIS-PACKAGE>>false</BASE-IS-THIS-PACKAGE>
      <PACKAGE-REF DEST="AR-PACKAGE"><?xm-replace_text {PACKAGE-REF}?></PACKAGE-REF><!--add package path -->
    </REFERENCE-BASE>
    <REFERENCE-BASE>
      <SHORT-LABEL NAME-PATTERN="{anyName}">PortInterfaces_Blueprint</SHORT-LABEL>
      <IS-DEFAULT>>false</IS-DEFAULT>
      <IS-GLOBAL>>false</IS-GLOBAL>
      <BASE-IS-THIS-PACKAGE>>false</BASE-IS-THIS-PACKAGE>
      <PACKAGE-REF DEST="AR-PACKAGE"><?xm-replace_text {PACKAGE-REF}?></PACKAGE-REF><!--add package path -->
    </REFERENCE-BASE>
  </REFERENCE-BASES>
  <ELEMENTS>
    <SENSOR-ACTUATOR-SW-COMPONENT-TYPE>
      <SHORT-NAME NAME-PATTERN="{anyName}DrvrSnsrElec{anyNamePart}">DrvrSnsrElec</SHORT-NAME>
      <LONG-NAME>
        <L-4 L="EN">Driver for Electrical Signals of Sensor</L-4>
      </LONG-NAME>
      <INTRODUCTION><!-- optional: add documentation -->
      </INTRODUCTION>
      <PORTS>
        <P-PORT-PROTOTYPE>
          <SHORT-NAME NAME-PATTERN="{anyName}ElecRaw{anyNamePart}">ElecRaw</SHORT-NAME>
          <LONG-NAME>
            <L-4 L="EN">Electrical Raw Value</L-4>
          </LONG-NAME>
        </P-PORT-PROTOTYPE>
      </PORTS>
    </SENSOR-ACTUATOR-SW-COMPONENT-TYPE>
  </ELEMENTS>
</AR-PACKAGE>

```

```

        <PROVIDED-INTERFACE-TREF DEST="SENDER-RECEIVER-INTERFACE"
            BASE="PortInterfaces_Blueprint">ElecRaw1</PROVIDED-
            INTERFACE-TREF>
    </P-PORT-PROTOTYPE>
    <P-PORT-PROTOTYPE>
        <SHORT-NAME NAME-PATTERN="{anyName}ElecBascFild{anyNamePart}"
            >ElecBascFild</SHORT-NAME>
        <LONG-NAME>
            <L-4 L="EN">Electrical Basic Filtered Value</L-4>
        </LONG-NAME>
        <PROVIDED-INTERFACE-TREF DEST="SENDER-RECEIVER-INTERFACE"
            BASE="PortInterfaces_Blueprint">ElecBascFild1</PROVIDED-
            INTERFACE-TREF>
    </P-PORT-PROTOTYPE>
</PORTS>
<!-- add correct reference to sensor actuator type -->
<SENSOR-ACTUATOR-REF DEST="HW-DESCRIPTION-ENTITY" BASE="
    HwDescriptionEntitys">SensorActuatorType</SENSOR-ACTUATOR-REF>
</SENSOR-ACTUATOR-SW-COMPONENT-TYPE>
<APPLICATION-SW-COMPONENT-TYPE>
    <SHORT-NAME NAME-PATTERN="DevDrvrSnsr{anyNamePart}">DevDrvrSnsr</
    SHORT-NAME>
    <LONG-NAME>
        <L-4 L="EN">Device Driver for Sensor</L-4>
    </LONG-NAME>
    <!-- Ports to be added -->
</APPLICATION-SW-COMPONENT-TYPE>
<APPLICATION-SW-COMPONENT-TYPE>
    <SHORT-NAME NAME-PATTERN="DevSnsrVirt{anyNamePart}">DevSnsrVirt</
    SHORT-NAME>
    <LONG-NAME>
        <L-4 L="EN">Virtual Device Driver for Sensor</L-4>
    </LONG-NAME>
    <!-- Ports to be added -->
</APPLICATION-SW-COMPONENT-TYPE>
</ELEMENTS>
</AR-PACKAGE>
<AR-PACKAGE>
    <SHORT-NAME>HwTypes_Blueprint</SHORT-NAME>
    <CATEGORY>BLUEPRINT</CATEGORY>
    <ELEMENTS>
        <HW-TYPE>
            <SHORT-NAME NAME-PATTERN="{anyName}">SensorActuatorType</SHORT-
            NAME>
            <HW-CATEGORY-REFS>
                <HW-CATEGORY-REF DEST="HW-CATEGORY" BASE="HwCategorys">
                    HwCategorys/SensorActuator</HW-CATEGORY-REF>
            </HW-CATEGORY-REFS>
        </HW-TYPE>
    </ELEMENTS>
</AR-PACKAGE>
<AR-PACKAGE>
    <SHORT-NAME>HwElements_Blueprint</SHORT-NAME>
    <CATEGORY>BLUEPRINT</CATEGORY>
    <ELEMENTS>
        <HW-ELEMENT>

```



```

        <SHORT-NAME NAME-PATTERN="{anyName}">mySensorActuatorElement</
        SHORT-NAME>
        <HW-TYPE-REF DEST="HW-TYPE" BASE="HwTypes">HwTypes/
        SensorActuatorType</HW-TYPE-REF>
    </HW-ELEMENT>
</ELEMENTS>
</AR-PACKAGE>
    
```

The `HwCategory`s should be provided centrally because they are standardized. Definition of `HwCategory` "SensorActuator" is shown in Listing 3.7.

**Listing 3.7: HW Categories as used in Sensor/Actuator Pattern**

```

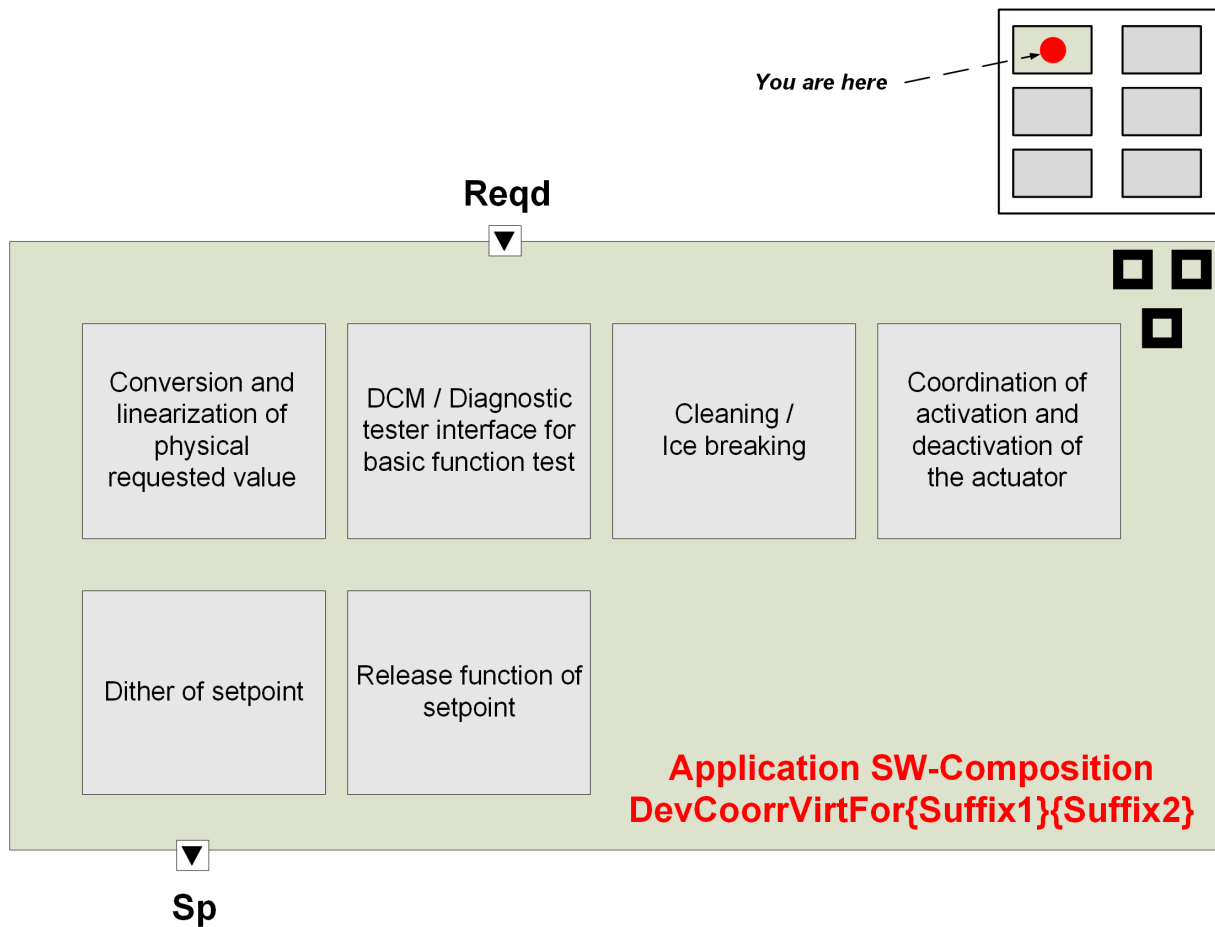
<AR-PACKAGE>
    <SHORT-NAME>HwCategorys_Blueprint</SHORT-NAME>
    <CATEGORY>BLUEPRINT</CATEGORY>
    <ELEMENTS>
        <HW-CATEGORY>
            <SHORT-NAME NAME-PATTERN="blueprintName">SensorActuator</SHORT-
            NAME>
        </HW-CATEGORY>
    </ELEMENTS>
</AR-PACKAGE>
    
```

## 3.8 Typical location of some common function within the specified layers

This chapter is for detailed description of the distribution of features across the device abstraction layers. It provides some examples of some typical and common features and their recommended location within the specified layers of the S/A-Pattern. Scope for this chapter is to make interface standardization easier.

### 3.8.1 Virtual Device Coordinator (DevCoorrVirt)

Virtual device is an abstraction of the physical representation of the actuator.



**Figure 3.13: Typical functions in layer DevCoorrVirt**

### 3.8.1.1 Conversion and linearization of physical requested value

Typically there is a delta between the mechanical endstops and the position where the physical effects are influenced due to the movement of the actuator. This gap could be compensated via offset compensation algorithm of the position sensor or via linearization of the requested setpoint value. The transfer function is used to compensate the actuator HW design/physics.

### 3.8.1.2 DCM service / Diagnostic tester interface for basic function test

The DCM service interface is typically used as a tester interface and can overwrite the requested value to perform a basic function test of the actuator.

### **3.8.1.3 Cleaning / Ice breaking**

Overwrite/Ignore the requested value, in order to prepare the actuator for proper actuation. The function switches between two different setpoint values for a specific time to either

1. condition the actuator for offset learning
2. clean particles/compounds from actuator
3. break up from ice

### **3.8.1.4 Dither of setpoint**

Continuous overlaid/modulated signal on setpoint value to overcome static friction of actuator.

### **3.8.1.5 Release function of setpoint**

The release function is manipulating the requested setpoint value. This could be needed in case of a blocked actuator, i.e. the actuator got stuck at its position.

### **3.8.1.6 Coordination of activation and deactivation of the actuator**

Activation: The actuator shall be activated as soon as actuation is requested.

Deactivation: To ensure safe operation, the actuator shall be shut off under certain conditions (incl. monitoring e.g. open hood) and shall be shut off to fail safe before voltage supply is switched off.

### 3.8.2 Actuator Device Driver (DevDrvrActr)

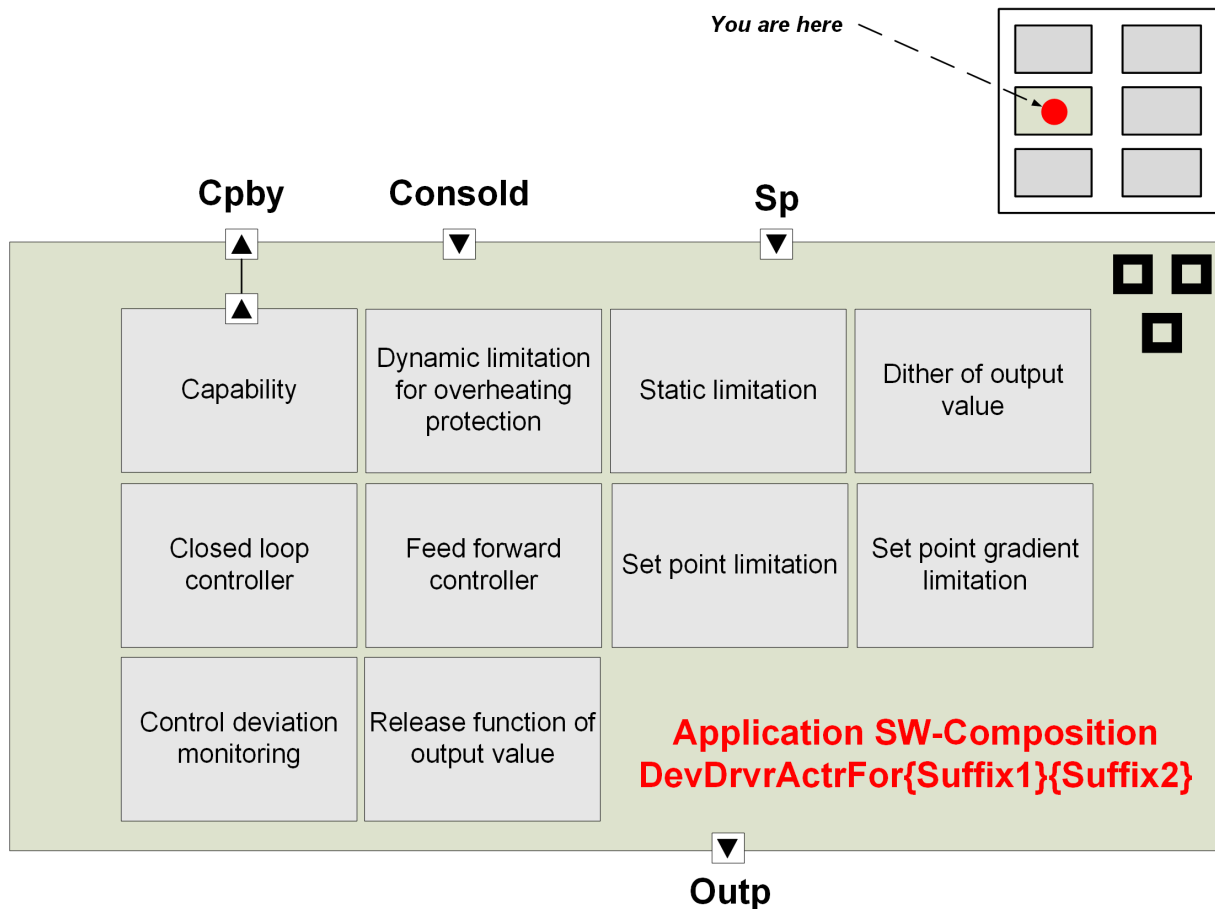


Figure 3.14: Typical functions in layer DevDrvrActr

#### 3.8.2.1 Dither of output value

Continuous overlaid/modulated signal on output value to overcome static friction of actuator.

#### 3.8.2.2 Release function of output value

The release function is manipulating the output value. This could be needed in case of a blocked actuator, i.e. the actuator got stuck at its position.

### **3.8.2.3 Limitation**

#### **3.8.2.3.1 Static limitation**

The output value is limited to protect the actuator from any mechanical or thermal damage at a static position. It is a static limitation of the output value.

Example: Limitation of dutycycle at the mechanical endstops, e.g. to avoid overheating.

#### **3.8.2.3.2 Dynamic limitation for overheating protection**

Effective current monitoring + housing/motor temperature monitoring is used as overheating protection. To protect the actuator of overheating, the energy input to the actuator or the temperature inside the actuator is observed. It is a dynamic limitation of the output value.

Hint: The temperature information could also come as a consolidated value from an abstracted sensor SW component.

#### **3.8.2.4 Feed forward controller**

The Feed Forward Controller compensates the influence of the known disturbances in the controlled system. It calculates the pre-controlled output value.

#### **3.8.2.5 Closed loop controller**

The Closed Loop Controller uses feedback to control output of a dynamic system, i.e. the output value is adapted according to the consolidated value.

#### **3.8.2.6 Set point limitation**

Set point limitation given by plant used as closed loop controller input.

#### **3.8.2.7 Set point gradient limitation**

Protection of the actuator by limiting the set point gradient, e.g. in position close to the endstops.

#### **3.8.2.8 Control deviation monitoring**

Monitoring of the permanent deviation between setpoint and consolidated value.

### 3.8.2.9 Capability

Providing a Capability is a way of summarizing all active limitations on an actuator. The Capability is related to the requested set point, providing the dynamic boundaries of possible usage.

For example, an electric machine actuator SW composition will report its capability to the coordinator functionality in the application software. If the capability is reduced, the coordinator functionality in the application software may use this capability information to redistribute the requested set points differently between the actuators of the system to obtain the overall system control objective.

Generic Signal Name	Long Name Concrete Sensor/Actuator Signal (EN)	Pattern of	Generic Long Name of Signal (EN)	AUTOSAR Definition
Cpby	Capability	{anyLong-NamePart}	Capability	Provides the dynamic instant capability typically based on output limitation but could also contain the limitation on rate of change of the consolidated value. It is expressed as percentage.

**Table 3.6: Signal Names and Semantics of function Capability**

This following section presents examples of capability.

The capability can be described as the temporary dynamic bounds of actuation. These bounds could depend on current working point of operation or some consolidated value. The capability is provided as percentage of maximum defined actuator limitations.

For example, if the capability is provided as neutral (see figure 3.15), the capability is set to 100%. Consequently, neutral capability does not reflect the current effectiveness of the actuator.



**Figure 3.15: Example for providing neutral Capability information**

In another example (see figure 3.16), the capability is provided as a function of the set point and output limitations. The dynamic set point and output limitations may then also be a function of the consolidated value.

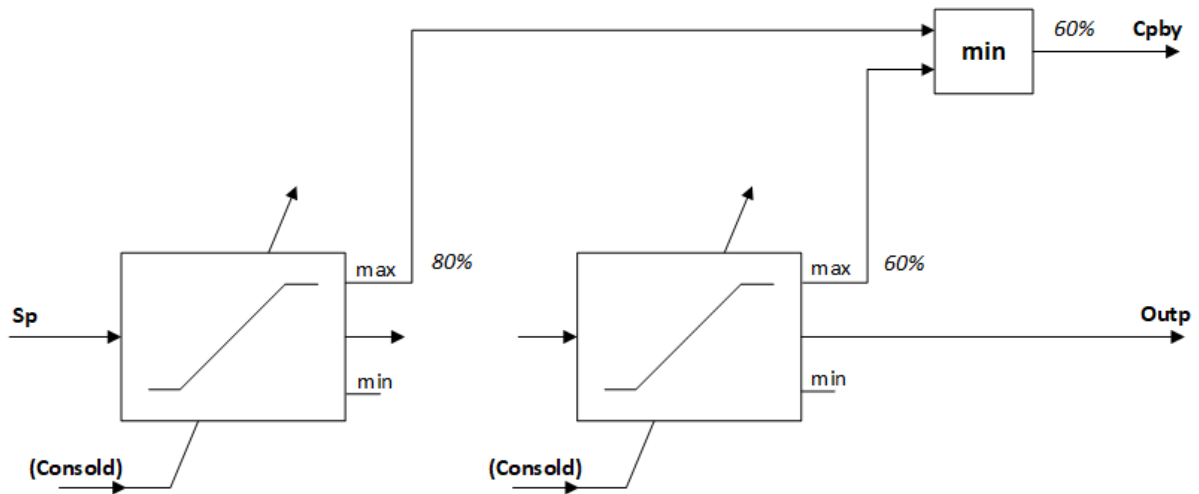


Figure 3.16: Example for simple Capability calculation

### 3.8.3 Electrical Actuator Driver (DrvrActrElec)

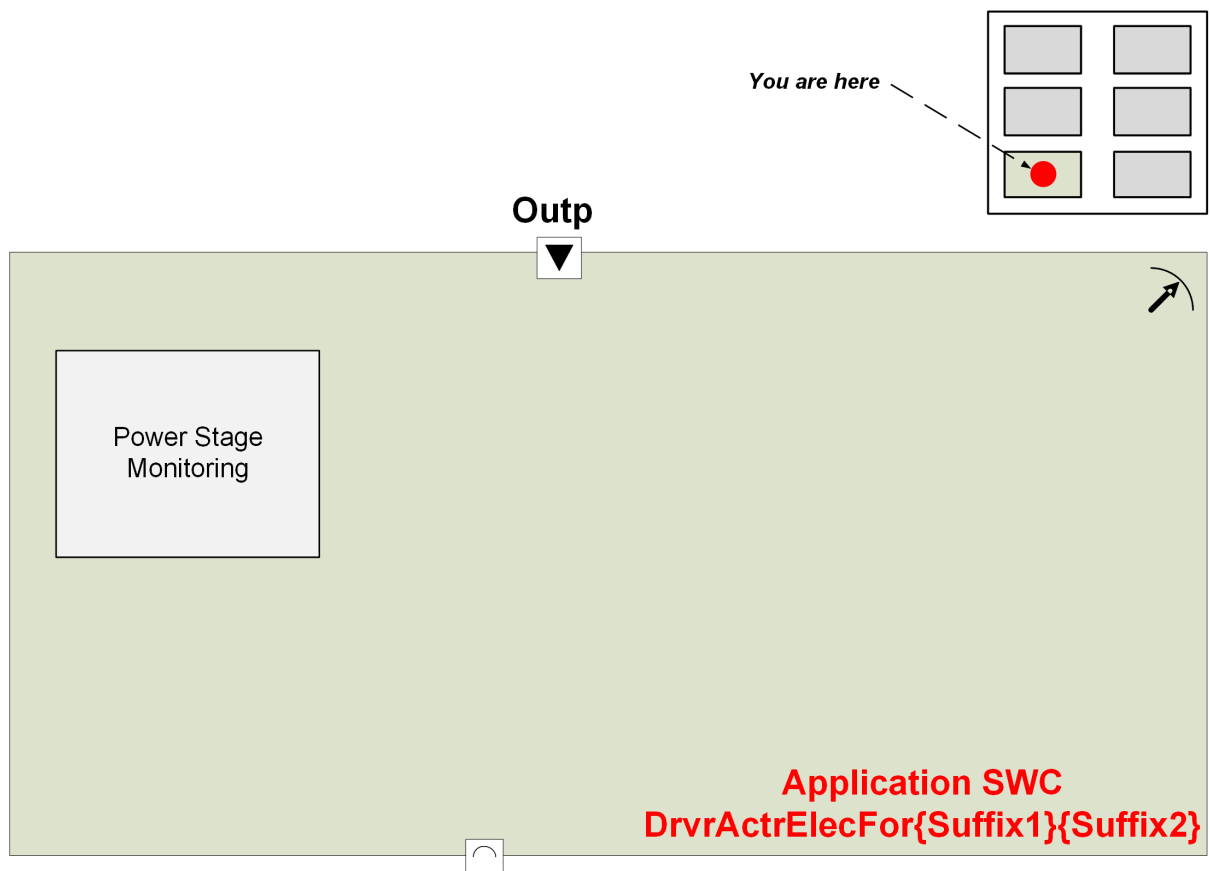


Figure 3.17: Typical functions in layer DrvrActrElec

### 3.8.3.1 Power stage monitoring

An ECU might contain various power stages for driving different electrical loads. Common electrical faults at power stages are Short Circuit to Battery (SCB), Short Circuit to Ground (SCG), and Open Load (OL). These faults can occur during either on-state or off-state of the power stage output.

### 3.8.4 Virtual Device Driver (DevSnsrVirt)

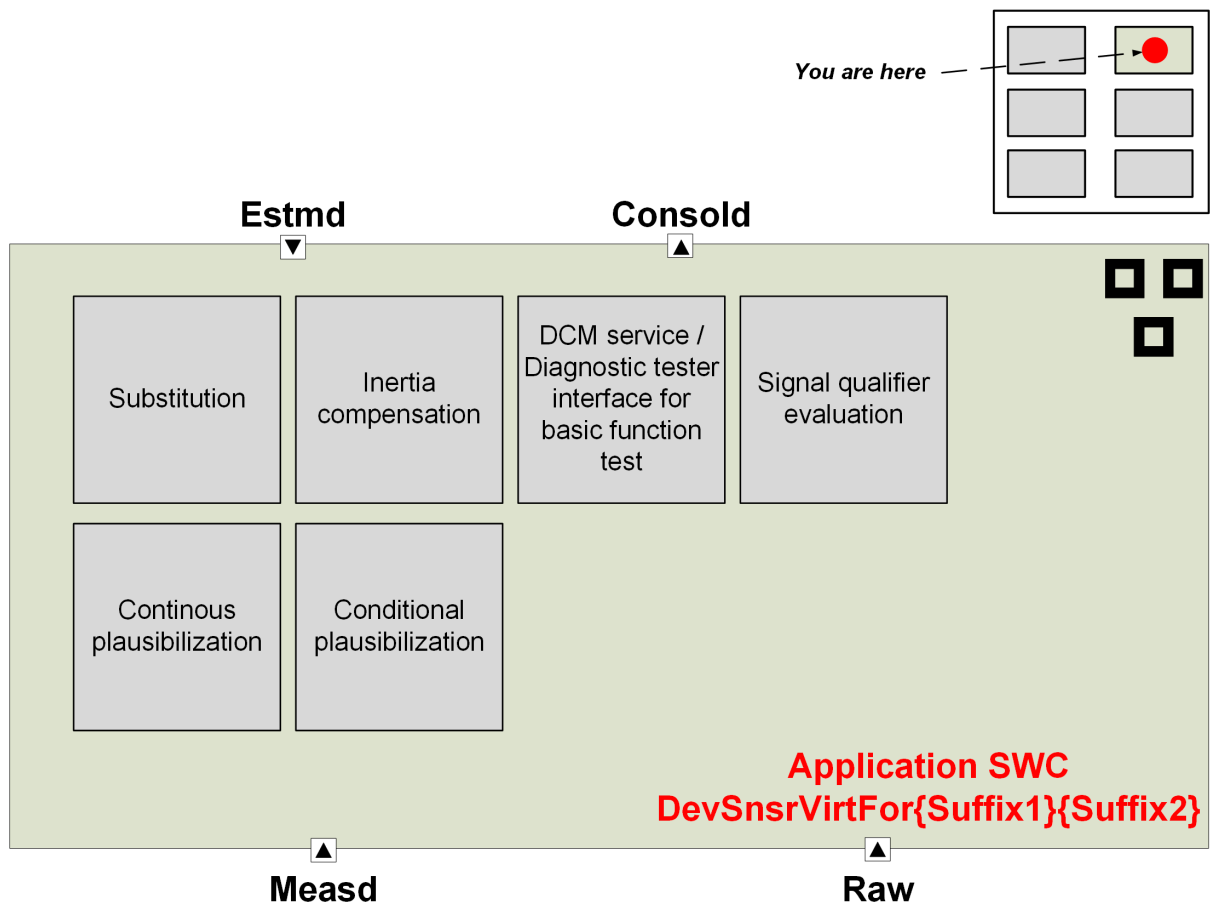


Figure 3.18: Typical functions in layer DevSnsrVirt

#### 3.8.4.1 Substitution

The function switches between the measured and a replacement value. The replacement value could be the estimated value.

Example: The switching can happen based on:

1. Sensor diagnostic information



2. Sensor signal quality
3. Sensor availability

### **3.8.4.2 Inertia compensation**

The function provides a predicted sensor value (forecast) to compensate the inertia of the sensor.

Examples: thermal inertia, mechanical inertia

### **3.8.4.3 Signal qualifier evaluation**

The quality of the consolidated value is provided by that function. It is determined by checking consolidated value and all sensor related diagnosis information.

### **3.8.4.4 DCM service / Diagnostic tester interface for basic function test**

The DCM service interface is typically used to overwrite and stimulate the consolidated sensor value.

### **3.8.4.5 Plausibilization**

#### **3.8.4.5.1 Continuous plausibilization**

The measured value is checked continuously against another redundant sensor information. This redundant sensor information can be provided by any other sensor or by the estimated value.

Example: Offset diagnosis, in case difference (measured value vs. redundant value) exceeds certain threshold, e.g. tolerance threshold.

#### **3.8.4.5.2 Conditional plausibilization**

The measured value is checked at specific points in time (e.g. once in a driving cycle or at specific driving modes) against another redundant sensor information. This redundant sensor information can be provided by any other sensor or by the estimated value.

Hint: The conditional plausibilization can be used to compensate or just identify sensor individual tolerances.

### 3.8.5 Sensor Device Driver (DevDrvrSnsr)

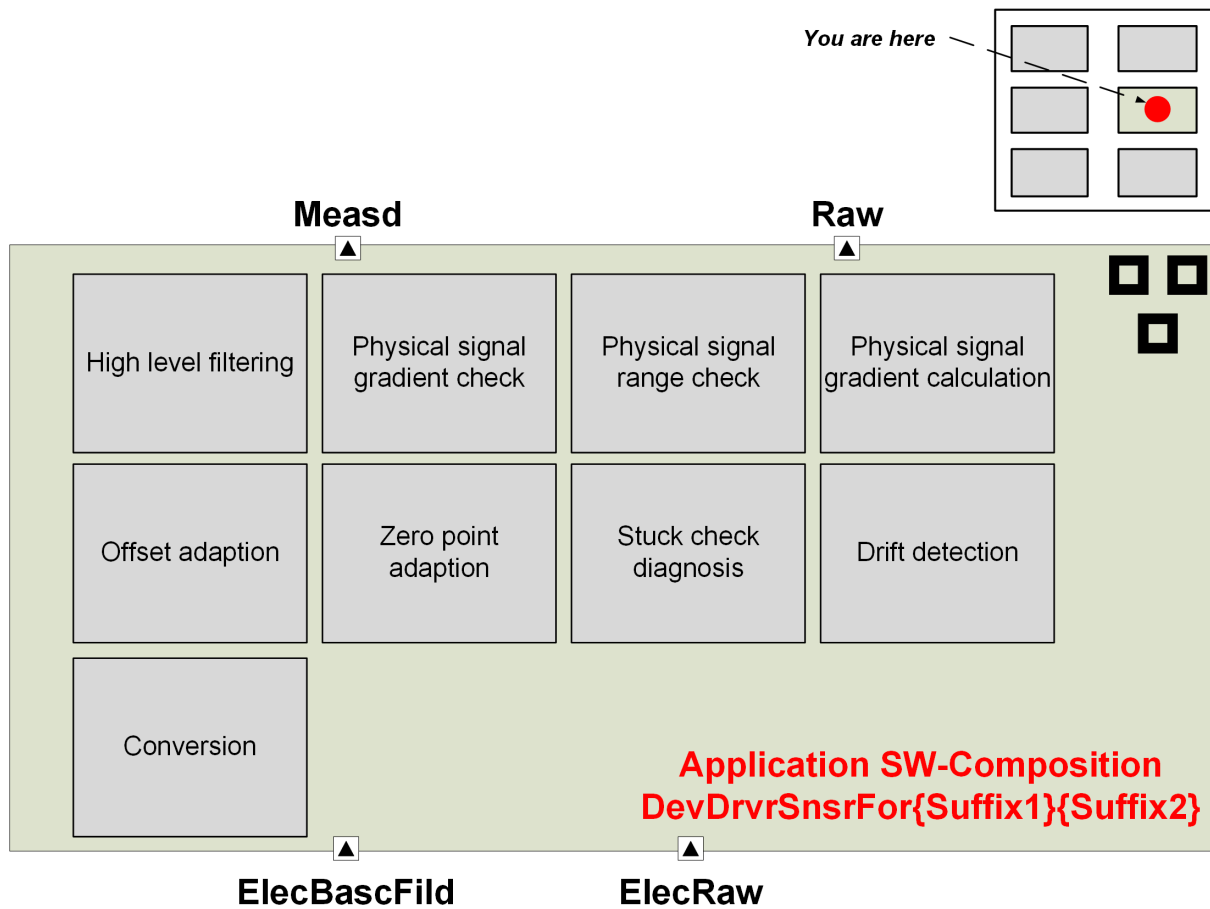


Figure 3.19: Typical functions in layer DevDrvrSnsr

#### 3.8.5.1 High level filtering

This function block contains every kind of filter which might lead to a significant phase shift of the sensor value in order to provide a physical sensor value, fitting to requirements from user functions (regarding timing, accuracy).

Hint: Therefore a good trade-off between phase shift and accuracy has to be found.

#### 3.8.5.2 Offset adaption

The result of conditional plausibilization can be used to do an offset adaption of measured value to compensate individual tolerances of the sensor. The determined offset information is used to adapt the sensor signal to show values closer to the actual physical signal.

Hint: The conditional plausibilization can be used to compensate or just identify sensor individual tolerances.

### **3.8.5.3 Zero point adaption**

The zero point adaption is used to adjust the transfer function in the conversion to the physical zero point.

Hint: The adaption of this zero point is done within the conversion block.

Example 1: Sensors measuring relative values (differential pressure) shall show 0 if there is equalized pressure.

Example 2: The sensor value is adapted to the mechanical endstop position of an closed loop operated actuator.

### **3.8.5.4 Drift detection**

Sensor values are monitored throughout the driving cycle and used to derive a sensor deviation compared to the first and last learned value.

Hint: Can be used for offset adaption, to improve sensor information or it can be used for diagnosis purpose only.

### **3.8.5.5 Conversion**

The electrical signal is converted into physical representation by transfer function. In case of nonlinear signal, linearization will be part of transfer function as well.

### **3.8.5.6 Physical signal gradient calculation**

In order to get information about the current dynamic of the sensed system, a gradient is calculated based on current and previous sensor information.

### **3.8.5.7 Physical signal gradient check**

The gradient of the physical signal is checked against a maximum. For certain sensors a maximum gradient should not be exceeded. In case the sensor shows a higher gradient, it could be indicated as defect.

### **3.8.5.8 Stuck check diagnosis**

Identify a "frozen" sensor information, in case the sensor signal does not change. A permanent "frozen" sensor information could be indicated as a defect.

### 3.8.5.9 Physical signal range check

Comparison of physical sensor signal against minimum and maximum thresholds for continuous diagnosis of physical limits.

### 3.8.6 Electrical Sensor Driver (Drvrsnselec)

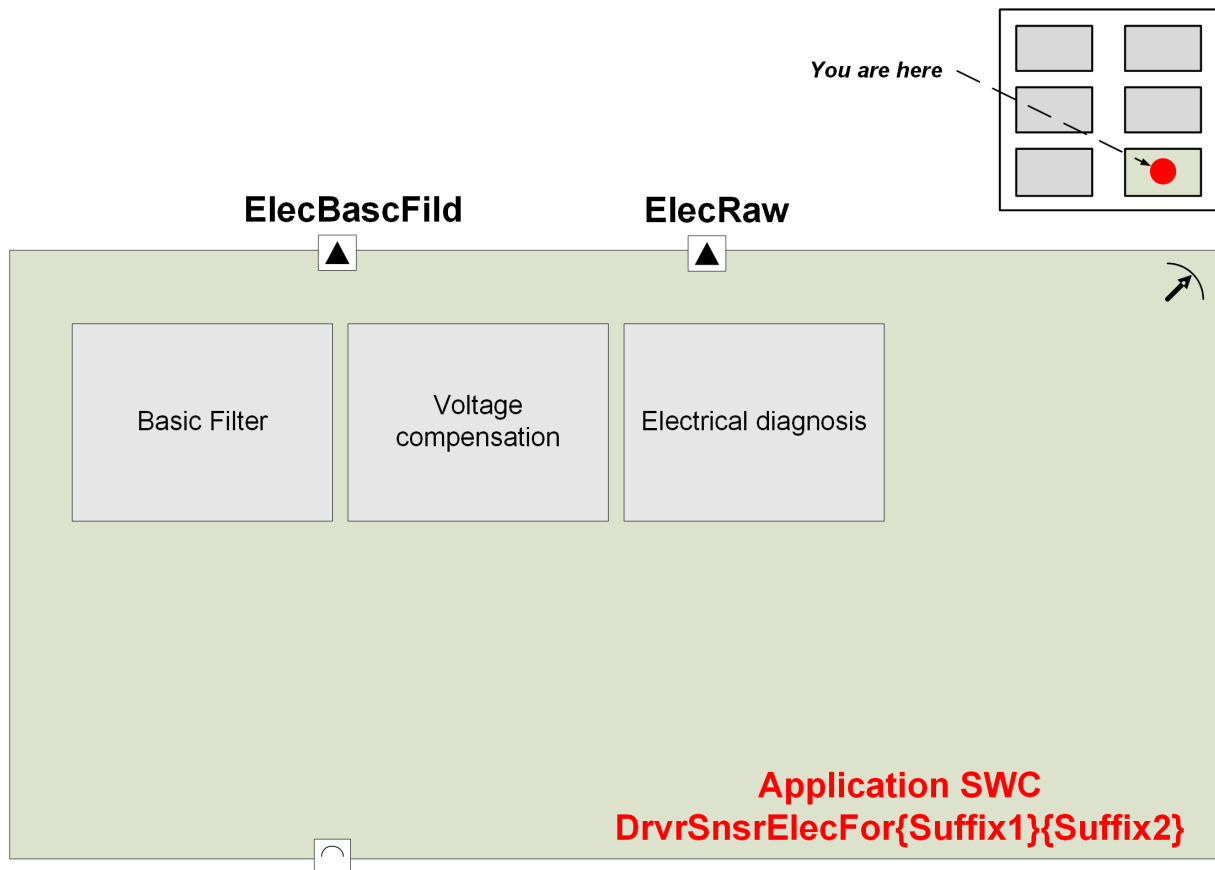


Figure 3.20: Typical functions in layer Drvrsnselec

#### 3.8.6.1 Basic filter

A basic filter is needed to mitigate electric noise. The timing behavior shall not give any significant phase shift to signal.

Example: The definition of a significant phase shift is that it does not have any impact on the physical behaviour of the system. For signals influenced by the combustion the phase shift should not exceed the time given by a 360deg camshaft rotation.

Hint: Possible filter types for this use case could be FIR (finite impulse response) filter or PLL (phase locked loop).

Reason: The DevDrvrsnselec transfers electrical value to physical value. In case the sig-

nal already has a phase shift, the timing within the upper layers cannot be compensated anymore.

### 3.8.6.2 Voltage compensation

Required for sensors with power supply from outside ECU. The separate power supply creates a potential difference in reference voltage which needs to be compensated in SW.

Hint: This functionality can be realized in hardware alternatively.

### 3.8.6.3 Electrical diagnosis

It is needed to diagnose electrical faults on the sensor.

Examples: Short Circuit to Battery (SCB), Short Circuit to Ground (SCG), Open Circuit, Loose Contact.

## 3.9 Known Issues

Sensor abstraction of sensors with typical digital interfaces (e.g. SENT, FAS) or which are connected via bus (e.g. CAN, LIN) is part of this pattern as well. Description of required extensions is in progress.

## 3.10 FAQ

- Why is the estimated value in Example "Actuator without Feedback Loop (Setpoint Alternative)" not used?  
An estimated value does not exist for every sensor. So there is no need for it to be used. In this example, the consolidated value is calculated based on the setpoint.
- Is there a signal quality considered in the pattern?  
The topic "signal qualifier" is not yet considered. At the moment (R19-11) there is no activity known for standardizing such a signal quality.
- How are the names for the layers derived (e.g. DevCoorrVirt)? Can they be changed?  
The AUTOSAR abbreviations are given by strict rules [3]. Even the concatenation of the abbreviations is defined. The names should not be changed due to backward compatibility reasons.

### 3.11 Known Uses

None.

### 3.12 Related Patterns

Pattern	Description
Arbitration Pattern (see Chapter 4)	The sensor/actuator pattern is typically combined with the arbitration pattern to allow several set point requesters, several providers of consolidated values or several providers of estimated values. This is, arbitration is not done within the sensor/actuator pattern but outside the device abstraction.

**Table 3.7: Related Patterns**

### 3.13 Anti-Patterns One Should be Aware of

None.

### 3.14 Further Readings

More information could be found in [12] and [13].

## 4 Arbitration between several requesters or providers

**Classification** Design Pattern

### 4.1 Problem

Arbitration between several different providers or requesters.

### 4.2 Applicability

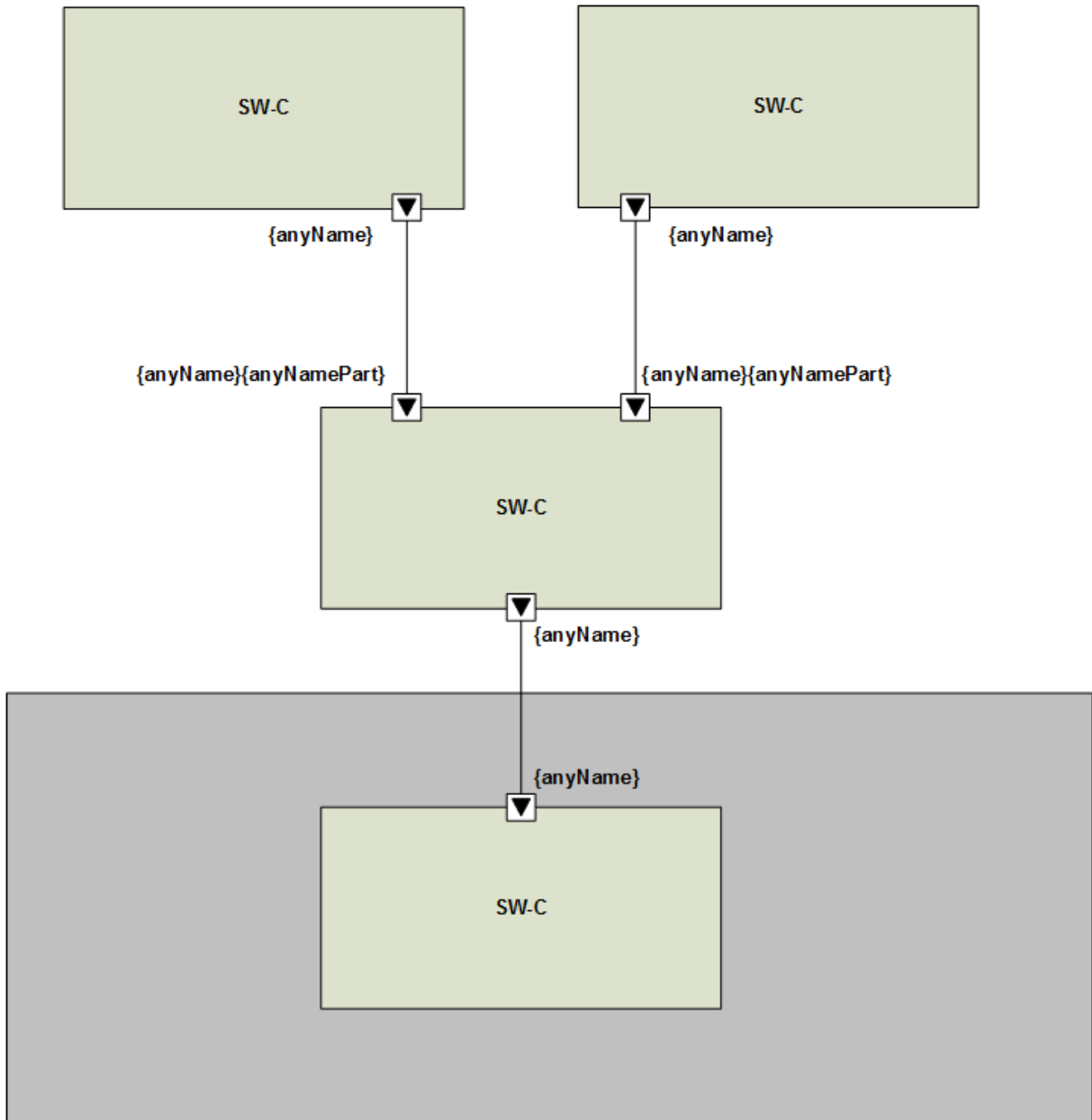
The number of requesters or providers, resp., has to be known at pre-compile time. The number of requesters or providers, resp., has to be known at implementation or generation time of the arbiter component.

This pattern can be applied in the context of Sensor/Actuator Design Pattern, e.g. for modeling several setpoint requesters, several providers of consolidated values or several providers of estimated values.

### 4.3 Solution

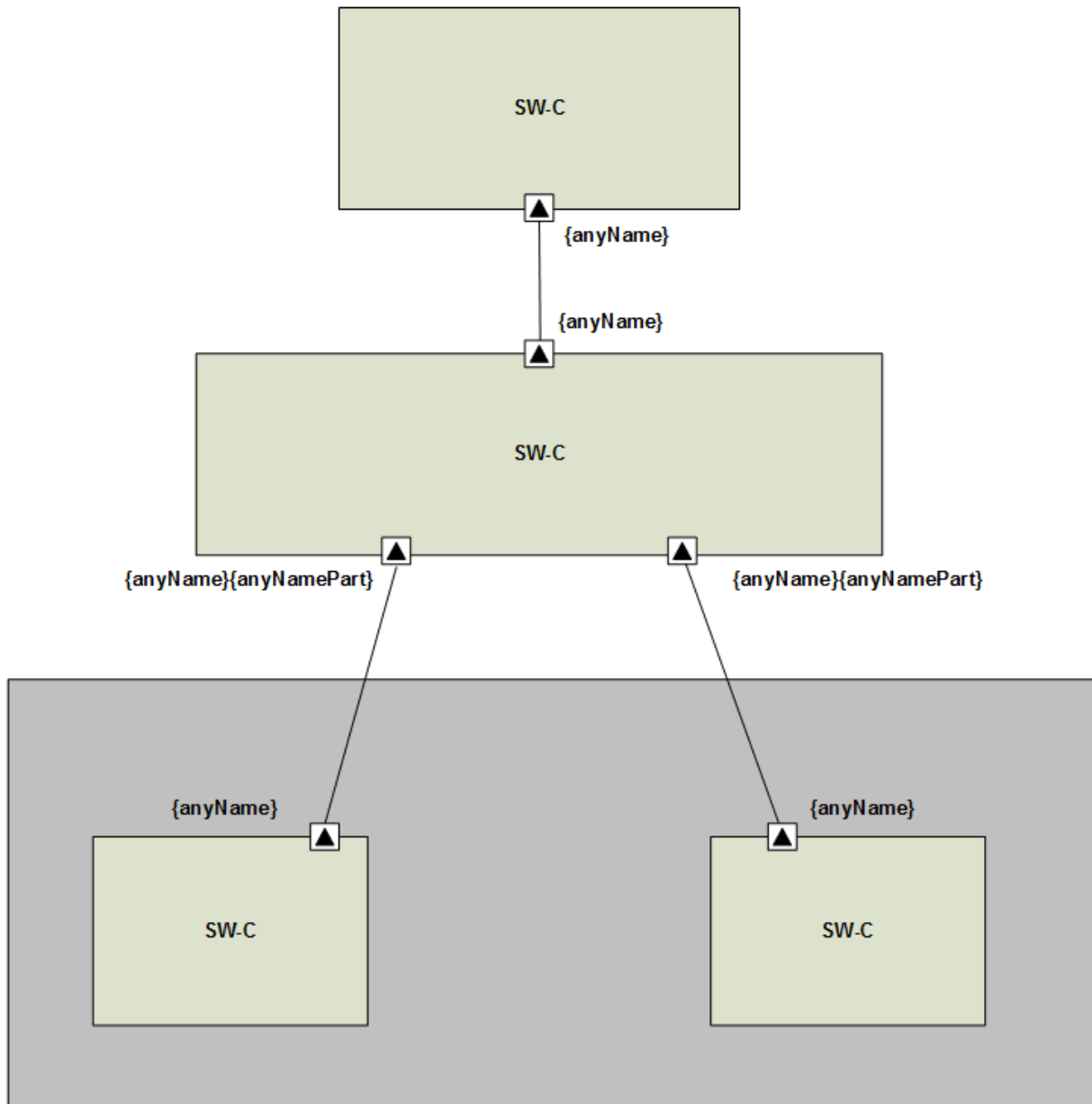
A new component for managing all requests from different requesters or providers, resp., is introduced. In Figure 4.1 the overall pattern for requesters is shown in case sender receiver interfaces are used. In Figure 4.2 the overall pattern for providers is shown in case sender receiver interfaces are used.

When using sender/receiver interfaces the arbitration component, also called "arbiter", needs to have unique names for the different requests or providers. This is realized by different request or provide ports, one per requester or provider, resp. The port interface or at least the application data type is typically the same for all of these requesters or providers, resp., and the resulting request or arbitrated value.



**Figure 4.1: Pattern "Arbitration between Several Requesters"**





**Figure 4.2: Pattern "Arbitration between Several Providers"**

**[TR\_AIDPC\_00006] Arbitration of requesters** [An arbitration component is introduced to support several requesters of the same action but not necessarily of the same value.] ([RS\\_MAIN\\_00060](#))

**[TR\_AIDPC\_00007] Arbitration of providers** [An arbitration component is introduced to support several providers of the same signal.] ([RS\\_MAIN\\_00060](#))

## 4.4 Examples

### 4.4.1 Several Setpoint Requesters

In the context of the sensor/actuator pattern (see Chapter 3) there might be several conflicting setpoint requesters. In this case a new component for managing all requests from different setpoint requesters is introduced, see Figure 4.3.

When using sender/receiver interfaces the arbitration component, also called "arbiter", needs to have unique names for the different requests. This is realized by different request ports, one per requester. The port interface or at least the application data type is typically the same for all of these requesters and the resulting request.

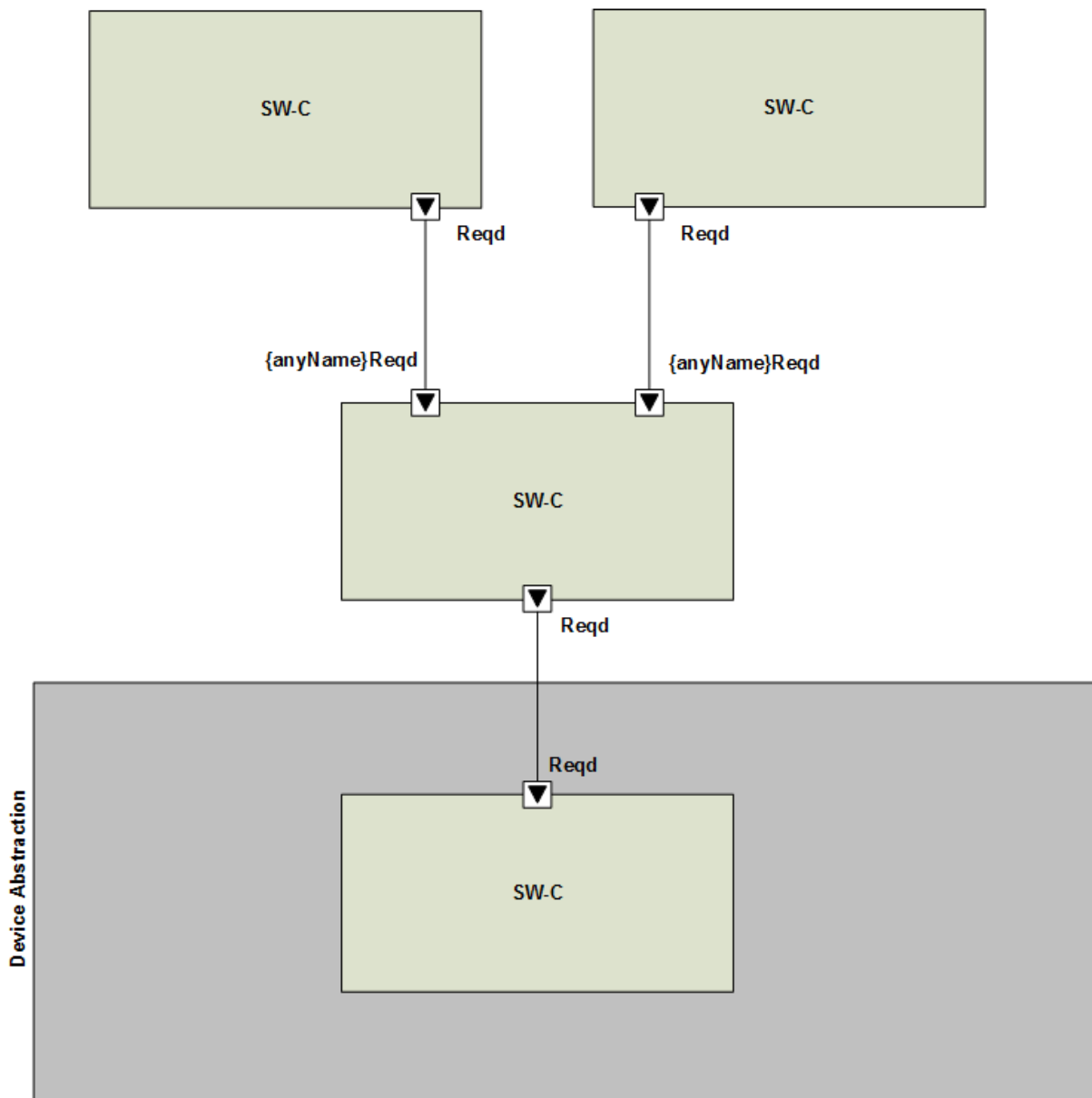


Figure 4.3: Pattern "Arbitration between Several Set-point Requester"

In grammar 4.1 it is described how the provide ports of the requesters as well as the request ports of the arbiter should be named: they all have the suffix "Reqd" for "Required". So terms like "desired", "wished" etc. should not be used to avoid that too many terms with similar meanings are used without being able to distinguish them.

**Listing 4.1: Name Pattern for Ports of Arbiter and Requesters**

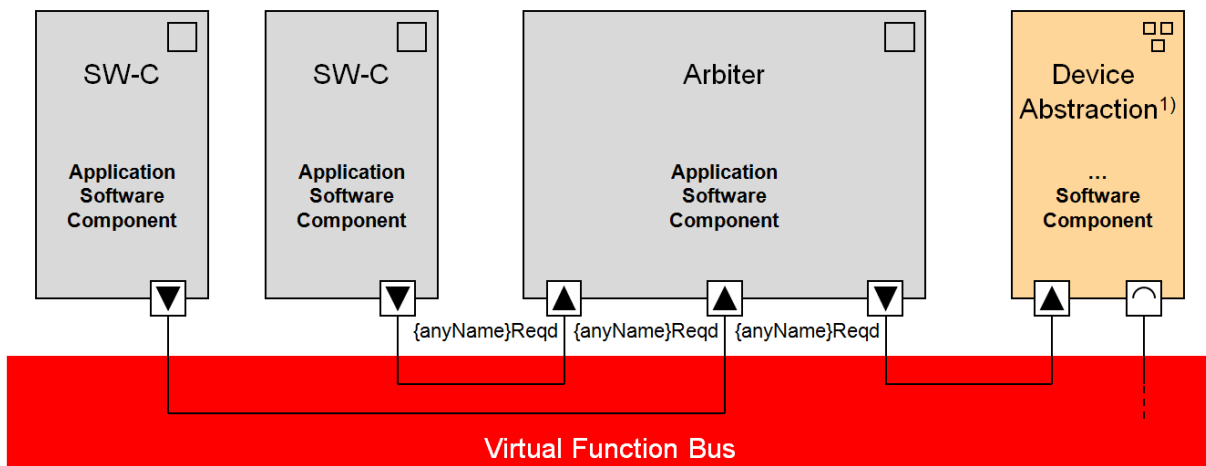
```

grammar PArbSpReqPortNames;

portName
    :    ({{anyName}}){'Reqd'} ;

anyName
    :    ('keyword')* ;
    
```

Figure 4.4 shows the pattern in the context of the RTE. The Device Abstraction is designed as one large composition but this is not requested by the Sensor/Actuator pattern.



<sup>1)</sup> Sensor and Actuator Design Pattern (PSnsrActr)

**Figure 4.4: Arbitration between Several Requesters via RTE**

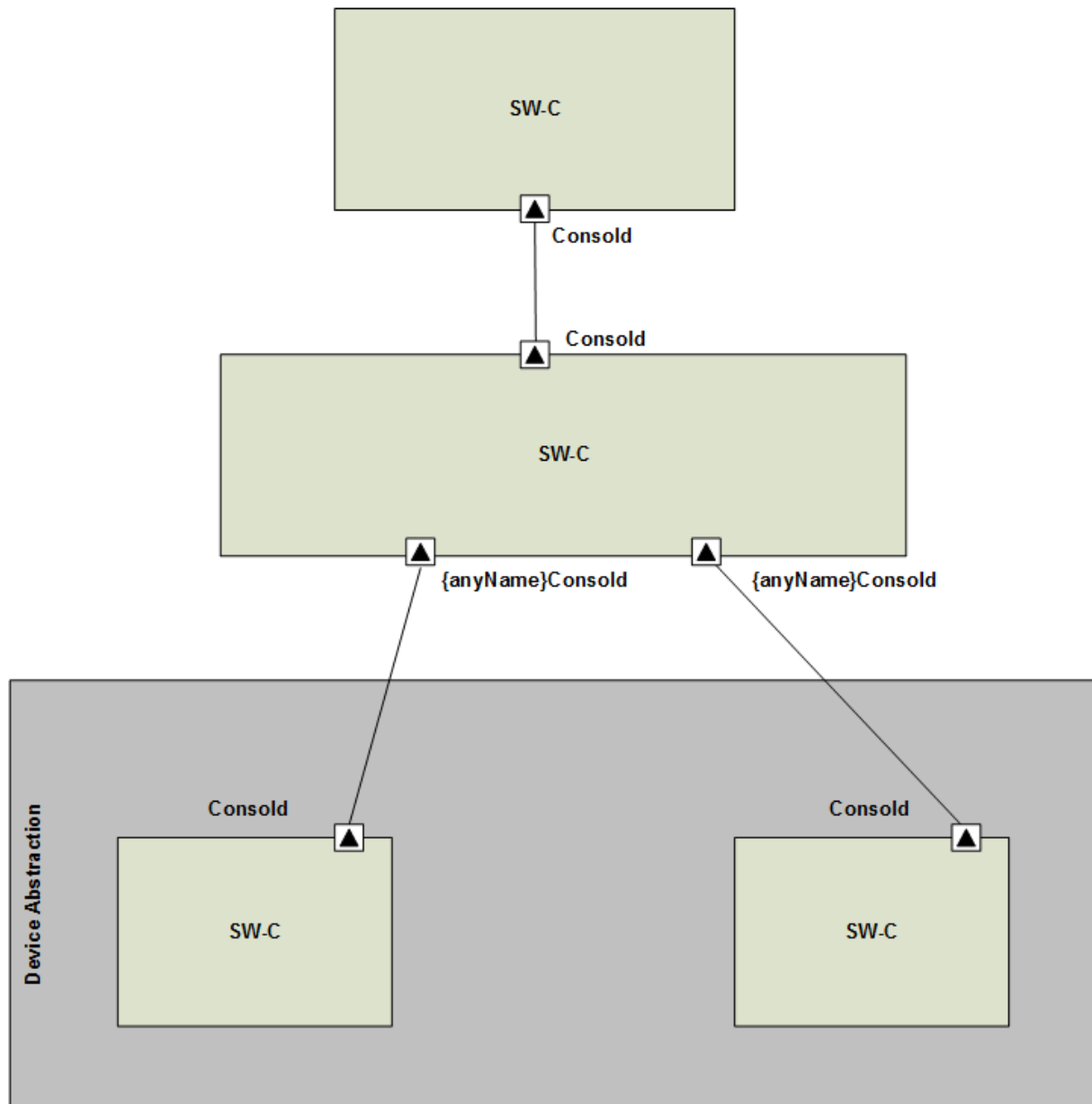
**4.4.2 Several Providers of Consolidated Values**

In the context of the sensor/actuator pattern (3) there might be several sensors providing the same physical information. This is, there are several component all providing a consolidated values for a specific physical signal.

A new component for managing all consolidated values from different providers is introduced, see Figure 4.5.

When using sender/receiver interfaces the arbitration component, also called "arbiter", needs to have unique names for the different providers. This is realized by different

request ports, one per provider. The port interface or at least the application data type is typically the same for all of these providers and the resulting consolidated value.



**Figure 4.5: Pattern "Arbitration between Several Providers of Consolidated Values"**

In grammar 4.2 it is described how the provide ports of the providers as well as the provide port of the arbiter should be named: they all have the suffix "Consold" for "Consolidated". So terms like "modeled" etc. should not be used to avoid that too many terms with similar meanings are used without being able to distinguish them.

**Listing 4.2: Name Pattern for Ports of Arbiter and Providers of Consolidated Values**

```
grammar PArbrConsoldPortNames;

portName
:    ({{anyName}}){'Consold'} ;
```

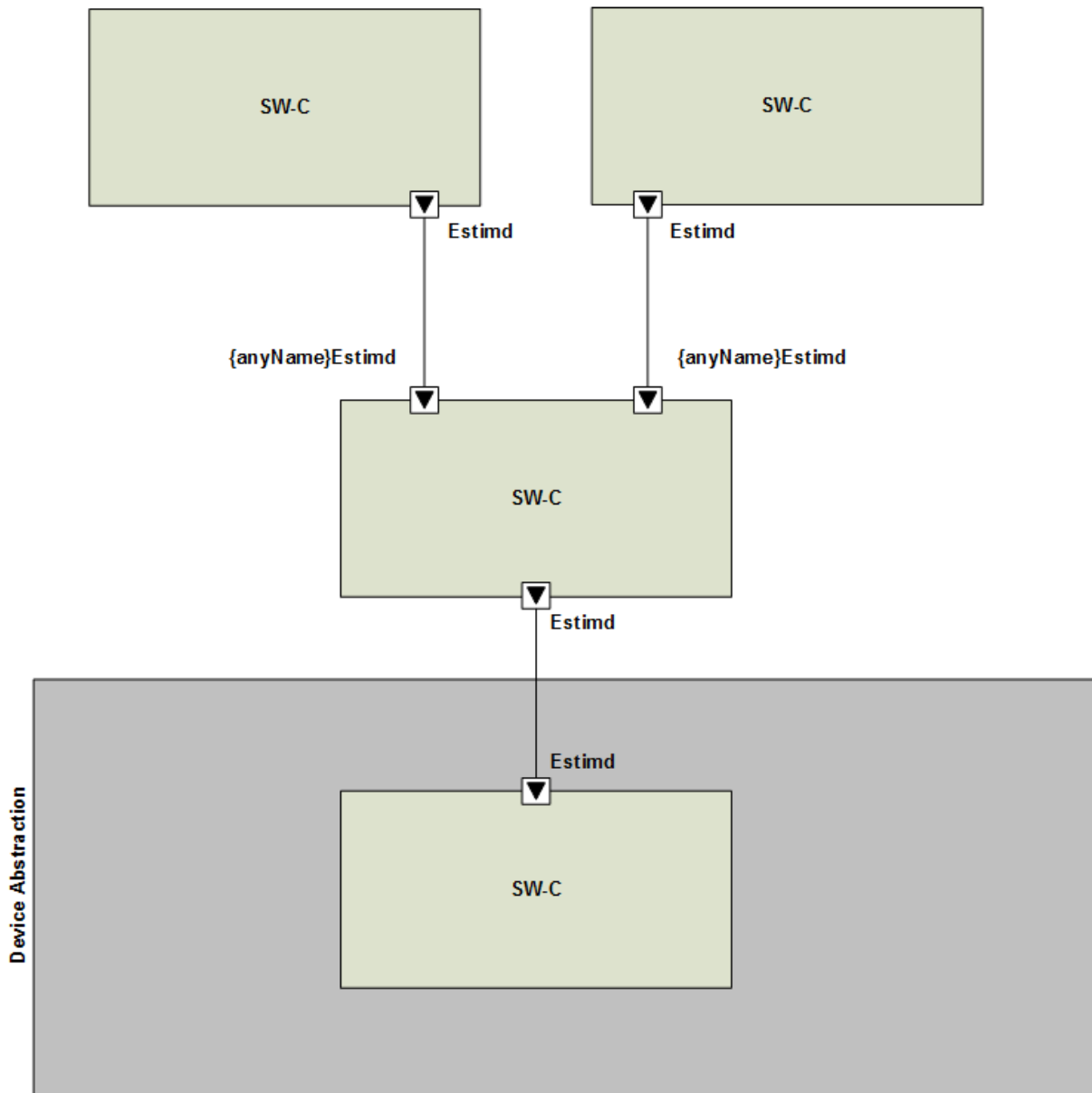
**anyName**

```
: ('keyword')* ;
```

### 4.4.3 Several Providers of Estimated Values

In the context of the sensor/actuator pattern (3) there might be several model for calculating an estimation value. However, in the end only one of the estimated values should be input to the sensor/actuator pattern. Therefore, a new component for managing all estimated values from different providers is introduced, see Figure 4.6.

When using sender/receiver interfaces the arbitration component, also called "arbiter", needs to have unique names for the different providers. This is realized by different request ports, one per provider. The port interface or at least the application data type is typically the same for all of these providers and the resulting estimated value.



**Figure 4.6: Pattern "Arbitration between Several Providers of Estimated Values"**

In grammar 4.3 it is described how the provide ports of the providers as well as the provide port of the arbiter should be named: they all have the suffix "Estimd" for "Estimated". So terms like "modeled" etc. should not be used to avoid that too many terms with similar meanings are used without being able to distinguish them.

**Listing 4.3: Name Pattern for Ports of Arbiter and Providers of Estimated Values**

```

grammar PArbEstimdPortNames;

portName
    :    ({{anyName}}){'Estimd'} ;

anyName
    :    ('keyword')* ;
    
```

## 4.5 Sample Code and Model

None.

## 4.6 Known Uses

This pattern is typically applied in the context of usage of the Sensor/Actuator Design Pattern.

## 4.7 Related Patterns

Pattern	Description
Sensor Actuator Pattern (see Chapter 3)	The sensor/actuator pattern is typically combined with the arbitration pattern to allow several set point requesters, several providers of consolidated values or several providers of estimated values. This is, arbitration is not done within the sensor/actuator pattern but outside the device abstraction.

**Table 4.1: Related Patterns**

## 5 Signal Quality States

**Classification** Design Pattern

### 5.1 Problem

For each (sensor) signal / value the corresponding quality information is also needed to be transferred along with the signal value.

The main intention is to have a common understanding of signal quality and to standardize the states a signal quality can have.

### 5.2 Applicability

This scope of this pattern is the definition of signal quality states (e.g. the content of the signal quality interfaces). The implementation of such a signal quality interface is not in scope of this document as there are several implementations possible.

The signal quality states defined in this document are a minimum set of recommended signal quality states.

### 5.3 Solution

Signal quality	State of related signal value	Meaning
UNDEFINED	Undefined value	No information about quality at all. It means that signal quality is not defined and the signal value is not initialized / calculated yet or is not calculated any more (e.g. desired deactivation of functionality)
VALID	Valid value	Trustworthy value from main signal source
REPLACEMENT	Replacement value with reduced validity	Modelled value or even defined constant value (mostly done by calibration). There is no information about the validity of the signal value, i.e. there is no additional information how "good" the replacement value represents the original value.
FROZEN	Frozen value	Frozen value. A valid value must have been calculated before. There is no information about since how long the signal value is frozen
INVALID	Invalid value	Value is not trustworthy and must not be used

**Table 5.1: Signal Quality States**

Additional information to table 5.1:

- Transitions from UNDEFINED to FROZEN is not allowed, because the previous value was not a valid value



- UNDEFINED level is default value of signal quality interfaces

## A Change History

### A.1 Change History AUTOSAR R4.3.0

#### A.1.1 Added Constraints in R4.3.0

No constraints were added in this release.

#### A.1.2 Changed Constraints in R4.3.0

No constraints were changed in this release.

#### A.1.3 Deleted Constraints in R4.3.0

No constraints were deleted in this release.

#### A.1.4 Added Specification Items in R4.3.0

Number	Heading
<a href="#">[TR_AIDPC_00006]</a>	Arbitration of requesters
<a href="#">[TR_AIDPC_00007]</a>	Arbitration of providers

**Table A.1: Added Specification Items in 4.3.0**

#### A.1.5 Changed Specification Items in R4.3.0

No specification items were changed in this release.

#### A.1.6 Deleted Specification Items in R4.3.0

No specification items were deleted in this release.

### A.2 Change History AUTOSAR R4.2.2

#### A.2.1 Added Constraints in R4.2.2

No constraints were added in this release.

### A.2.2 Changed Constraints in R4.2.2

No constraints were changed in this release.

### A.2.3 Deleted Constraints in R4.2.2

No constraints were deleted in this release.

### A.2.4 Added Specification Items in R4.2.2

Number	Heading
<a href="#">[TR_AIDPC_00001]</a>	Access to Hardware by PSnrAct
<a href="#">[TR_AIDPC_00002]</a>	Collaboration supported by PSnrAct
<a href="#">[TR_AIDPC_00003]</a>	Deployment/Relocation supported by PSnrAct
<a href="#">[TR_AIDPC_00004]</a>	Layers of PSnrAct
<a href="#">[TR_AIDPC_00005]</a>	Naming within PSnrAct

**Table A.2: Added Specification Items in 4.2.2**

### A.2.5 Changed Specification Items in R4.2.2

No specification items were changed in this release.

### A.2.6 Deleted Specification Items in R4.2.2

No specification items were deleted in this release.

## A.3 Change History AUTOSAR R4.2.1

### A.3.1 Added Constraints in R4.2.1

No constraints were added in this initial release.

### A.3.2 Added Specification Items in R4.2.1

No specification items were added in this initial release.

## B Mentioned Class Tables

For the sake of completeness, this chapter contains a set of class tables representing meta-classes mentioned in the context of this document but which are not contained directly in the scope of describing specific meta-model semantics.

<b>Class</b>	<b>ApplicationSwComponentType</b>			
<b>Package</b>	M2::AUTOSARTemplates::SWComponentTemplate::Components			
<b>Note</b>	The ApplicationSwComponentType is used to represent the application software. <b>Tags:</b> atp.recommendedPackage=SwComponentTypes			
<b>Base</b>	ARElement, ARObject, AtomicSwComponentType, AtpBlueprint, AtpBlueprintable, AtpClassifier, AtpType, CollectableElement, Identifiable, MultilanguageReferrable, PackageableElement, Referrable, <a href="#">SwComponentType</a>			
<b>Attribute</b>	<b>Type</b>	<b>Mult.</b>	<b>Kind</b>	<b>Note</b>
-	-	-	-	-

**Table B.1: ApplicationSwComponentType**

<b>Class</b>	<b>CompositionSwComponentType</b>			
<b>Package</b>	M2::AUTOSARTemplates::SWComponentTemplate::Composition			
<b>Note</b>	A CompositionSwComponentType aggregates SwComponentPrototypes (that in turn are typed by SwComponentTypes) as well as SwConnectors for primarily connecting SwComponentPrototypes among each others and towards the surface of the CompositionSwComponentType. By this means hierarchical structures of software-components can be created. <b>Tags:</b> atp.recommendedPackage=SwComponentTypes			
<b>Base</b>	ARElement, ARObject, AtpBlueprint, AtpBlueprintable, AtpClassifier, AtpType, CollectableElement, Identifiable, MultilanguageReferrable, PackageableElement, Referrable, <a href="#">SwComponentType</a>			
<b>Attribute</b>	<b>Type</b>	<b>Mult.</b>	<b>Kind</b>	<b>Note</b>
component	<a href="#">SwComponentPrototype</a>	*	aggr	<p>The instantiated components that are part of this composition. The aggregation of SwComponentPrototype is subject to variability with the purpose to support the conditional existence of a SwComponentPrototype. Please be aware: if the conditional existence of SwComponentPrototypes is resolved post-build the deselected SwComponentPrototypes are still contained in the ECUs build but the instances are inactive in in that they are not scheduled by the RTE.</p> <p>The aggregation is marked as atpSplitable in order to allow the addition of service components to the ECU extract during the ECU integration.</p> <p>The use case for having 0 components owned by the CompositionSwComponentType could be to deliver an empty CompositionSwComponentType to e.g. a supplier for filling the internal structure.</p> <p><b>Stereotypes:</b> atpSplitable; atpVariation <b>Tags:</b> atp.Splitkey=component.shortName, component.variationPoint.shortLabel vh.latestBindingTime=postBuild</p>





Class	CompositionSwComponentType			
connector	SwConnector	*	aggr	<p>SwConnectors have the principal ability to establish a connection among PortPrototypes. They can have many roles in the context of a CompositionSwComponentType. Details are refined by subclasses.</p> <p>The aggregation of SwConnectors is subject to variability with the purpose to support variant data flow.</p> <p>The aggregation is marked as atpSplitable in order to allow the extension of the ECU extract with AssemblySw Connectors between ApplicationSwComponentTypes and ServiceSwComponentTypes during the ECU integration.</p> <p><b>Stereotypes:</b> atpSplitable; atpVariation <b>Tags:</b> atp.Splitkey=connector.shortName, connector.variationPoint.shortLabel vh.latestBindingTime=postBuild</p>
constantValue Mapping	ConstantSpecification MappingSet	*	ref	<p>Reference to the ConstantSpecificationMapping to be applied for initValues of PPortComSpecs and RPortCom Spec.</p> <p><b>Stereotypes:</b> atpSplitable <b>Tags:</b>atp.Splitkey=constantValueMapping</p>
dataType Mapping	DataTypeMappingSet	*	ref	<p>Reference to the DataTypeMapping to be applied for the used ApplicationDataTypes in PortInterfaces.</p> <p>Background: when developing subsystems it may happen that ApplicationDataTypes are used on the surface of CompositionSwComponentTypes. In this case it would be reasonable to be able to also provide the intended mapping to the ImplementationDataTypes. However, this mapping shall be informal and not technically binding for the implementors mainly because the RTE generator is not concerned about the CompositionSwComponent Types.</p> <p>Rationale: if the mapping of ApplicationDataTypes on the delegated and inner PortPrototype matches then the mapping to ImplementationDataTypes is not impacting compatibility.</p> <p><b>Stereotypes:</b> atpSplitable <b>Tags:</b>atp.Splitkey=dataTypeMapping</p>
instantiation RTEEventProps	InstantiationRTEEvent Props	*	aggr	<p>This allows to define instantiation specific properties for RTE Events, in particular for instance specific scheduling.</p> <p><b>Stereotypes:</b> atpSplitable; atpVariation <b>Tags:</b> atp.Splitkey=instantiationRTEEventProps.shortLabel, instantiationRTEEventProps.variationPoint.shortLabel vh.latestBindingTime=codeGenerationTime</p>

**Table B.2: CompositionSwComponentType**

Class	EcuAbstractionSwComponentType
Package	M2::AUTOSARTemplates::SWComponentTemplate::Components





<b>Class</b>	<b>EcuAbstractionSwComponentType</b>			
<b>Note</b>	The ECUAbstraction is a special AtomicSwComponentType that resides between a software-component that wants to access ECU periphery and the Microcontroller Abstraction. The EcuAbstractionSwComponentType introduces the possibility to link from the software representation to its hardware description provided by the ECU Resource Template. <b>Tags:</b> atp.recommendedPackage=SwComponentTypes			
<b>Base</b>	<i>ARElement, ARObject, AtomicSwComponentType, AtpBlueprint, AtpBlueprintable, AtpClassifier, AtpType, CollectableElement, Identifiable, MultilanguageReferrable, PackageableElement, Referrable, SwComponentType</i>			
<b>Attribute</b>	<b>Type</b>	<b>Mult.</b>	<b>Kind</b>	<b>Note</b>
hardware Element	HwDescriptionEntity	*	ref	Reference from the EcuAbstractionComponentType to the description of the used HwElements.

**Table B.3: EcuAbstractionSwComponentType**

<b>Class</b>	<b>FlatInstanceDescriptor</b>			
<b>Package</b>	M2::AUTOSARTemplates::CommonStructure::FlatMap			
<b>Note</b>	Represents exactly one node (e.g. a component instance or data element) of the instance tree of a software system. The purpose of this element is to map the various nested representations of this instance to a flat representation and assign a unique name (shortName) to it. Use cases: <ul style="list-style-type: none"> <li>Specify unique names of measurable data to be used by MCD tools</li> <li>Specify unique names of calibration data to be used by MCD tool</li> <li>Specify a unique name for an instance of a component prototype in the ECU extract of the system description</li> </ul> Note that in addition it is possible to assign alias names via AliasNameAssignment.			
<b>Base</b>	<i>ARObject, Identifiable, MultilanguageReferrable, Referrable</i>			
<b>Attribute</b>	<b>Type</b>	<b>Mult.</b>	<b>Kind</b>	<b>Note</b>
ecuExtract Reference	AtpFeature	0..1	iref	Refers to the instance in the ECU extract. This is valid only, if the FlatMap is used in the context of an ECU extract.  The reference shall be such that it uniquely defines the object instance. For example, if a data prototype is declared as a role within an SwcInternalBehavior, it is not enough to state the SwcInternalBehavior as context and the aggregated data prototype as target. In addition, the reference shall also include the complete path identifying instance of the component prototype and the Atomic SoftwareComponentType, which is referred by the particular SwcInternalBehavior. <b>Tags:</b> xml.sequenceOffset=40 <b>InstanceRef implemented by:</b> AnyInstanceRef
role	Identifier	0..1	attr	The role denotes the particular role of the downstream memory location described by this FlatInstanceDescriptor.  It applies to use case where one upstream object results in multiple downstream objects, e.g. ModeDeclaration GroupPrototypes which are measurable. In this case the RTE will provide locations for current mode, previous mode and next mode.





Class	FlatInstanceDescriptor			
rtePluginProps	RtePluginProps	0..1	aggr	The properties of a communication graph with respect to the utilization of RTE Implementation Plug-in. <b>Stereotypes:</b> atpSplittable <b>Tags:</b> atp.Splitkey=rtePluginProps
swDataDef Props	SwDataDefProps	0..1	aggr	The properties of this FlatInstanceDescriptor.
upstream Reference	AtpFeature	0..1	iref	Refers to the instance in the context of an "upstream" descriptions, wich could be the system or system extract description, the basic software module description or (if a flat map is used in preliminary context) a description of an atomic component or composition. This reference is optional in case the flat map is used in ECU context. The reference shall be such that it uniquely defines the object instance in the given context. For example, if a data prototype is declared as a role within an SwcInternal Behavior, it is not enough to state the SwcInternal Behavior as context and the aggregated data prototype as target. In addition, the reference shall also include the complete path identifying the instance of the component prototype that contains the particular instance of Swc InternalBehavior. <b>Tags:</b> xml.sequenceOffset=20 <b>InstanceRef implemented by:</b> AnyInstanceRef

**Table B.4: FlatInstanceDescriptor**

Class	HwCategory			
Package	M2::AUTOSARTemplates::EcuResourceTemplate::HwElementCategory			
Note	This metaclass represents the ability to declare hardware categories and its particular attributes. <b>Tags:</b> atp.recommendedPackage=HwCategorys			
Base	<i>ARElement, ARObject, AtpDefinition, CollectableElement, Identifiable, MultilanguageReferrable, PackageableElement, Referrable</i>			
Attribute	Type	Mult.	Kind	Note
hwAttributeDef	HwAttributeDef	*	aggr	This aggregation describes particular hardware attribute definition.

**Table B.5: HwCategory**

Class	HwDescriptionEntity (abstract)			
Package	M2::AUTOSARTemplates::EcuResourceTemplate			
Note	This meta-class represents the ability to describe a hardware entity.			
Base	<i>ARObject, Referrable</i>			
Subclasses	<a href="#">HwElement</a> , <a href="#">HwPin</a> , <a href="#">HwPinGroup</a> , <a href="#">HwType</a>			
Attribute	Type	Mult.	Kind	Note
hwAttribute Value	HwAttributeValue	*	aggr	This aggregation represents a particular hardware attribute value. <b>Stereotypes:</b> atpVariation <b>Tags:</b> vh.latestBindingTime=systemDesignTime xml.sequenceOffset=50





<b>Class</b>		<b>HwDescriptionEntity</b> (abstract)		
hwCategory	<a href="#">HwCategory</a>	*	ref	One of the associations representing one particular category of the hardware entity. <b>Tags:</b> xml.sequenceOffset=30
hwType	<a href="#">HwType</a>	0..1	ref	This association is used to assign an optional HwType which contains the common attribute values for all occurrences of this HwDescriptionEntity. Note that HwTypes can not be redefined and therefore shall not have a hwType reference.

**Table B.6: HwDescriptionEntity**

<b>Class</b>		<b>HwElement</b>		
<b>Package</b>		M2::AUTOSARTemplates::EcuResourceTemplate		
<b>Note</b>		This represents the ability to describe Hardware Elements on an instance level. The particular types of hardware are distinguished by the category. This category determines the applicable attributes. The possible categories and attributes are defined in HwCategory. <b>Tags:</b> atp.recommendedPackage=HwElements		
<b>Base</b>		ARElement, ARObject, CollectableElement, <a href="#">HwDescriptionEntity</a> , Identifiable, MultilanguageReferrable, PackageableElement, Referrable		
<b>Attribute</b>	<b>Type</b>	<b>Mult.</b>	<b>Kind</b>	<b>Note</b>
hwElement Connection	HwElementConnector	*	aggr	This represents one particular connection between two hardware elements. <b>Stereotypes:</b> atpVariation <b>Tags:</b> vh.latestBindingTime=systemDesignTime xml.sequenceOffset=110
hwPinGroup	HwPinGroup	*	aggr	This aggregation is used to describe the connection facilities of a hardware element. Note that hardware element has no pins but only pingroups. <b>Stereotypes:</b> atpVariation <b>Tags:</b> vh.latestBindingTime=systemDesignTime xml.sequenceOffset=90
nestedElement	<a href="#">HwElement</a>	*	ref	This association is used to establish hierarchies of hw elements. Note that one particular HwElement can be target of this association only once. I.e. multiple instantiation of the same HwElement is not supported (at any hierarchy level). <b>Stereotypes:</b> atpVariation <b>Tags:</b> vh.latestBindingTime=systemDesignTime xml.sequenceOffset=70

**Table B.7: HwElement**

<b>Class</b>		<b>HwType</b>		
<b>Package</b>		M2::AUTOSARTemplates::EcuResourceTemplate::HwElementCategory		
<b>Note</b>		This represents the ability to describe Hardware types on an abstract level. The particular types of hardware are distinguished by the category. This category determines the applicable attributes. The possible categories and attributes are defined in HwCategory. <b>Tags:</b> atp.recommendedPackage=HwTypes		







<b>Class</b>	<b>HwType</b>			
<b>Base</b>	<i>ARElement, ARObject, CollectableElement, HwDescriptionEntity, Identifiable, MultilanguageReferrable, PackageableElement, Referrable</i>			
<b>Attribute</b>	<b>Type</b>	<b>Mult.</b>	<b>Kind</b>	<b>Note</b>
–	–	–	–	–

**Table B.8: HwType**

<b>Primitive</b>	<b>Identifier</b>			
<b>Package</b>	M2::AUTOSARTemplates::GenericStructure::GeneralTemplateClasses::PrimitiveTypes			
<b>Note</b>	<p>An Identifier is a string with a number of constraints on its appearance, satisfying the requirements typical programming languages define for their Identifiers.</p> <p>This datatype represents a string, that can be used as a c-Identifier.</p> <p>It shall start with a letter, may consist of letters, digits and underscores.</p> <p><b>Tags:</b>                      xml.xsd.customType=IDENTIFIER                      xml.xsd.maxLength=128                      xml.xsd.pattern=[a-zA-Z][a-zA-Z0-9_]*                      xml.xsd.type=string</p>			
<b>Attribute</b>	<b>Type</b>	<b>Mult.</b>	<b>Kind</b>	<b>Note</b>
blueprintValue	String	0..1	attr	<p>This represents a description that documents how the value shall be defined when deriving objects from the blueprint.</p> <p><b>Tags:</b>                      atp.Status=draft                      xml.attribute=true</p>
namePattern	String	0..1	attr	<p>This attribute represents a pattern which shall be used to define the value of the identifier if the identifier in question is part of a blueprint.</p> <p>For more details refer to TPS_StandardizationTemplate.</p> <p><b>Tags:</b>xml.attribute=true</p>

**Table B.9: Identifier**

<b>Class</b>	<b>Keyword</b>			
<b>Package</b>	M2::AUTOSARTemplates::CommonStructure::StandardizationTemplate::Keyword			
<b>Note</b>	<p>This meta-class represents the ability to predefine keywords which may subsequently be used to construct names following a given naming convention, e.g. the AUTOSAR naming conventions.</p> <p>Note that such names is not only shortName. It could be symbol, or even longName. Application of keywords is not limited to particular names.</p>			
<b>Base</b>	<i>ARObject, Identifiable, MultilanguageReferrable, Referrable</i>			
<b>Attribute</b>	<b>Type</b>	<b>Mult.</b>	<b>Kind</b>	<b>Note</b>
abbrName	NameToken	1	attr	<p>This attribute specifies an abbreviated name of a keyword. This abbreviation may e.g. be used for constructing valid shortNames according to the AUTOSAR naming conventions.</p> <p>Unlike shortName, it may contain any name token. E.g. it may consist of digits only.</p>





Class	Keyword			
classification	NameToken	*	attr	This attribute allows to attach classification to the Keyword such as MEAN, ACTION, CONDITION, INDEX, PREPOSITION

**Table B.10: Keyword**

Class	<b>PortPrototype</b> (abstract)			
<b>Package</b>	M2::AUTOSARTemplates::SWComponentTemplate::Components			
<b>Note</b>	Base class for the ports of an AUTOSAR software component. The aggregation of PortPrototypes is subject to variability with the purpose to support the conditional existence of ports.			
<b>Base</b>	<i>ARObject, AtpBlueprintable, AtpFeature, AtpPrototype, Identifiable, MultilanguageReferrable, Referrable</i>			
<b>Subclasses</b>	<i>AbstractProvidedPortPrototype, AbstractRequiredPortPrototype</i>			
Attribute	Type	Mult.	Kind	Note
clientServer Annotation	ClientServerAnnotation	*	aggr	Annotation of this PortPrototype with respect to client/server communication.
delegatedPort Annotation	DelegatedPort Annotation	0..1	aggr	Annotations on this delegated port.
ioHwAbstraction Server Annotation	IoHwAbstractionServer Annotation	*	aggr	Annotations on this IO Hardware Abstraction port.
modePort Annotation	ModePortAnnotation	*	aggr	Annotations on this mode port.
nvDataPort Annotation	NvDataPortAnnotation	*	aggr	Annotations on this non volatile data port.
parameterPort Annotation	ParameterPort Annotation	*	aggr	Annotations on this parameter port.
senderReceiver Annotation	SenderReceiver Annotation	*	aggr	Collection of annotations of this ports sender/receiver communication.
triggerPort Annotation	TriggerPortAnnotation	*	aggr	Annotations on this trigger port.

**Table B.11: PortPrototype**

Class	<b>PortPrototypeBlueprint</b>			
<b>Package</b>	M2::AUTOSARTemplates::CommonStructure::StandardizationTemplate::BlueprintDedicated::Port PrototypeBlueprint			
<b>Note</b>	This meta-class represents the ability to express a blueprint of a PortPrototype by referring to a particular PortInterface. This blueprint can then be used as a guidance to create particular PortPrototypes which are defined according to this blueprint. By this it is possible to standardize application interfaces without the need to also standardize software-components with PortPrototypes typed by the standardized Port Interfaces. <b>Tags:</b> atp.recommendedPackage=PortPrototypeBlueprints			
<b>Base</b>	<i>ARElement, ARObject, AtpBlueprint, AtpClassifier, AtpFeature, AtpStructureElement, Collectable Element, Identifiable, MultilanguageReferrable, PackageableElement, Referrable</i>			
Attribute	Type	Mult.	Kind	Note
initValue	PortPrototypeBlueprint InitValue	*	aggr	This specifies the init values for the dataElements in the particular PortPrototypeBlueprint.
interface	PortInterface	1	ref	This is the interface for which the blueprint is defined. It may be a blueprint itself or a standardized PortInterface





Class	PortPrototypeBlueprint			
providedComSpec	PPortComSpec	*	aggr	Provided communication attributes per interface element (data element or operation).
requiredComSpec	RPortComSpec	*	aggr	Required communication attributes, one for each interface element.

**Table B.12: PortPrototypeBlueprint**

Class	SensorActuatorSwComponentType			
Package	M2::AUTOSARTemplates::SWComponentTemplate::Components			
Note	The SensorActuatorSwComponentType introduces the possibility to link from the software representation of a sensor/actuator to its hardware description provided by the ECU Resource Template. <b>Tags:</b> atp.recommendedPackage=SwComponentTypes			
Base	ARElement, ARObject, AtomicSwComponentType, AtpBlueprint, AtpBlueprintable, AtpClassifier, AtpType, CollectableElement, Identifiable, MultilanguageReferrable, PackageableElement, Referrable, SwComponentType			
Attribute	Type	Mult.	Kind	Note
sensorActuator	HwDescriptionEntity	0..1	ref	Reference from the Sensor Actuator Software Component Type to the description of the actual hardware.

**Table B.13: SensorActuatorSwComponentType**

Class	SwComponentPrototype			
Package	M2::AUTOSARTemplates::SWComponentTemplate::Composition			
Note	Role of a software component within a composition.			
Base	ARObject, AtpFeature, AtpPrototype, Identifiable, MultilanguageReferrable, Referrable			
Attribute	Type	Mult.	Kind	Note
type	SwComponentType	0..1	tref	Type of the instance. <b>Stereotypes:</b> isOfType

**Table B.14: SwComponentPrototype**

Class	SwComponentType (abstract)			
Package	M2::AUTOSARTemplates::SWComponentTemplate::Components			
Note	Base class for AUTOSAR software components.			
Base	ARElement, ARObject, AtpBlueprint, AtpBlueprintable, AtpClassifier, AtpType, CollectableElement, Identifiable, MultilanguageReferrable, PackageableElement, Referrable			
Subclasses	AtomicSwComponentType, CompositionSwComponentType, ParameterSwComponentType			
Attribute	Type	Mult.	Kind	Note
consistencyNeeds	ConsistencyNeeds	*	aggr	This represents the collection of ConsistencyNeeds owned by the enclosing SwComponentType. <b>Stereotypes:</b> atpSplitable; atpVariation <b>Tags:</b> atp.Splitkey=consistencyNeeds.shortName, consistencyNeeds.variationPoint.shortLabel vh.latestBindingTime=preCompileTime





<b>Class</b>	<b>SwComponentType</b> (abstract)			
port	<a href="#">PortPrototype</a>	*	aggr	<p>The PortPrototypes through which this SwComponentType can communicate.</p> <p>The aggregation of PortPrototype is subject to variability with the purpose to support the conditional existence of PortPrototypes.</p> <p><b>Stereotypes:</b> atpSplitable; atpVariation  <b>Tags:</b>  atp.Splitkey=port.shortName, port.variationPoint.shortLabel  vh.latestBindingTime=preCompileTime</p>
portGroup	PortGroup	*	aggr	<p>A port group being part of this component.</p> <p><b>Stereotypes:</b> atpVariation  <b>Tags:</b>vh.latestBindingTime=preCompileTime</p>
swComponentDocumentation	SwComponentDocumentation	0..1	aggr	<p>This adds a documentation to the SwComponentType.</p> <p><b>Stereotypes:</b> atpSplitable; atpVariation  <b>Tags:</b>  atp.Splitkey=swComponentDocumentation, swComponentDocumentation.variationPoint.shortLabel  vh.latestBindingTime=preCompileTime  xml.sequenceOffset=-10</p>
unitGroup	UnitGroup	*	ref	<p>This allows for the specification of which UnitGroups are relevant in the context of referencing SwComponentType.</p>

**Table B.15: SwComponentType**